

RTEMS - 5.0.0-m2004-2 Release Notes

29 April 2020

RTEMS 5 Series Release Notes

These notes cover the dot releases: 5.1 5.0 5.1 (open)

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Statistics
          Total 775
          Fixed 705
        Invalid 13
  Works for me 8
      Duplicate 12
      Won't fix 33
Distribution
        defect
                                                                  465 / 468
   enhancement 201 / 202
          task 80 / 80
          infra 16 / 16
        project 9/9
Summary
  #1247 RTEMS does not implement locks needed by multithreaded newlib
  #1394 scandir() fails due to MAXNAMELEN is incorrect
  #1662 termios.c: semaphore not deleted, consequently resulting in failure of rtems termios open
  #1747 Heap extend allows discontinuous memory regions.
  #1971 Memory leak in tmpfile()
  #2132 <rtems/score/basedefs.h> superfluously includes <limits.h>
  #2133 <rtems/score/basedefs.h> superfluously includes <string.h>
  #2135 times() and _times() are subject to integer overflows
  #2173 Potential integer overflow problem in EDF scheduler
  #2176 fishy behavior in termios tx task mode
  #2198 Automate doxygen build
  #2207 RTEMS tar does not overwrite.
  #2213 Decreased performance for whetstone benchmark using GCC >=4.5
  #2261 Add coverage report generation support to rtems-tools
  #2266 Move bsp_pretasking_hook() into files named bsppretaskinghook.c
  #2284 h8300 gets error linking dl0* tests
  #2289 rtems_ada_self is broken on SMP
  #2305 sp07 needs to be split into an user extensions and a notepad test
  #2306 powerpc/mvme5500/vectors/exceptionhandler.c uses task variables
  #2308 Change uniprocessor INIT task mode to preempt.
  #2325 Broken console driver infrastructure for SPARC
  #2344 Second argument of ualarm() is ignored
  #2350 One watchdog ticks header per scheduler instance
  #2354 Replace red-black tree implementation, change API
  #2355 SPARC: Several shared drivers are not SMP ready
  #2363 SPARC: Silent FP context corruption possible
  #2366 Create a Public API for the Atomic Operations
  #2367 Documentation of User Extensions needs more information
  #2377 rtems waf: Tools without a version are not supported
  #2385 Warning from commit "bsps/arm: Do not use ARM ARCH 7A
  #2407 Enable function and data sections
  #2408 Linker set based initialization
  #2412 Improved priority inheritance implementation
  #2420 RSB %source file fails
  #2423 rtems_iterate_over_all_threads lacks user callback private pointer pass through
  #2428 Add 4.12 Tool Target Configurations to RSB
  #2441 lpc1768 variants fail to build with error in gpio.c
  #2442 Remove avrtest BSP
  #2443 Remove AVR Architectural Port
  #2444 Remove m68k/mvme136 BSP
  #2445 Remove m68k/sim68000 BSP
  #2446 Remove M32R Architectural Port
  #2447 Remove m32r/m32rsim
  #2448 Remove mips/mongoose BSP
  #2449 Remove arm/gba BSP
  #2450 Remove arm/nds
  #2451 Remove arm/gp32 BSP
  #2452 Remove H8300 Architectual Port
  #2453 Remove h8300/h8sim BSP
  #2454 Warning in threadgops.c
  #2455 Warning in spsimplesched02
  #2457 Remove powerpc/ep1a BSP
  #2458 Remove powerpc/score603e BSP
  #2459 Add rtems_chain_get_first_unprotected() to chain API
  #2464 RSB: Tool patches use the RTEMS version
  #2468 Add Thread Local Storage (TLS) support on x86
  #2477 Remove <rtems/debug.h>
  #2487 Should https://devel.rtems.org/wiki/TBR/Delete/SpecBuilder be Deleted?
  #2488 Vagrant Scripts
  #2490 RSB: Use SHA512 instead of MD5
  #2493 Remove notepads
  #2494 Remove task variables
  #2503 mvme5500 BSP: Exception Handler uses deprecated Notepads.
  #2509 Should "https://devel.rtems.org/wiki/TBR/Delete/BSP_Template" be replaced?
  #2513 Remove m68k/idp BSP
  #2514 Make POSIX API mandatory (except signals and the sporadic server)
  #2515 i386 score/libcpu API Layering Violation
  #2527 Move pc386/tools/bin2boot to rtems-tools
  #2529 BSP for the Atmel SAM V71/V70/E70/S70 chip platform
  #2536 RSB allows use of insecure hash algorithms like MD5 and SHA1
```

#2537 Use Newlib exec*() variants and remove RTEMS versions

- **#2542** Review cxx_iostream size change per function-section changes **#2543** Obsolete gen68302 BSP
- #2544 Osolete m68k/ods68302
- #2545 Obsolete mbx8xx BSP
- #2546 Obsolete idp BSP
- #2553 [mvme3100] boot_card() broken by 37030e38
- #2554 New watchdog handler implementation
- #2555 Eliminate the Giant lock
- #2556 Implement the O(m) Independence-Preserving Protocol (OMIP)
- #2557 Add word splitting to print output
- #2559 Delete the EXTERN pattern #2560 smdk2410 is broken due to gp32 removal
- #2562 RSB Docs Quick Start version number
- #2576 arm/lpc176x: linker script update (add KEEP() sections)
- **#2606** alarm() uses seconds watchdog and thus is affected by clock changes
- **#2608** POSIX Condition Variables Clock Attribute Support
- **#2617** rtems_heap_allocate_aligned_with_boundary() body and prototype inconsistent
- #2624 Fix the year 2038 problem
- #2625 Use one lookup tree per-thread for the POSIX keys
- #2626 Unify thread cancel/join and delete
- #2627 Fix CPU time used for threads on SMP
- **#2628** Avoid home-grown condition variable implementation in the Classic Regions
- **#2631** Use an ISR lock to protect the state of Classic Rate Monotonic objects
- #2632 rtems-tester failure
- **#2633** waf build failed for rtems-libbsd
- **#2634** New warning in pc386 VESA driver **#2638** pc386: Id -r issue with per function sections
- **#2641** configure: enable-rtemsbsp doesn't warn if bsp does not exist
- **#2644** sis does not run on gdb 7.11 but does on gdb 7.9
- **#2649** RSB remove 4.11, 4.10 and 4.9 from the master branch.
- #2663 pc386 BSP has complex dependencies
- #2664 spclock_err02
- #2669 Update OpenRISC toolchain in 4.12
- **#2672** After latest patches with Objects_Get_by_name rtems-master not compiling without --enable-posix
- #2674 CORE spinlock implementation is next to be useless
- #2676 Obsolete clock_get() directive
- **#2680** Add pthread_setconcurrency() and pthread_getconcurrency()
- #2683 Configuration table's smp_enabled conditional on RTEMS_SMP
- #2684 rtems/c/src/lib/libbsp/sparc/leon3/clock/ckinit.c:122: duplicate if
- #2685 c/src/lib/libbsp/arm/atsam/network/if_atsam.c:409: possible bad if statement
- **#2689** POSIX key destructors must be called during thread restart
- #2692 User extensions execution order must be clarified
- **#2693** Update doc to reflect obsoleting rtems_clock_get() **#2694** linking issue for htonl, etc when using -std=c99
- #2694 Inking issue for fitoni, etc whe
- #2695 Add libatomic for RTEMS
- **#2696** Unpredictable errno value returned by sem_wait() in case of semaphore deletion
- #2698 GCC 6.1 is broken for microblaze
- #2700 cpukit/libfs/src/nfsclient/src/rpcio.c:524]: (style) Suspicious condition
- #2701 Rename asm file with .S(upper case) ext. name
- **#2702** Remove descriptor objects for POSIX message queues
- #2706 Buffer allocation of capture engine is broken on SMP configurations
- #2707 Unsafe use of current processor index in capture engine
- #2714 A pthread_detach() does not lead to a resource reclamation
- #2718 Blocking _CORE_message_queue_Submit() may lead to unpredictable results
- #2722 SEM_VALUE_MAX is unusually small on RTEMS
- #2723 CPUINFO command to report per-processor information
- #2725 Classic binary semaphores without a locking protocol can be released by everyone
- #2726 grascs.c: Questionable use of binary semaphore
- #2727 FAT file systems use wrong semaphore for mutual exclusion
- #2728 Pipes use wrong semaphore for mutual exclusion
- **#2729** TFTP client uses wrong semaphore for mutual exclusion
- #2732 Add clock_nanosleep()
- #2734 pthread_setschedprio() is missing
- #2735 pthread_setschedparam() sets the priority not according to POSIX
- **#2736** pthread getschedparam() returns wrong priority values
- #2737 Add CLOCK_DRIVER_USE_ONLY_BOOT PROCESSOR
- #2740 Suboptimal type for Timestamp_Control
- #2741 New warning from printf plugin changes
- #2742 New warning in SHM driver
- #2745 Use clock from pthread condattr in pthread cond timedwait
- #2748 Move RTEMS-specific socket wake-up to RTEMS-specific <rtems/rtems_bsdnet.h>
- #2749 rtems_task_set_scheduler() has insufficient parameters
- #2750 Compile Error When Multiprocessing Enabled
- #2751 Thread dispatch via interrupt is broken at least on ARM and PowerPC
- #2752 Relax execution enviroment for thread begin extensions
- #2754 no .strtab section
- #2765 Application level deadlocks may lead to SMP lock level deadlocks
- **#2768** untar does not keep permissions correctly.
- **#2769** rtems-syms does not clean up temp files.
- **#2770** Missing documentation for RTEMS_LINKER_ROSET_CONTENT and RTEMS_LINKER_RWSET_CONTENT
- **#2771** Empty C++ file with just <rtems.h> does not compile with HEAD.
- **#2775** ARM CP15 arm_cp15_set_translation_table_entries fails if TTB in read-only memory
- #2776 SPI Framework
- #2777 Remove librtems++
- **#2784** Add function to get the current priority of a task by scheduler instance
- **#2788** RTEMS I2C API only defines Standard-mode (Sm) speed as a default.
- #2790 Linker sets broken with GCC 7
- **#2795** Overrun Handling for general real-time models
- **#2797** Add ability to add/remove processors to/from a scheduler instance
- #2798 Fix POSIX timer interval
- **#2800** qoriq variants failing to build
- #2802 Test "libdl (RTL) 5" fails on SPARC targets

#2803 Get rid of CPU_BIG_ENDIAN and CPU_LITTLE_ENDIAN

#2805 Use SPRG0 on PowerPC for current per-CPU control (SMP only)

- **#2806** Undocumented confdefs.h Configure Options
- **#2807** rtems-docs repository is not known to trac **#2808** Conditionally provide rtems_interrupt_frame
- #2809 Reduce interrupt latency on SMP configurations during thread dispatch
- #2810 Remove sparc/sis BSP variant
- #2811 More robust thread dispatching on SMP and ARM Cortex-M
- **#2816** Many ARM BSPs have Static Assert
- #2817 All Blackfin BSPs do not Compile on Master
- #2818 NIOS2 Does Not Compile on Master
- #2819 powerpc-ss555 does not compile on master
- #2820 All SPARC64 BSPs do not Build on master #2821 No BSPs Build on Master
- #2822 m32csim does not build on master
- #2823 Nearly all m68k BSPs do not Build on Master
- #2824 arm/lpc23xx_tli800 no longer links tar01
- **#2825** Improve the fatal error handling chapter of the user manual
- **#2826** arm_cp15_get_translation_table_base_control_register warning.
- #2829 xz git URL in README is broken
- #2835 Ada support is broken on SMP configurations
- #2836 Add posix_devctl()
- #2838 Termios task driven mode should use mutex for device operations
- #2839 Add new interrupt server driven Termios mode
- #2840 Use self-contained mutexes for Termios framework
- #2841 Add NXP SC16IS752 serial device driver
- **#2843** Use self-contained objects instead of Classic API for drivers and support libraries
- #2844 JFFS2: Add IO controls to get filesystem instance information and force a garbage collection
- #2845 Add I2C framework documentation
- #2849 ATA/IDE support in RTEMS is out-dated
- #2850 Driver manual covers non-existent Analog Driver
- **#2851** Driver manual covers non-existent Discrete Driver **#2853** Driver manual covers non-existent Non-Volatile Memory Driver
- #2858 Add user defined thread names
- #2859 Implement POSIX Shared Memory Objects
- **#2862** docs.rtems.org Add support to ReST format releases.
- **#2863** Update POSIX 1003.1 Compliance Guide for ReST
- **#2864** docs.rtems.org Automatic update of branches content when a rtems-doc.git change is made.
- #2865 Coverpage installed when building the docs repeats catalogue.xml entries
- #2867 Fix exclude rule in rtems-test-check
- #2868 src/c/src/lib/libbsp/arm/smdk2410/smc/smc.c: 3 * pointless local variables ?
- **#2873** src/c/src/lib/libbsp/arm/raspberrypi/i2c/i2c.c:320: defective error checking ?
- #2874 src/c/src/lib/libbsp/powerpc/beatnik/marvell/gt_timer.c: 4 * pointless check ?
- **#2877** DHCP client fails on complex networks
- #2878 src/c/src/lib/libbsp/sparc/shared/can/occan.c:1573: broken error checking ?
- #2879 src/cpukit/libdebugger/rtems-debugger-server.c: four problems
- #2880 src/cpukit/libfs/src/jffs2/src/readinode.c:189: faulty logic
- #2883 src/c/src/lib/libbsp/arm/tms570/console/tms570-sci.c:248: strange expression ?
- #2885 Fix rtems_rate_monotonic_postponed_job_count() prototype
- #2889 RTEMS_STACK_CHECKER_EXTENSION has incomplete definition
- #2890 _RBTree_Initialize_node generates warnings
- **#2893** Remove CONFIGURE_SMP_APPLICATION
- #2894 Rename CONFIGURE_SMP_MAXIMUM_PROCESSORS to CONFIGURE_MAXIMUM_PROCESSORS
- #2895 Prefix the confdefs.h internal defines with an underscore
- #2896 RSB requirements are missing pax
- #2897 Update termios.h to match the latest FREEBSD definitions
- #2905 Merge LEON
- #2906 rtems-doc waf configure does not detect sphinxcontrib.bibtex status
- #2909 xz: Support for 64-bit CRC is build although XZ_USE_CRC64 is not defined
- **#2912** libdebugger: control reaches end of non-void function
- #2916 termios: Change receive callback invocation to enable select() and poll() support
- #2917 termios: Make write POSIX compatible
- **#2922** libdl unresolved externals that use more than one block or multiple entries corrupts.
- #2923 Questionable Code in resource_snapshot.c
- #2924 Warnings in SPARC BSPs
- #2925 Warnings in rtl-obj-cache.c on some targets
- #2930 Coverity Reports Out of Bounds Read in drvmgr_print.c
- **#2933** Flexibleassignto is broken on new ticket page.
- #2935 Termios task driven mode not compatible with SMP
- #2941 building rsb freezes
- #2942 rtems building error
- #2943 rtems building error
- #2945 Many failures on LEON3 with SMP disabled
- **#2946** Add a top level global testsuite configuration file (.tcfg) and a 'user-input' test state.
- #2949 Questionable patch organization in RTEMS tools and RSB
- #2951 Error path in rtems-gcc-6.3.0-newlib-2.5.0.20170228-1.cfg
- #2954 ARM: Optimize context switch
- #2957 Shared memory support internal locking is broken
- #2958 Add some popular benchmark programs to the testsuite
- #2959 arm/libdl: C++ exception index tables may not be ordered correctly
- **#2962** Set test configurations to reflect test results.
- **#2963** Add a testsuite top level confguration file that is common to all tests.
- #2965 bootstrap sort inconsistent with sb-bootstrap for acinclude
- #2967 ARM: Change ABI to not use short enums
- #2968 newlib inttypes.h is missing some methods
- #2969 qoriq BSPs depend on mkimage which is not always available
- #2976 warnings in rtems-debugger-server.c
- #2977 warnings in Dhrystone Benchmark
- **#2980** pc586-sse does not compile fsjffs2gc01 **#2981** testdata excludes on included tcfq files does not work
- #2982 LibBSD broken with GCC+RTEMS changes
- #2983 Create <rtems/inttypes.h> to consolidate extensions to <inttypes.h>
- #2984 Changing Trac milestone page fails.



https://devel.rtems.org/wiki/Release//5/5.1

4/130

#3142 POSIX: Reduce size of pthread_once_t and make it zero-initialized

- #3148 PSXRDWRV Test failure on Beaglebone Black
- #3152 Beaglebone Black crashes on u-boot master build.
- #3153 Accept PTHREAD_PROCESS_SHARED for POSIX rwlocks
- #3157 PowerPC tools don't build on 32-bit hosts
- #3158 Examples v2 does not build
- #3159 Examples v2 trace linker ini files reference non-existing dump-on-error
- #3160 Trace linker score support is broken
- #3163 Add I2C device driver for temperature sensor LM75A #3166 New default ticket assignee: NeedsReview
- #3167 Internal status codes must not depend on RTEMS_POSIX_API
- #3168 Simplify POSIX_API_Control
- **#3170** Use BSP_output_char via RTEMS printer or simple console driver for test output by default
- #3171 RSB GCC does not build on High Sierra and APFS
- #3172 i386 PC BSP does not reset when bsp_reset is called. #3173 XIIinx AXI I2C driver IP race condition causes clock glitch.
- #3174 Remove rtems_pthread_attribute_compare()
- #3175 Merge FreeBSD timecounter changes from 2015-01-20 to now
- #3176 ____getreent in libc.a and generated by confdefs.h
- #3177 Replace/update POSIX Compliance Guide
- #3178 Update sh-rtems4.12 bset to use rtems-default (using old gcc)
- **#3179** New warnings from Time Changes **#3180** ar warning: `u' modifier ignored since `D' is the default (see `U')
- #3181 Various cc1plus warnings for "valid for C/ObjC but not for C++
- #3182 CLOCK_REALTIME timeout implementation is not POSIX compliant
- #3185 Change uptime seconds to int32_t
- #3187 smptests/Makefile.am Issues #3188 Add C11 Threading Examples
- #3189 MUTEX_INITIALIZER missing braces warning
- #3190 RTEMS Tester covoar does not link on MacOS
- #3191 RTEMS Tester covoar dies with no arguments.
- #3198 Add lazy update of line control and baud divisor to NS16550 serial driver
- #3200 m32c tests don't build -- test_context too large
- #3201 epiphany tools checksum error
- #3202 or1k tools build error
- #3203 Upgrade trac to fix numerous problems.
- #3204 Exception in rtems-test
- #3205 Relative timespec timeouts are subject to integer overflows
- #3207 Supported Architectures Page is out of date
- #3209 RSB should fail on this error
- #3210 Improve the RSB build email message
- **#3211** Fix pthread_create() with user provided stack
- #3212 Qemu Fails to Build, RSB Gives Odd Traceback
- #3213 Move erc32, leon2, leon3, psim and jmr3904 to Tier 2 #3215 Configuring a System Still Includes Notepads and Has Wrong Heading
- #3216 Replace vprintk() implementation #3217 Add RTEMS version, build and tools details to tests
- #3218 Termios canonical mode (ICANON) does not return input line by line
- #3219 Zyng BSP missing linker option --gc-sections
- #3220 Change RTEMS release number scheme from 4.12 to 5
- #3221 RSB wiki page duplicates documentation
- #3224 Upgrade or1k and m32c to Binutils 2.29
- #3225 Upgrade m32c to GDB 8.0.1
- #3226 gdb: pr 16827, fix sim on Mavrick
- #3227 sb-check fails on Msys2 64-bit
- #3228 aarch64 missing from 5/rtems-all build set
- #3229 Add index to all documents.
- #3231 RTEMS Top level README needs updating.
- #3232 Use of `.. include::` in the User Manual should be changed.
- #3234 Quick Start Instructions Inconsistent
- #3235 Fix rtems_semaphore_flush() for priority inheritance semaphores
- #3236 Fix thread queue owner priority update in _Thread_queue_Flush_critical()
- #3237 Fix priority ceiling updates
- **#3238** Git push to Trac with more than one commit does not update tickets.
- #3239 Add getentropy() implementation provided by each BSP
- #3240 cpukit/libmisc/stackchk/check.c stack addresses formatted incorrectly.
- #3242 Workarounds for UT699, UT700, and GR712RC errata
- #3243 Simplify global construction
- **#3244** Change rtems_panic() implementation and document this function
- #3245 Replace BSP_panic() with rtems_panic()
- #3246 Remove BSP Fatal error()
- #3247 Remove BSP-specific defaults for RTEMS_BSP_CLEANUP_OPTIONS()
- #3248 Add BSP_VERBOSE_FATAL_EXTENSION to RTEMS_BSP_CLEANUP_OPTIONS
- #3249 imx7 does not link getentropy01 test on master #3254 Reorganize header files to avoid "make preinstall"
- #3255 Warnings on 64-bit targets
- #3256 Ada run-time needs support for self-contained POSIX synchronization objects
- #3260 libpci depends on BSP-specific header files
- **#3261** A couple of documentation typos
- #3264 Add monotonic watchdog based on uptime
- #3265 Use second one based uptime for CLOCK MONOTONIC for FreeBSD compatibility
- #3266 cpukit/libpci references BSP headers.
- #3267 rtems/status-checks.h calls printk without including the needed header.
- #3268 PowerPC BSP include naming mess
- #3270 Remove unused support for MPC505
- #3277 OorIO: Add MAC-less DPAA driver to libbsd #3278 bsp-builder has incorrect print (%s in output)
- #3281 Add epiphany support to GDB 8.0.0
- #3283 Bad URL in OpenOCD/Xilinx_Zynq Wiki Page
- #3284 RSB uses hard coded GCC binary paths
- #3285 Reorganize BSP source directory
- #3290 Add device tree support to Altera/Intel Cyclone V BSP
- #3294 gcc version report for released tools is wrong

- #3305 Add paravirtualization support to ARM
- **#3306** Add paravirtualization support to PowerPC **#3307** PowerPC linkcmds.base missing wildcards on some sections
- **#3309** rtems_task_create's initial_mode SMP update
- **#3312** RSB macro calls such as define fail on unicode keys.
- **#3315** Move expat's home site to github from SF.
- #3318 Improve INTERNAL_ERROR_THREAD_EXITTED to show the id and thread name
- **#3320** Add a simple task console driver
- #3323 mhttpd's http etag can result in invalid caching in a browser.
- #3325 Simplify clustered scheduler configuration
- #3327 Eliminate score/cpu/*/.../types.h
- #3328 bootstrap uses non-POSIX compliant echo -e
- #3329 Trac Login Failure (bad password) Causes Internal Error
- #3334 deadlock in _once()
- #3339 Several PowerPC linker commands do not support constructors/destructors with priority
- #3340 gen83xx warning for macros redefined
- #3341 sparc64: Macro Redefined
- **#3342** pthread_setschedparam() has incorrect prototype
- #3343 pthread_mutex_getprioceiling() has incorrect prototype
- #3344 mcf5272/mcf5272.h Timer3 Duplicate Definition #3345 mvme3100 spaces needed around quote in macro definitions in bsp.h
- #3346 bf533.h
- #3348 beatnick:spaces needed around quote in macro definitions in bsp.h
- #3349 pc386 edid.h invalid macro names
- #3350 sptimecounter02 warning due to defining _KERNEL and disabling part of <sys/time.h>
- #3352 Warning in all lpc176x variants
- #3354 PowerPC BSPs duplicate PAGE_MASK, etc redefinition
- **#3358** Deprecate rtems_disk_create_phys(), etc.
- #3374 rtems-test does not honor --mail-from argument
- #3375 Remove command line pre-processor defines
- #3376 Remove cklength program
- #3377 Remove eolstrip program
- #3378 Remove unhex program
- #3379 Remove packhex program
- #3380 Move rtems-bin2c program to rtems-tools
- **#3381** rtems-test command line documentation appears to be out of date **#3382** Testsuite Makefile merge to one per group of tests
- **#3383** Require --enable-rtemsbsp with --enable-smp or --enable-multiprocessor
- **#3384** Prefer int for int32 t
- **#3385** Generate an error if RTEMS's gcc is not found when the user runs configure
- #3386 Trac's git changeset browsing is suspect.
- #3387 Add subdir-objects to automake flags
- **#3388** rtems-tester: possible parsing error for qemuprep-altivec on exclude SMP configuration
- #3389 Warning flags have disappeared with recent autoconf changes
- #3390 NFS: Remove support for cexp
- **#3392** infinite loop in RSB's path when a prefix path is not writable
- **#3395** rtems-ld does not remove executable when there is an output error
- #3396 rtems-ld does not handle R_ARM_V4BX relocation records
- #3397 The register keyword is deprecated in C++11
- #3401 dl06: tms570* Mixed LSB/MSB Error
- #3402 dl06: mips hurricane Mixed Endian Error
- #3403 RSB RTEMS tool set build is irreproducible
- #3407 Move Gaisler.org and Gaisler.se hosted RSB patches to rtems.org
- #3409 Strip down configure checks to the bare minimum
- #3410 Remove bin2boot program used by i386 BSPs
- **#3411** qemuppc does not install linkcmds.base
- #3413 examples-v2 both_hello and triple_period fail to build
- **#3415** Add examples and tests as components
- #3416 Update Ubuntu RSB Instructions for 17.10
- #3417 Add libdwarf to elftoolchain and provide a C++ wrapper
- **#3418** Remove difftest and sorttimes test tools
- **#3419** Always build network services (tftpfs, ftpfs, ftpd, telnetd, libdebugger)
- #3421 New Trac components for Coverage and Trace
- **#3423** examples-v2: m68k/powerpc BSPs undefined reference to _Thread_Life_action_handler
- #3424 examples-v2: no MIPS BSPs pass configuration step
- #3425 examples-v2: PowerPC fails to build fat_ramdisk
- #3432 Remove Simple SMP Priority Scheduler
- #3433 Add SMP support for RISC-V
- #3434 Add CONFIGURE_MINIMUM_POSIX_THREAD_STACK_SIZE configuration option
- #3435 Add test case for CONFIGURE BSP PREREOUISITE DRIVERS configuration option
- #3436 Remove clock driver Clock_driver_support_shutdown_hardware() hook
- #3437 Replace use of printk() in free() with a fatal error
- #3443 Remove shgen program
- #3444 Remove nios2gen program
- #3445 Remove multigen script
- #3446 Remove cvsignore-add.sh script
- **#3447** Remove rtems-testsuite-autostuff script
- **#3451** Remove size rtems script
- #3452 Update RISC-V tool chain to support standard 64-bit chips
- #3453 Add RISC-V GDB
- **#3454** Tracing Framework Documentation in User Manual
- #3455 Remove install-if-change script
- #3458 rtems-test should not use the env PATH to find covoar
- **#3459** Rework initialization and interrupt stack support
- #3460 GDB 8 SIS LEON2 LEON3 Patches
- #3461 Canadian cross compilation of RTEMS tools not supported for x86_64-w64-mingw32
- **#3463** Convert covoar to use DWARF function data
- #3465 Integrate all changes from Linux v3.11 to v4.17 made in the JFFS2 sources
- #3471 Update libfdt as of date 2018-07-09
- #3472 Update of libbsd to a version close to the FreeBSD 12 release
- #3475 Add RTEMS_PREDICT_TRUE() and RTEMS_PREDICT_FALSE() for static branch prediction hints
- #3478 RISCV BSP Tester Cleanup Needed

#3480 CONFIGURE_MINIMUM_TASK_STACK_SIZE may affect CONFIGURE_INTERRUPT_STACK_SIZE **#3482** Relax the buffer alignment required by rtems_partition_create()

#3484 RFS: Remove stray call of rtems_disk_release() in rtems_rfs_buffer_sync()

#3486 Use uintptr_t and size_t instead of uint32_t in rtems_partition_create()

#3488 Remove CONFIGURE_HAS_OWN_MOUNT_TABLE

#3489 Obsolete CONFIGURE_HAS_OWN_CONFIGURATION_TABLE

#3490 Remove CONFIGURE_HAS_OWN_CONFIGURATION_TABLE

- **#3491** Align mprotect() prototype with POSIX
- **#3496** Remove superfluous interrupt enable in _Thread_Dispatch_enable()
- **#3498** Command and Variable Index is empty **#3499** The "Index" chapter is empty
- #3500 Change rtems_waf's RTEMS path check from `bin` to share/rtems<version>`
- #3501 MSR_RI defined multiple places
- #3502 PL111_LCD_CONTROL_LCD_BPP_16 Redefined
- **#3503** PDF Documentation is missing an index **#3504** Warning and formatting in bsps/powerpc/mpc55xxevb/dev/dspi.c
- #3505 powerpc/virtex redefined warning
- #3506 waf for building RTEMS applications needs updating
- #3507 Add flexible per-CPU data
- **#3508** Add support for thread to processor pinning
- **#3510** ATA driver uses deprecated rtems_blkdev services
- **#3511** int/pointer size warnings in powerpc-qoriq_e6500_64
- **#3512** sb-check:No python command with Python 2 and Python 3 installed **#3513** Convert tqm8xx console driver to new Termios API
- **#3516** sb-set-builder should report disk usage of build
- #3517 RSB Ubuntu Host Requirements Missing Some
- #3518 RSB MacOS Nits
- #3519 RSB does not strictly check args
- #3520 Remove CONFIGURE_HAS_OWN_FILESYSTEM_TABLE
- #3522 Update mDNSResponder to Apple version v878.30.4
- #3523 Add FEC network interface driver for TQM8XX
- #3525 Add MMC/SDCard support for i.MX 7Dual BSP
- #3526 Convert PTY driver to new Termios API
- #3528 Remove undocumented and untested CONFIGURE_MAXIMUM_PTYS
- **#3529** Fix issues raised by Coverity Scan for Telnet server **#3530** Fix issues raised by Coverity Scan for FTP server
- **#3531** Add POSIX Attribute Reports for More Than Scheduler (examples-v2)
- #3532 RSB source only download is host specific
- #3533 Add rtems_task_exit()
- #3535 Remove stdin, stdout, stderr convenience routines for CEXP
- #3536 Move RTEMS configuration data to a common `config` directory
- #3537 RSB and RTEMS Tools Support for python2 and python3
- #3538 Classic API Barrier Wait Section Title Has Wrong Name
- #3539 Remove CPU_PROVIDES_IDLE_THREAD_BODY
- #3542 Remove keep_stdio feature from Telnet service
- #3543 Change Telnet server to allocate most resources during initialization
- #3545 Support O_DIRECTORY open() flag
- #3546 Support O_NOFOLLOW open() flag
- #3547 Support O_CLOEXEC open() flag #3549 Obsolete powerpc/virtex BSP
- **#3551** Move default configuration to separate library
- #3552 cpu usage error in SMP mode
- #3553 rtems-libbsd Missing waf in Top Directory
- #3554 rtems-libbsd README.waf Needs an Update Sweep
- #3555 IRC bots need to be registered to join #rtems
- #3557 Test ticket
- #3558 Update TracSpamFilter
- #3559 Fix NavAdd plugin.
- #3560 Fix FlexibleAssignTo
- #3561 Migrate to CommitTicketUpdater
- #3562 Use a short paths for the RSB temporary build path on Windows
- #3568 RSB: UnboundLocalError: local variable 'build max size human' referenced before assignment
- #3569 waf version in various rtems-repositories incompatible with python 3.7
- #3576 gdb 8.0.1 sis does not build on Cygwin
- #3577 Avoid CLooG and ISL host depencencies for target GCC
- **#3577** testsuite's rtems-test-check.py python version support
- **#3583** Add rtems_malloc() and rtems_calloc()
- #3585 Add Rents_malloc() and #3585 Deprecate proc_ptr
- #3587 Deprecate rtems_context
- **#3589** Deprecate rtems_context_fp
- #3591 Deprecate region_information_block
- **#3593** Deprecate rtems_thread_cpu_usage_t
- **#3595** Deprecate rtems_rate_monotonic_period_time_t
- **#3598** Move internal types of API objects to separate header file
- **#3599** Remove m32c architecture port
- **#3600** Update or1k tools to use GCC master
- #3602 Update or1k tool chain to use the upstream GCC
- **#3603** Remove support for 16-bit object identifiers
- #3604 RTL Unresolved Symbols from common section on i386/pc686 (cloned)
- #3605 RTL Allows Unloading a Module other Modules Depend Upon (cloned)
- **#3609** Update Spike Version in RSB (RISC-V simulator)
- **#3612** RTL unresolved compaction does not update string indexes after removing a string
- #3620 CommitTicketUpdater does not process commits in order
- #3621 Statically initialize object information structures
- **#3622** Remove cache routines working with a processor set
- **#3624** MSYS2 builds appear to ignore tcfg file
- #3625 RTL Allows Unloading a Module other Modules Depend Upon (cloned)
- **#3626** sigtimedwait() needed when POSIX is disabled **#3629** Add RSB reporting section to the documentation.
- **#3630** Build of rtems-tools fails with i686-w64-mingw32
- **#3636** Add rtems_scheduler_get_maximum_priority()
- #3637 Fix rtems_task_restart() argument type
- **#3649** Error with IRC anouncing in examples-v2 commits.

#3651 Sphinx 1.8 PDF (latex) on FreeBSD does not build

- **#3664** RSB config parsing slow on python3
- **#3665** Add low level event recording infrastructure **#3666** Add support for C++17 std::aligned_alloc
- **#3667** Support data cache disable on ARMv7-AR
- **#3668** Commit message in examples-v2 and libbsd didn't trigger a ticket update.
- #3669 rtems-docs.git does not build with Sphinx 1.8.2 and 1.8.3
- #3670 examples-v2 uses deprecated or obsolete RTEMS interfaces
- #3672 No i386 BSP can link all tests after cache manager changes
- #3673 xilinx_zynq_a9_qemu fails to link psxconfig01
- #3674 Raspberry Pi Fails to Build
- #3675 RSB: Change default prefix to OS prefix + "rtems" + \$rtems_version
- #3677 ARM BSP contains ARM code in THUMB only build
- **#3678** Add RISC-V BSP with support for the grlib **#3682** Add BSP for Xilinx Zynq UltraScale+ MPSoC platform
- **#3683** Git clone via HTTPS does not give much interactive feedback
- #3684 rtems_print_buffer is broken
- #3685 Add large memory support to libdl
- #3686 Add library searching and loading to libdl
- **#3687** Add architecture section support to libdl and support PowerPC's small data.
- #3688 rtems-docs fails to build with python3
- #3692 libdl does not honour write unlock/lock for sections
- #3693 libdl incorrectly handles MIPS16hi/lo relocs
 #3694 shm_open has logically unreachable code (Coverity ID: 1399706, 1399714)
- #3696 Basic Support for Trace Compass
- #3699 Wrong system register specified for ARM virtual timer value retrieval
- #3720 mfill shell command uses the wrong arguments for the memset()
- #3724 bsp/lpc24xx: Convert SSP driver to Linux API
- #3725 bsp/lpc24xx: Convert I2C driver to Linux API
- **#3728** Set small data seciton to max size for mvme5500 and motorola_powerpc BSPs
- #3731 Add rtems_scheduler_get_processor()
- #3732 Add rtems_scheduler_get_processor_maximum()
- #3733 Add general reg support to libdebugger
- #3734 Add RTEMS_CONST attribute
- #3735 Remove CONFIGURE_HAS_OWN_MULTIPROCESSING_TABLE
- #3736 PowerPC Beatnik BSP C++ exceptions broken
- #3741 libdl loading ELF objects from libbsd NFS file system ends in a deadlock
- **#3742** T_config conflicting type qualifiers for 'config' **#3743** RSB os and arch config logic is broken
- **#3746** libdl test dl05.exe failing
- #3747 Address Cortex-M3 Errata 602117
- **#3748** libdl uses a linear symbol search on object file symbols
- **#3751** No documentation on Region Get Information Directives
- #3753 Rename CONFIGURE LIBIO MAXIMUM FILE DESCRIPTORS
- **#3754** Users Guide Ubuntu Instructions Have Typo
- **#3756** Condition codes in PSR are destroyed by lazy FP context switch
- #3760 BBB MMU update crashes
- #3762 Return the current handler from ARM cp15 set exception call
- #3763 RSB SIS build fails on FreeBSD
- #3768 Add staging support to Makefile.inc
- #3769 RSB BSP Buildsets
- #3770 RSB 3rd party packages failing to build
- #3773 RPi fails to boot
- #3774 RPi2 SMP does not build
- #3775 libdl does not handle ARM mode reloc tramp parsing
- #3776 libdl ARM does not support ARM mode trampolines.
- #3777 libdl object unload debugger delete support is broken
- #3781 RSB crashes in case the host as an unreadable directory in "/"
- #3783 MSYS2 RSB build error
- #3785 Add RISC-V BSP with support for the Freedom E310 Arty A7 FPGA
- #3789 TMS570 applciation build error
- #3792 RSB fails to build on MSYS2
- #3793 trace record tool does not build on Windows
- #3794 Initial POSIX Signals Mask Incorrect
- **#3796** docs/develenv directory structure bitrot **#3797** Add LLVM as a package
- #3798 Add sockatmark to libbsd
- **#3800** termios Add Capability to Generate SIGINTR and SIGQUIT
- **#3802** RSB Build of Spike Fails on Second TIme (bug in upstream spike)
- #3803 RSB ssl context error fetching gemu patches
- #3804 sb-get-sources: Error repo mail referenced before assignment
- **#3805** libdebugger build error on atsamv
- **#3806** Add fatal error for heap errors
- #3808 Fix qemu-couverture-git RSB download file name
- #3809 Fix epiphany-rtems5-gdb-7.8 RSB download file name
- **#3810** Use the release details in the release build docs
- **#3811** Release source path on ftp.rtems.org is wrong
- **#3812** Released RSB has no source set for rtems-tools
- **#3813** RSB does not handle --rsb-file in releases
- #3814 Releasing creates 2 copies or the kernel and tools.
- **#3815** Improve SMP EDF scheduler configuration
- #3817 RSB fails on FreeBSD 12.0 (32bit and 64bit)
- **#3821** Port NVMe support from FreeBSD to libbsd
- **#3822** Release created VERSION file in rtems-tools-.*.tar.xz is wrong **#3823** Untar family doesn't handle nested directories
- #3826 top on SMP shows invalid priorities
- **#3830** Build problems with user names which contain space characters
- **#3831** Duplicate description of Tiers and Rules
- **#3833** Simplify RTEMS semaphore configuration
- #3834 Simplify clock driver
- #3835 Support statically allocated threads
- **#3836** Specify the application configuration options
- **#3837** Rename CONFIGURE_LIBIO_MAXIMUM_FILE_DESCRIPTORS

#3838 Rework work area initialization #3839 RTEMS revision does not handle `-`

- #3840 Add CONFIGURE_IMFS_ENABLE_MKFIFO
- #3841 Add rtems_object_get_local_node() #3842 RSB RTEMS version message string is fixed to the git hash
- #3843 Add CONFIGURE DIRTY MEMORY
- #3844 Remove CONFIGURE_HAS_OWN_DEVICE_DRIVER_TABLE
- #3845 Remove Ada-specific configuration options
- #3848 Libdebugger test in libbsd should depend on libdebugger.a
- #3849 Fix PSIM memory map
- #3856 posix_devctl Add support for SOCKCLOSE
- #3857 Use EAGAIN for POSIX mq wait in ISR error
- #3859 No output from joel scripts in telnet
- **#3861** Add CONFIGURE_VERBOSE_SYSTEM_INITIALIZATION #3862 Canonicalize CONFIGURE_ZERO_WORKSPACE_AUTOMATICALLY
- #3863 Remove support for the BSP_ZERO_WORKSPACE_AUTOMATICALLY BSP option
- **#3864** rtems-tester does not work with gdb simulators
- #3865 Fix linker set item declarations for small data area targets
- #3868 newlib links breaks mingw build
- #3871 Remove rtems_configuration_get_posix_api_configuration()
- #3873 Remove CONFIGURE_HAS_OWN_INIT_TASK_TABLE
- #3874 Remove CONFIGURE_POSIX_HAS_OWN_INIT_THREAD_TABLE **#3875** Split up confdefs.h in component based header files
- #3876 Remove CONFIGURE_DISABLE_SMP_CONFIGURATION
- **#3881** Add API functions to map a task priority to/from a POSIX thread priority
- #3882 Add POSIX user environment pointer to TCB
- #3885 Context switch extension is broken in SMP configurations
- #3887 Do not report remotes in RSB build log if --mail is used
- #3888 Update rtems_waf in libbsd
- #3893 RSB staging changes have broken building a 3rd party package
- #3894 Replace the device filesystem with a specialization of the IMFS
- #3895 Add a migration to RTEMS 5 chapter to User Manual
- #3896 RSB option --source-only-download does not work with releases
- #3898 Remove CONFIGURE_MAXIMUM_DEVICES
- #3900 New template for boolean feature defines #3901 New template for configuration options with a value
- #3903 raspberrypi2 libbsd 5-freebsd-12 does not build
- #3904 Add methods to dump the event records in base64 encoding (optionally zlib compressed)
- **#3907** Update Getting Started Instructions
- #3909 rtems_waf with python2 needs to handle unicode strings with waf
- #3911 Remove gdbarmsim
- #3914 Spike has hard-coded path to DTC
- #3919 RSB may not download source of pkconfig checked packages
- #3921 QorIQ clock tick interval is off by one hardware clock tick
- #3927 tclsh required to build sqlite -- makes all BSP bsets fail
- #3936 C++ thread-local storage broken on sparc64
- #3938 Many (~40) BSPs Fail to Link all Tests
- **#3944** qoriq_e500 BSP bset fails
- #3945 Update DTC example on rtems-docs/user/rsb/configuration.rst #3949 clock_settime() can lead to a failed _Assert()
- #3953 rtems_extensions_create() accepts a NULL pointer table #3956 RSB BSP build with tests does not keep a copy
- #3960 Add to FreeBSD host setup information
- #3961 bsps/arm: CPU counter based on arm generic timer doesn't work correctly
- #3962 Licensing Requirements for Submissions Poorly Documented

Details

Hence -	Created	Resolution	Component	Reporter	Owner	Modified
#1247	13 years ago	fixed	fs	strauman	Chris Johns	2 years ago
Summar y	RTEMS does not in	mplement locks ne	eded by multithreade	d newlib		
Descripti on	 global list d stdio FILE individual l global hasl individual l atexit list list of envii global time) using mutexes. own implementati added to RTEMS a 	of FILE objects object initializer FILEs [since FILEs h table used by tel DIR structures (op ronment variables zone variable It expects the OS ion of 'sys/lock.h' a while ago but sin	with the exception of Idir/seekdir endir/readdir) to implement these lo and therefore vital dat ce then, newlib has in	a structures in newlib are currer troduced more locks and a gene		ronment variables 'envlock.c' had been vill obsolete 'envlock.c').
	acceptable.	5	nentation which shoul	d be available shortly.		
#1394	acceptable.	5	nentation which shoul tool/newlib	d be available shortly. Daniel Hellstrom	Chris Johns	2 years ago

	RTEMS	5.1	Release	Notes
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wi3 5.			wower the neurlible	no doog not work due to MAVN/	AMLEN and NAMLEN differ. scandir in libcsuppo	
				HAVE_SCANDIR is defined.	a leef and to a leef amon ocarian in hocouppo	rt seems to have a fix for this, nowever my
	It is used in scandir() (newlib-1.17.0/r	newlib/libc/posix/sc	candir.c:117) by the macro DIRS	SIZ. Where DIRSIZ uses the MAXNAMELEN def	
					(DIRSIZ(d)) will try to allocate 4GB which fail.	
					s/sys/dirent.h or newlib-1.17.0/newlib/libc/sys/	rtems/include/limits.n or in a new file.
				n directly from the scandir(3) m		
	/* print files i	n current director	y in reverse order *	*/ #include <dirent.h> main(){</dirent.h>		
	struct	dirent namelist;	int n;			
Descripti on	n = sc	andir(".", &nameli	ist, 0, NULL);	< 0)		
		perror("scandir")	;			
	else {					
		while(n) {				
		printf("%s	h", namelist[n]->ه/،	d_name); free(namelist[n]);		
		<pre>} free(namelist);</pre>				
	}					
	}					
#1662	10 years ago	fixed	fs	Bharath Suri	Chris Johns	2 years ago
Summar	termios.c: semaphor	e not deleted, cor	sequently resulting	g in failure of rtems_termios_op	pen	
У	The computers econ	is still in use in u	rtams tarmias clas	a while an attempt to delete it i	is made and hence is not deleted. Consequentl	v it results in a PTEMS TOO MANY on
Descripti on				of rtems_termios_open.	is made and hence is not deleted. Consequenti	y, it results in a KTEMS_TOO_MANT ON
#1747	9 years ago	fixed	score	Chris Johns	Sebastian Huber	2 years ago
Summar	Heap extend allows of	liscontinuous mer	nory regions.			
У	The check in (cpukit,	Changel og) state	25.			
			sebastian.huber@	~		
			-	scattered heap areas.		
					s the region is continuous between the first an	d last blocks of the bean. Making the gaps in
	the regions passed to	o the heap extend	d call used is question		s_block_in_heap test not really perform the tas	
Descripti on	I also wonder about kind of safe to probe				regions. The previous heap code knew if an add	dress was in the heap and therefore it was
		for a valid block.			regions. The previous heap code knew if an ad	dress was in the heap and therefore it was
	kind of safe to probe The former heap ext	for a valid block. end code:	This assumption is			dress was in the heap and therefore it was
	kind of safe to probe The former heap ext http://www.r	for a valid block. end code: cems.org/viewvc/r	This assumption is rtems/cpukit/score/	now not valid. /src/heapextend.c?revision=1.7		
	kind of safe to probe The former heap ext http://www.r	for a valid block. end code: ems.org/viewvc/r e of memory that	This assumption is rtems/cpukit/score/ t can be added to an	now not valid. /src/heapextend.c?revision=1.7	&view=markup	
	kind of safe to probe The former heap ext http://www.rl clearly states the typ comments are added The heap extend cod	for a valid block. end code: eems.org/viewvc/r be of memory that I to the heap extended is used by the r	This assumption is rtems/cpukit/score/ t can be added to and ind code. rtems_region_exten	now not valid. /src/heapextend.c?revision=1.7 n existing heap. The current coo nd call and this call clearly state:	&view=markup de has no restrictions. The user manual is not g s in the manual that the memory region must l	great in this area. It would also be useful if
	kind of safe to probe The former heap ext http://www.rl clearly states the typ comments are added The heap extend cod	for a valid block. end code: eems.org/viewvc/r be of memory that I to the heap extended is used by the r	This assumption is rtems/cpukit/score/ t can be added to and ind code. rtems_region_exten	now not valid. /src/heapextend.c?revision=1.7 n existing heap. The current coo nd call and this call clearly state:	<mark>&view=markup</mark> de has no restrictions. The user manual is not g	great in this area. It would also be useful if
on #1971 Summar	kind of safe to probe The former heap ext http://www.rf clearly states the typ comments are addec The heap extend coo discuss the API chan	for a valid block. end code: tems.org/viewvc/r be of memory that t to the heap exte e is used by the r ge and make bett fixed	This assumption is rtems/cpukit/score/ t can be added to an ind code. rtems_region_exten zer note of it. I also	<pre>: now not valid. /src/heapextend.c?revision=1.7 n existing heap. The current con nd call and this call clearly state suspect the testsuite will need</pre>	&view=markup de has no restrictions. The user manual is not s in the manual that the memory region must l additions to test any API changes.	great in this area. It would also be useful if be continuous. If this has changed we should
on #1971	kind of safe to probe The former heap ext http://www.rt clearly states the typ comments are added The heap extend cod discuss the API chan 8 years ago Memory leak in tmpf	for a valid block. end code: tems.org/viewvc/r be of memory that t to the heap exte e is used by the r ge and make bett fixed	This assumption is rtems/cpukit/score/ t can be added to an ind code. rtems_region_exten zer note of it. I also	<pre>: now not valid. /src/heapextend.c?revision=1.7 n existing heap. The current con nd call and this call clearly state suspect the testsuite will need</pre>	&view=markup de has no restrictions. The user manual is not s in the manual that the memory region must l additions to test any API changes.	great in this area. It would also be useful if be continuous. If this has changed we should
on #1971 Summar	kind of safe to probe The former heap ext http://www.rt clearly states the typ comments are added The heap extend cod discuss the API chan 8 years ago Memory leak in tmpf Hello,	for a valid block. end code: tems.org/viewvc/r be of memory that t to the heap exte e is used by the r ge and make bett fixed ile()	This assumption is rtems/cpukit/score/ t can be added to an ind code. tems_region_exten ier note of it. I also fs	now not valid. /src/heapextend.c?revision=1.7 n existing heap. The current cound call and this call clearly state suspect the testsuite will need nopscmn	&view=markup de has no restrictions. The user manual is not g s in the manual that the memory region must l additions to test any API changes. Chris Johns	great in this area. It would also be useful if be continuous. If this has changed we should 2 years ago
on #1971 Summar	kind of safe to probe The former heap ext http://www.rt clearly states the typ comments are addee The heap extend coo discuss the API chan 8 years ago Memory leak in tmpf Hello, I use tmpfile() in my	for a valid block. end code: tems.org/viewvc/r e of memory that t to the heap exte e is used by the r ge and make bett fixed ile() RTEMS applicatio	This assumption is rtems/cpukit/score/ t can be added to an ind code. tems_region_exten ter note of it. I also fs on, and found that it	<pre>: now not valid. /src/heapextend.c?revision=1.7 n existing heap. The current cod nd call and this call clearly state suspect the testsuite will need nopscmn t has memory leak. I wrote small</pre>	&view=markup de has no restrictions. The user manual is not end of the manual that the memory region must ladditions to test any API changes. Chris Johns	great in this area. It would also be useful if be continuous. If this has changed we should 2 years ago
on #1971 Summar y	kind of safe to probe The former heap ext http://www.rt clearly states the typ comments are addee The heap extend cod discuss the API chan 8 years ago Memory leak in tmpf Hello, I use tmpfile() in my Memory usage befor	for a valid block. end code: tems.org/viewvc/r e of memory that to the heap exte e is used by the r ge and make bett fixed ile() RTEMS applicatio e: Number of use	This assumption is rtems/cpukit/score/ t can be added to as ind code. tems_region_exten ter note of it. I also fs on, and found that if d blocks: 12 Larges	<pre>: now not valid. /src/heapextend.c?revision=1.7 n existing heap. The current cod nd call and this call clearly state: suspect the testsuite will need nopscmn t has memory leak. I wrote smast used block: 1288 Total bytes</pre>	&view=markup de has no restrictions. The user manual is not get in the manual that the memory region must ladditions to test any API changes. Chris Johns all test application (see attachment), that output used: 3628	great in this area. It would also be useful if be continuous. If this has changed we should 2 years ago
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on #1971 Summar y Descripti on #2133 Summar y Descripti on #2135 Sumar y Descripti on #2135 Sumar y Descripti Sumar y Sumar y Descripti Sumar y Sumar Sumar y Sumar Su	kind of safe to probe The former heap ext http://www.rt clearly states the typ comments are addec The heap extend cod discuss the API chan 8 years ago Memory leak in tmpf Hello, I use tmpfile() in my Memory usage befor Memory usage	for a valid block. end code: tems.org/viewvc/r te of memory that it to the heap exter e is used by the r ge and make bett fixed lile() RTEMS applicatio e: Number of used Number of used b tch call tmpfile() c fixed efs.h> superfluou: ons <rtems.h> pr include in the fut fixed are subject to int efined as unsigned fixed</rtems.h>	This assumption is rtems/cpukit/score/ t can be added to an ind code. tems_region_exten ter note of it. I also fs on, and found that if d blocks: 12 Larges blocks: 1013 Largest cause memory leak score sly includes <limits <limits.h="" rovided=""> ture. score sly includes <string <string.h="" rovided=""> ture. posix teger overflows d long for RTEMS in score</string></limits>	<pre>s now not valid. /src/heapextend.c?revision=1.7 n existing heap. The current cod nd call and this call clearly state: suspect the testsuite will need nopscmn t has memory leak. I wrote sma st used block: 1288 Total bytes t used block: 1288 Total bytes t used block: 1288 Total bytes Sebastian Huber s.h> indirectly. The include of <limit 1ms="" <="" a="" clock="" huber="" nhewlib.="" pre="" sebastian="" tick="" with=""></limit></pre>	&view=markup de has no restrictions. The user manual is not get in the manual that the memory region must ladditions to test any API changes. Chris Johns all test application (see attachment), that output used: 3628 used: 112064 Sebastian Huber is.h> was added to not break application source is.h> was added to not break application source Sebastian Huber ing.h> was added to not break application source Sebastian Huber an overflow happens after 7 days on 32-bit lon	great in this area. It would also be useful if be continuous. If this has changed we should 2 years ago ut memory usage: 2 years ago e files that relied on this accidentally. 2 years ago ce files that relied on this accidentally. 2 years ago ago
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on #1971 Summar y Descripti on #2133 Summar y Descripti on #2135 Sumar y Descripti on #2135 Sumar y Descripti Sumar y Sumar y Descripti Sumar y Sumar Sumar y Sumar Su	kind of safe to probe The former heap ext http://www.rt clearly states the typ comments are addee The heap extend cod discuss the API chan 8 years ago Memory leak in tmpf Hello, I use tmpfile() in my Memory usage befor Memory usage	for a valid block. end code: tems.org/viewvc/r te of memory that it to the heap exter e is used by the r ge and make bett fixed lile() RTEMS applicatio e: Number of used Number of used b tch call tmpfile() c fixed efs.h> superfluou: ons <rtems.h> pr include in the fut fixed are subject to int efined as unsigned fixed</rtems.h>	This assumption is rtems/cpukit/score/ t can be added to an ind code. tems_region_exten ter note of it. I also fs on, and found that if d blocks: 12 Larges blocks: 1013 Largest cause memory leak score sly includes <limits <limits.h="" rovided=""> ture. score sly includes <string <string.h="" rovided=""> ture. posix teger overflows d long for RTEMS in score</string></limits>	<pre>s now not valid. /src/heapextend.c?revision=1.7 n existing heap. The current cod nd call and this call clearly state: suspect the testsuite will need nopscmn t has memory leak. I wrote sma st used block: 1288 Total bytes t used block: 1288 Total bytes t used block: 1288 Total bytes Sebastian Huber s.h> indirectly. The include of <limit 1ms="" <="" a="" clock="" huber="" nhewlib.="" pre="" sebastian="" tick="" with=""></limit></pre>	&view=markup de has no restrictions. The user manual is not get in the manual that the memory region must ladditions to test any API changes. Chris Johns all test application (see attachment), that output used: 3628 used: 112064 Sebastian Huber is.h> was added to not break application source is.h> was added to not break application source Sebastian Huber ing.h> was added to not break application source Sebastian Huber an overflow happens after 7 days on 32-bit lon	great in this area. It would also be useful if be continuous. If this has changed we should 2 years ago ut memory usage: 2 years ago e files that relied on this accidentally. 2 years ago ce files that relied on this accidentally. 2 years ago ago
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on #1971 Summar y Descripti on #2133 Summar y Descripti on #2135 Summar y Descripti on #2135 Summar y Descripti Summar y Summar	kind of safe to probe The former heap ext http://www.rt clearly states the typ comments are addee The heap extend cod discuss the API chan 8 years ago Memory leak in tmpf Hello, I use tmpfile() in my Memory usage befor Memory usage	for a valid block. end code: tems.org/viewvc/r te of memory that it to the heap exter e is used by the r ge and make bett fixed lile() RTEMS applicatio e: Number of used Number of used b tch call tmpfile() c fixed efs.h> superfluou: ons <rtems.h> pr include in the fut fixed are subject to int efined as unsigned fixed</rtems.h>	This assumption is rtems/cpukit/score/ t can be added to an ind code. tems_region_exten ter note of it. I also fs on, and found that if d blocks: 12 Larges blocks: 1013 Largest cause memory leak score sly includes <limits <limits.h="" rovided=""> ture. score sly includes <string <string.h="" rovided=""> ture. posix teger overflows d long for RTEMS in score</string></limits>	<pre>s now not valid. /src/heapextend.c?revision=1.7 n existing heap. The current cod nd call and this call clearly state: suspect the testsuite will need nopscmn t has memory leak. I wrote sma st used block: 1288 Total bytes t used block: 1288 Total bytes t used block: 1288 Total bytes Sebastian Huber s.h> indirectly. The include of <limit 1ms="" <="" a="" clock="" huber="" nhewlib.="" pre="" sebastian="" tick="" with=""></limit></pre>	&view=markup de has no restrictions. The user manual is not get in the manual that the memory region must ladditions to test any API changes. Chris Johns all test application (see attachment), that output used: 3628 used: 112064 Sebastian Huber is.h> was added to not break application source is.h> was added to not break application source Sebastian Huber ing.h> was added to not break application source Sebastian Huber an overflow happens after 7 days on 32-bit lon	great in this area. It would also be useful if be continuous. If this has changed we should 2 years ago ut memory usage: 2 years ago e files that relied on this accidentally. 2 years ago ce files that relied on this accidentally. 2 years ago ago

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	1 Release Notes				11				
	On 2014-03-21 14:46, Gedare Bloor <sebastian.huber@> wrote:</sebastian.huber@>	m wrote:> On Fri,	Mar 21, 2014 at 9:43 AM, Seba	istian Huber					
	· · · · · · · · · · · · · · · ·								
	[]								
	No. For this, I think we need to use "deadline folding" which is just modulo arithmetic.								
	Thread Control *the thre								
		au, unitsz_t ueaun	lile						
) {								
Descripti	Priority_Control new_prior	rity;							
on	if (deadline) {								
	/* Initializing or sh	nifting deadline. */	new_priority = (_Watchdog_Ti	cks_since_boot + deadline)					
	& ~SCHEDU	JLER_EDF_PRIO_M	SB;						
	} else {								
	/* Switch back to !	<pre>} else { /* Switch back to background priority. */ new priority = the thread->Start.initial priority;</pre>							
	}	;	,,						
	the_thread->real_priority	= new_priority; _	Thread_Change_priority(the_th	read, new_priority, true);					
	}								
	_Watchdog_Ticks_since_boot us uin				-				
#2176	- , j	score	Jeffrey Hill	Joel Sherrill	2 years ago				
Summar v	fishy behavior in termios tx task mo	de							
ÿ				ermios drivers appear to transmit characters at tas frame of characters to send then this could lock or					
	FWIW, I had a closer look at this too UART can accept characters immedia characters it sent; presumably this v	day, and maybe so ately in the write ro would occur by call	mething is fishy in the termios outine then we wouldnt need to	code when the TX part of termios runs in task driv turn on any interrupts at all. The write routine wo aracters in the driver's write function. I see in the	en mode. It seems that in task mode if the uld need to somehow tell termios how many code that this tries to work,				
		posts the semapho	ore of termios tx and increases	the characters sent count of termios. However after	er the write routine returns it goes badly.				
	If the transmitter runs in termios TA	SK mode and the other that the termion	driver's write routine does not i	the characters sent count of termios. However aft mmediately enable an interrupt, then it returns to hrough all of the characters remaining (I think that	the code below in rtems_termios_puts and it				
	If the transmitter runs in termios TA sets the transmitter to rob_busy. Af them because the transmitter stays	SK mode and the otter that the termion in rob_busy state.	driver's write routine does not i s tx daemon proceeds to step t	mmediately enable an interrupt, then it returns to	the code below in rtems_termios_puts and it I see this in the debugger) and discards				
	If the transmitter runs in termios TA sets the transmitter to rob_busy. Aft them because the transmitter stays It's also probably odd that termios c	ASK mode and the of ter that the termion in rob_busy state. calls the write funct globally disabled.	driver's write routine does not i s tx daemon proceeds to step t	mmediately enable an interrupt, then it returns to hrough all of the characters remaining (I think that	the code below in rtems_termios_puts and it I see this in the debugger) and discards				
	If the transmitter runs in termios TA sets the transmitter to rob_busy. Aff them because the transmitter stays It's also probably odd that termios or routine at task level with interrupts if (tty->rawOutBufState == r	ASK mode and the of ter that the termine in rob_busy state. calls the write funct globally disabled. rob_idle) {	driver's write routine does not i s tx daemon proceeds to step t	mmediately enable an interrupt, then it returns to hrough all of the characters remaining (I think that en it is in task driven mode; we could loop outputt	the code below in rtems_termios_puts and it I see this in the debugger) and discards				

(char *)&tty->rawOutBuf.theBuf[tty->rawOutBuf.Tail],1);

} else {

/* remember that output has been stopped due to flow ctrl*/ tty->flow_ctrl |= FL_OSTOP;

} tty->rawOutBufState = rob_busy;

}

Descripti on

	[debug]#1 0x000 0x00015d9c in rt console_write (m 0x0006c328 in rt 0x0006c328 in rt 0x00068044 in d [debug]#6 0x000 (debug]#9 0x000 (debug]#9 0x000 (debug]#9 0x000 4.1/newilb/libc/sf patched/gcc-4.1/ rtems/altera/alte rtems/rtems/rtem rtems/rtems/rtem rtems/rtems/rtem rtems/rtems/rtem	15c68 in oproc (ems_termios_wri ajor=0, minor=0 ems_io_write (inp= 17ref4 in write (fr rrite_r (ptr=0x9d 16d488 in _fflush, 175488 in _fflush, 175488 in _fflush, 175488 in _fflush, 180/gnu-tools, ns-git/rtems-4.10 ns-git/rtems-4.10 er () at /home/h	c=99 'c', tty=0x1150'c) te (arg=0x9cad8) at /hom ajor=0, minor=0, argun 0x114988, buffer=0x33664, 1638, fd=1, buf=0x33664, 1638, fd=1, buf=0x3664, r(tp=0x9638, fp=0) f(f=0x04, fp=0) f(at /home/hill/nios2-rtems/rter pme/hill/nios2-rtems/rtems/rter le/hill/nios2-rtems/rtems/rter le/hill/nios2-rtems/rtems/rter lefold, count=44) at /home/hill/nios2- dd, nbytes=44) at /home/hill/nios2- dd, nbytes=44) at /home/hill/nios2- ray048c, at ./////nios2-r x9d98c, at .//////nios2-r x9d98c, at .// Qc in sprint_r (ptr=0x9d638, fp 11 0x00072fc8 in _vfprintf_r (cf newlib/libc/stdio/vfprintf.c:52 (df tworking/nfs/bootp_subr.c:997 tworking/rtems/rtems_bodp.c: tworking/rtems/rtems_glue.c:91 ems-git/rtems-4.10/c/src///	s2-rtems/rtems-git/rtems-4.10/c/src//./ ns/rtems-git/rtems-4.10/c/src/.//cpukit/libcsupport/s -git/rtems-4.10/c/src/.ilb/libbsp/nios2/altera-sys- so2-rtems/rtems/rtems-git/rtems-4.10/c/src/./. nios2-rtems/rtems-git/rtems-4.10/c/src/./. rtems/rtems/rtems-git/rtems-4.10/c/src/./. rtems/rtems/rtems-git/rtems-4.10/c/src/./. rtems/altera11.0/gnu-tools/rtems-patched .//./.nios2-rtems/altera/altera11.0/gnu-tools =0x20, uio=0x9cbe0) at////.nios2-rtems/ altaa=0x9d638, fp=0x9d98c, fmt0 = <value optimi<br="">1 [debug]#12 0x0006dce8 in printf (fmt=0x1 "") abug]#15 0x00057de0 in bootpc_init (update_fii [debug]#15 0x00025218 in Intems_bsdnet_ini 30 [debug]#16 0x00025218 in Intt (ignored=565 :pukit/score/src/threadhandler.c:145 [debug]#16 uations/score/src/threadhandler.c:145 [debug]#16</value>	port/src/termios.c:748 [debug]#2 src/termios.c:770 [debug]#3 0x00002ef0 in config/./console/console.c:171 [debug]#4 /.cpukit/sapi/src/iwrite.c:47 [debug]#5 .//cpukit/libfs/src/imfs/deviceio.c:160 ukit/libcsupport/src/write_r.c:38 /gcc-4.1/newlib/libc/stdio/fflush.c:214 /rtems-patched/acc- ms/altera/altera11.0/gnu-tools/rtems- ized out>, ap=0x0) at////nios2- ot at////inios2- es=false, forever=true) at /home/hill/nios2- tialize_network () at /home/hill/n
#2198	5 years ago	fixed	doc	Gedare Bloom	Chris Johns	23 months ago
Summar y	Automate doxyge	n build				
Descripti on	The doxygen buil	ds are no longer	being generated nightly.			
#2207	5 years ago	fixed	unspecified	Chris Johns	Chris Johns	2 years ago
Summar y	RTEMS tar does r	ot overwrite.				
Descripti on	because you have Tar should be cha	e to remove files inged to overwrit		he tar will be successful and mo e refreshed.	4S tar requires you remove the existing files rath oving and saving files assumes you know the con	
#2213	5 years ago	fixed	tool/gcc	daniel.cederman	Daniel Hellstrom	2 years ago
Summar	, ,		tone benchmark using G		,	_ , 390
У		4.4.2 to 4.9.2 in	creases the execution ti	me of the whetstone benchmar	k on both SPARC and x86. The cause seems to b	e a single commit. I have submitted a bug
y Descripti on	report to the GCC	5	bug.cgi?id=64193			
Descripti	report to the GCC	5	_bug.cgi?id=64193 unspecified	Joel Sherrill		2 years ago

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	1 Release N		state of the project sta	arted by Krzysztof Miesowicz <	<pre>krzysztof.miesowicz@>. The goals of the pr</pre>	roject were to:
	replace theadd the ca	e existing shell scri pability to generat orts harder to read	pts in rtems-testing w e a report per director	vith Python code integrated into y. This is important because a	rtems-tools arge body of code with low coverage negativ	
Descripti	The remaining eff capability to add t			ated to coverage such as inclusi	on of gcov generated reports is a good "sum	mer of code" type project. This is an importan
on	The attached tar f	ile krzy-patches.ta	ar.bz2 contains the cur	rrent code. There may be other	issues to resolve but writing from memory,	he following are the highest priority ones:
	 configurati 		ded paths. This should	d be able to be a template whic hat used in other Python code i	n is adjusted by the tools at run-time. n rtems-tools	
				owicz.blogspot.com/p/esa-socis erage support. This may have c		ools are built with the RTEMS Source Builder
#2266	5 years ago	invalid	unspecified	Joel Sherrill		2 years ago
Summar	Move bsp_pretask	king_hook() into fil	es named bsppretaski	nghook.c		
У	Over the past few	vears we have sr	alit out the BSP require	ed methods into their own files	with consistent names. bsp_pretasking_hook	() is next on the list
Descripti on	\$ grep -rl bsp_pre ./powerpc/virtex5 ./powerpc/ep1a/s	etasking_hook/s /startup/bspstart.c tartup/bspstart.c .	parc/shared/bsppreta c ./powerpc/virtex5/st /arm/lpc176x/startup/	skinghook.c ./powerpc/score60 art/start.S ./powerpc/shared/st	3e/startup/bspstart.c ./powerpc/beatnik/star artup/pretaskinghook.c ./powerpc/virtex4/st up/bspstart.c ./bfin/bf537Stamp/startup/bsp	tup/bspstart.c artup/bspstart.c ./powerpc/virtex4/start/start
#2284	5 years ago	wontfix	unspecified	Joel Sherrill	Chris Johns	2 years ago
Summar	h8300 gets error	linking dl0* tests	· · ·			
)escripti on	\ -o dl01.exe dl-sym.o: In funct	e init.o dl-load.o dl tion `rtems_rtl_ba	l-tar.o dl-sym.o	rldgTgaaa.c:(.text+0xa): und	rototypes -Wimplicit-function-declaration -W lefined reference to `rtems <u>rtl_base_qlobals_</u>	/strict-prototypes -Wnested-externs -mh -mir size' rldqTqaaa.c:(.text+0x10): undefined
#2289	5 years ago	fixed	tool/gcc	Sebastian Huber	Needs Funding	2 years ago
ummar	rtems_ada_self is	broken on SMP				
У						
	The global variabl	e rtems_ada_self i	is broken on SMP (sim	ilar to the task variables) and s	hould be replaced with a function call or thre	ad specific data.
Descripti on #2305	The global variabl 5 years ago	e rtems_ada_self i wontfix	is broken on SMP (sim unspecified	ilar to the task variables) and s Joel Sherrill	hould be replaced with a function call or thre	ad specific data. 2 years ago
on	5 years ago sp07 needs to be I was reviewing al	wontfix split into an user e Il code in the tree	unspecified extensions and a notep which uses deprecated	Joel Sherrill bad test d methods. I will fix most of the	cases. This is going to take a little more tim	-
on #2305 Summar y Descripti on	5 years ago sp07 needs to be I was reviewing al notepad usage ca The remaining use I am starting to m Hopefully I can re	wontfix split into an user of Il code in the tree of notepads in sp nove code to spnot solve this without	unspecified extensions and a notep which uses deprecated st spnotepad02. It is p 007 can probably just l eepad02 from sp07 tha much feedback.	Joel Sherrill Dad test d methods. I will fix most of the primarily ensuring that two three be a count down on the priority at is not related to the tasks cou	cases. This is going to take a little more tim ads can exchange values through notepads.	2 years ago e. This test needs to be split apart. I think the
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Descripti on

#2408 Summar У

In order to reduce the size of executables it is beneficial to put all global functions and data into separate sections. This enables the linker to perform a garbage collection which removes all items not directly referenced. The following steps are necessary:

1. Modify the build system to use the following compiler and linker flags:

CFLAGS += -ffunction-sections -fdata-sections LDFLAGS += -WI,--gc-sections

2. Review all linker command files and ensure that linker sets and global constructor sections are not affected by the garbage collection (e.g. use the KEEP() directive of GNU ld).

5 years ago	fixed	unspecified	Sebastian Huber	Sebastian Huber	3 months ago
Linker set based initi	alization				

Linker sets are used for example in Linux, FreeBSD (they are used in the RTEMS port of the FreeBSD network stack, e.g. libbsd), eCos and for global C++ constructors. They provide a space efficient and flexible means to initialize modules. A linker set consists of

dedicated input sections for the linker (e.g. .ctors) and .ctors.* in the case of global constructors),
a begin marker (e.g. provided by crtbegin.o, and

- an end marker (e.g. provided by ctrend.o).

A module may place a certain data item into the dedicated input section. The linker will collect all such data items in this section and creates a begin and end marker. The initialization code can then use the begin and end markers to find all the collected data items (e.g. function pointers).

Lets look how this works using a simple example. For this we need three files myset.h.

#ifndef MYSET_H #define MYSET H /* The linker set items */ typedef struct {
 void (*func) (void); } item; * Macro to create a linker set item. The first parameter is * the designator of the item. It must be unique within the * module scope. The second parameter is the desired function. #define MYSET_TTEM(i, f) \
 __attribute__((used)) \
 __attribute__((section(".rtemsroset.myset.content"))) \
 static item i = { f }

#endif /* MYSET H */

module.c

```
#include "myset.h"
#include <stdio.h>
 * Some global function that needs a module specific
* intialization done by f().
void
g(void)
          printf("g() \n");
static void
f(void)
         printf("f() \n");
```

MYSET ITEM(i, &f);

and init.c.

Descripti

on

#include "myset.h #include <stddef.h> Should be in a proper header file */ void g(void); _attribute__((used)) ((section(".rtemsroset.myset.begin"))) attribute static volatile const item begin[0]; Define the end marker ' __attribute__((used)) __attribute__((section(".rtemsroset.myset.end"))) static volatile const item end[0]; int main (void) size_t n = &end[0] - &begin[0]; size_t i; /* Call all functions of the linker set */ for (i = 0; i < n; ++i) { (*begin[i].func)(); } * This will pull in the module.c and register its item in the * linker set "myset". So g() can rely on f() being called first. g(); return (0);

In the linker command file of the GNU linker we need the following statement.

.rtemsroset : KEEP (*(SORT(.rtemsroset.*)))

The KEEP() ensures that a garbage collection by the linker will not discard the content of this section. This would be normally the case since the linker set items are not referenced

directly. The <u>sort()</u> directive sorts the input sections lexicographically. Please note the lexicographical order of the <u>.begin</u>, <u>.content</u> and <u>.end</u> section name parts in the previous example which ensures that the position of the begin and end markers are right. The interesting part of linker map file of the previous example may look like this.

.rtemsroset 0x0000000001001990	0x4 load address 0x0000000002268c
(SORT(.rtemsroset.))	
.rtemsroset.myset.begin	
0x000000001001990	0x0 init.o
.rtemsroset.myset.content	
0x000000001001990	0x4 module.o
.rtemsroset.myset.end	
0×000000001001994	0x0 init.o

So what is the benefit of using linker sets to initialize modules? Currently in RTEMS all available managers (semaphore, message queue, barrier, etc.) are initialized since the initialization code doesn't know what is actually used by the application. With the linker set approach we need to initialize only those managers that are used by the application. In case an application uses message queues, then it must call rtems_message_queue_create(). In the module implementing this function we can place a linker set item and register the message queue handler constructor. Otherwise, in case the application doesn't use message queues, then there will be no reference to the rtems_message_queue_create() function and the constructor is not registered, thus nothing of the message queue handler will be in the final executable.

#2412	5 years ago	fixed	score	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Improved priority inf	neritance impleme	ntation			

Problem

The RTEMS mutexes implement only a very simple approximation of the priority inheritance protocol. The real priority of a thread is only restored once it releases its last mutex. Lets consider this scenario. We have a file system instance protected by one mutex (e.g. JFFS2) and a dynamic memory allocator protected by another mutex. A low priority thread performs writes some log data into a file, thus it acquires the file system instance mutex. The file system allocates dynamic memory. Now a high priority thread interrupts and tries to allocate dynamic memory. The allocator mutex is already owned, so the priority of the low priority thread is raised to the priority of the high priority thread. The memory allocator completes and the allocator mutex is released, since the low priority thread still owns the file system instance mutex is continues to execute with the high priority (the high priority thread is not scheduled). It may now perform complex and long file system operations (e.g. garbage collection, polled flash erase and write functions) with a high priority.

Functional requirements

- The mutex shall use the priority inheritance protocol to prevent priority inversion. On SMP configurations OMIP shall be used.
- The mutex shall allow vertical nesting (a thread owns multiple mutexes).
- The mutex shall allow horizontal nesting (a thread waits for ownership of a mutex those owner waits for ownership of a mutex, and so on).
- Threads from one scheduler instance shall wait in priority order. The highest priority thread shall be dequeued first.
- The highest priority waiting thread of each scheduler instance shall wait in FIFO order.
- The mutex shall provide an acquire operation with timeout.
- In case a mutex is released, then the previous owner shall no longer use the priorities inherited by this mutex.
- In case a mutex acquire operation timeout occurs, then the current owner of the mutex shall no longer use the priorities inherited by the acquiring thread.
- The order of the mutex release operations may differ from the order of the mutex acquire operations.
- Priority changes not originating due to the priority inheritance protocol shall take place immediately.
- Deadlock shall be detected. In case a deadlock would occur an error status shall be returned or a fatal error shall be generated.
- Deadlocks at application level shall not lead to a deadlock at operating system level.

Performance requirements

- The mutex acquire operation shall use only object-specific locks in case the mutex is not owned currently.
- The mutex release operation shall use only object-specific locks in case no threads wait for ownership of this mutex.

Invariants

Descripti

- A mutex shall be owned by at most one thread.
- A thread shall wait for ownership of at most one mutex.

Possible implementation

Use a recursive data structure to determine the highest priority available to a thread for each scheduler instance, e.g.

typeder struct inread_Priority_node {
Priority_Control current_priority;
Priority_Control real_priority;
<pre>struct Thread_Priority_node *owner;</pre>
RBTree_Node Node;
RBTree_Control Inherited_priorities;
} Thread_Priority_node;
typedef struct {

Thread_Priority_node *priority_nodes; /* One per scheduler instances */

} Thread Control;

Initially a thread has a priority node reflecting its real priority. The Thread_Priority_node::owner is NULL. The Thread_Priority_node::current_priority is set to the real priority. The Thread_Priority_node::Inherited_priorities is empty.

In case the thread must wait for ownership of a mutex, then it enqueues its priority node in Thread_Priority_node::Inherited_priorities of the mutex owner.

In case the thread is dequeued from the wait queue of a mutex, then it dequeues its priority node in Thread_Priority_node::Inherited_priorities of the previous mutex owner (ownership transfer) or the current mutex owner (acquire timeout).

In case the minimum of Thread_Priority_node::real_priority and Thread_Priority_node::Inherited_priorities changes, then Thread_Priority_node::current_priority is updated. In case the Thread_Priority_node::owner its not NULL, the priority change propagates to the owner, and so on. In case Thread_Priority_node::current_priority changes, the corresponding scheduler is notified.

The biggest issue is the locking on SMP configurations in case of recursive minimum updates.

Somehow we must connect this to the scheduler helping protocol for OMIP. We may have to replace the return value based scheduler operations with a pre-context-switch action. Due to some recent implementation changes the run-time of the _Thread_Dispatch() function is no longer average-case performance critical.

	#2420	5 years ago	wontfix	tool/rsb	Jakob Viketoft	Chris Johns	5 months ago
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RTEMS 5.1 Release Notes Summar RSB %source file fails

Descripti					ich in the "manual", but it also fails to find it. Looking at actual file/directory using that the entire url (including fil	
on	This unfortunately path.isdir(url)" in tl			ython to correct this, althoug	h it appears that the "file://" part should be cut from the	e URL before calling the "return
2423	5 years ago	fixed	unspecified	Jeffrey Hill		2 years ago
ummar	rtems_iterate_over	_all_threads lack	ks user callback private	pointer pass through		
y escripti on	private state inside them using the sam the same way as a An enhanced versio void rtems_iterate_ typedef void (*rten	e of his callback w ne method with r virtual method ir on of the API mig _over_all_threads ns_per_thread_re	vithout being forced to rtems_iterate_over_all_ n C++, but nevertheles pht look like this. s_xxx(rtems_per_thread_coutine_xxx)(Thread_Coutine_xxx)	employ a global variable. A <u>c</u> threads. This type of "void * is retaining a compatible C b ad_routine routine, void * co ontrol *the_thread, void * co	nst pUserPrivatePassThrogh); onst pUserPrivatePassThrogh);	e objects instances created each of ng the users callback to behave mu
	user's callback.	-		s retained for the duration of	the rtems_iterate_over_all_threads_xxx function only set	o that it can be passed through to t
2428	thanks for your cor 5 years ago	nsideration of this fixed	s matter tool/rsb	Joel Sherrill	Chris Johns	2 years ago
ummar	Add 4.12 Tool Targ			Joer Sherrin	Ciriis Joinis	2 years ago
у		5				
escripti	Even though so far	the 4.11 and ma	aster tools don't need to	o be different, 4.12 configura	tions need to be added. This gives us space to:	
on	+ (DONE) remove	obsolete targets	(avr, h8300, m32r) + ((DONE) update versions + co	mplete submission of patches and bump gdb +	
2441	4 years ago	fixed	unspecified	Joel Sherrill	Sebastian Huber	2 years ago
ummar	lpc1768 variants fa	il to build with er	rror in gpio.c			
У	arm-lpc1768 mbed	d ahb ram eth a	arm-lpc1768 mbed ah	ram arm-lpc1768mbed		
escripti on +2442 ummar y +2443 ummar y escripti	//./././/pc: #error "Inva ///././lpc: #if GPIO_LA ////././ static lpc176 /////./ LPC176X_RI /////./ const lpc176 4 years ago Remove avrtest BS Remove the avr/av 4 years ago Remove the avr/av	1768_mbed_ahb_ alid BSP_GPIO_PI 1768_mbed_ahb_ AST_BANK_PINS rtems/c/src/lib/lil 6x_registered_int rtems/c/src/lib/lil ESERVED_ISR_FL rtems/c/src/lib/lil 6x_gpio_direction fixed fixed tectural Port ort per the instru- emoved before th	_ram_eth/lib/include/b: IN_COUNT or BSP_GPI _ram_eth/lib/include/b: > 0 bbsp/arm/lpc176x/gpic terrupt_function functio bbsp/arm/lpc176x/gpic uNCT_SIZE]; bbsp/arm/lpc176x/gpic n dir bsps e instructions at https://	O_PINS_PER_BANK." sp/gpio.h:41:5: error: division //gpio.c:29:8: error: unknow on_vector[//gpio.c:30:3: error: 'LPC176 //gpio.c:35:9: error: unknow Joel Sherrill /devel.rtems.org/wiki/Developer/Ref n be removed. These are trace	n type name 'lpc176x_registered_interrupt_function' X_RESERVED_ISR_FUNCT_SIZE' undeclared here (not ir n type name 'lpc176x_gpio_direction' joel.sherrill@ pper/Removing_a_BSP Joel Sherrill <joel@> emoving_a_Port. cked by the following tickets:</joel@>	n a function) 2 years ago 2 years ago
on	of GCC for this targ avr-rtems the only	get is poor. It is n user of AVR+nev	marginally maintained. wlib. The target size is a	Atmel maintains their own pa a challenge but that was why	large enough to run RTEMS. This by itself is not enough t atch set independent of GCC. Plus they use their own sm t the port was initially interesting. It provided a real goal bins it for CC or PTEMS.	all (and unique) C Library. This ma
	of GCC for this targ avr-rtems the only	get is poor. It is n user of AVR+nev	marginally maintained. wlib. The target size is a	Atmel maintains their own pa	atch set independent of GCC. Plus they use their own sm v the port was initially interesting. It provided a real goal	all (and unique) C Library. This ma
2444	of GCC for this targ avr-rtems the only port which is incom	get is poor. It is n user of AVR+nev pplete and has ne fixed	marginally maintained. wlib. The target size is either users nor anyone	Atmel maintains their own pa a challenge but that was why interested in actively mainta	atch set independent of GCC. Plus they use their own sm v the port was initially interesting. It provided a real goal aining it for GCC or RTEMS.	all (and unique) C Library. This ma . But the tool state is painful for a
2444	of GCC for this targ avr-rtems the only port which is incom 4 years ago Remove m68k/mvr	get is poor. It is n user of AVR+nev pplete and has ne fixed me136 BSP	marginally maintained. wlib. The target size is a either users nor anyone arch/m68k	Atmel maintains their own pa a challenge but that was why interested in actively mainta Joel Sherrill	atch set independent of GCC. Plus they use their own sm / the port was initially interesting. It provided a real goal aning it for GCC or RTEMS. Aun-Ali Zaidi <admin@></admin@>	all (and unique) C Library. This ma . But the tool state is painful for a
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2444 Jummar y escripti on 2445 Jummar y escripti Jummar y escripti	of GCC for this targ avr-rtems the only port which is incom 4 years ago Remove m68k/mvr Remove the m68k/ Rationale: Although It is unlikely to be 4 years ago Remove m68k/simu Remove the m68k/ Rationale: This is a Although a decent 4 years ago Remove M32R Arch Remove the M32R All BSPa must be re #2447 - m32rsim	get is poor. It is n user of AVR+new nplete and has ne fixed me136 BSP /mvme136 BSP /mvme136 BSP pr h it is sad to see I available and with fixed 68000 BSP /sim68000 BSP /sim68000 BSP pr b BSP for a simula tool, it was extrem fixed nitectural Port port per the instr emoved before the 2R port is incomple fixed	marginally maintained. wilib. The target size is a either users nor anyone arch/m68k ber the instructions at h the BSP for the board t hout a NIC, isn't that u arch/m68k er the instructions at h ator named BSVC (http mely slow. unspecified ructions at https://deve he architectural port car	Atmel maintains their own p a challenge but that was why interested in actively mainta Joel Sherrill ttps://devel.rtems.org/wiki/f abar RTEMS was developed for seful. Joel Sherrill ttps://devel.rtems.org/wiki/f ttps://devel.rtems.org/wiki/f Joel Sherrill al.rtems.org/wiki/Developer// n be removed. These are trace	atch set independent of GCC. Plus they use their own sm the port was initially interesting. It provided a real goal aining it for GCC or RTEMS. Aun-Ali Zaidi <admin@> Developer/Removing_a_BSP or be removed, this board was current in 1988-9. It has of Aun-Ali Zaidi <admin@> Developer/Removing_a_BSP /bsvc/) that was never under a truly free license and has Joel Sherrill Removing_a_Port. cked by the following tickets:</admin@></admin@>	all (and unique) C Library. This ma . But the tool state is painful for a 2 years ago only 1MB RAM, 2 UARTS, and no N 2 years ago s not been updated in a decade.

	1 Release No		anah (mina	Joel Sherrill		17
ummar	4 years ago Remove mips/mong	fixed oose BSP	arch/mips	Joel Shernii		2 years ago
у						
	Remove the m32r/n	132rsim BSP per th	ne instructions at https	://devel.rtems.org/wiki/Develope	er/Removing_a_BSP	
escripti on					ssions. After discussions with various NASA and commercial engrable length of time. The missions still underway (including Nev	
			elopment infrastructur		able length of time. The missions still underway (including new	v Horizons) are locked
2449	4 years ago	fixed	arch/arm	Joel Sherrill	Aun-Ali Zaidi <admin@></admin@>	2 years ago
ummar	Remove arm/gba BS	βP				
У	Pomovo tho arm/ab	a RCD por the instr	ructions at https://dov	el.rtems.org/wiki/Developer/Rem	oving a RSD	
escripti						
on		ers of those cartric			cor or hard to obtain programmable game cartridge. Nintendo w comate testing. This was a useful BSP when there were few ARM	
+2450	4 years ago	fixed	arch/arm	Joel Sherrill	Aun-Ali Zaidi <admin@></admin@>	2 years ago
ummar y	Remove arm/nds					
	Remove the arm/nd	s BSP per the instr	uctions at https://dev	el.rtems.org/wiki/Developer/Rem	oving_a_BSP	
escripti on	shutting down resell	ers of those cartric			or or hard to obtain programmable game cartridge. Nintendo w	
2451	Beagle, etc. these d		arch/arm	lool Shorrill	Aun Ali Zaidi zadmin@	2 years ago
ummar	4 years ago Remove arm/gp32 E	fixed BSP	arch/arm	Joel Sherrill	Aun-Ali Zaidi <admin@></admin@>	2 years ago
У	Domovo the arm/an	22 BCD par the ine	tructions at https://da	val stama ara (wiki (Davalanar (Da	moving a PCD	
escripti				vel.rtems.org/wiki/Developer/Rei		
on					2001. Wikipedia notes that 30K units were sold but it has been with the Pi, Beagle, etc. these days are long past.	unavailable since 2007.
2452	4 years ago	fixed	unspecified	Joel Sherrill	Joel Sherrill	18 months ago
ummar	Remove H8300 Arch					
У	Dame of the second		and the second second		ing a Dat	
				.rtems.org/wiki/Developer/Remov		
	All BSPa must be re	moved before the a	architectural port can t	be removed. These are tracked by	y the following tickets:	
escripti on	#2453 - h8sim					
011					ons and tickets filed. The architecture itself has issues which le ures like iconv in newlib. With no users, end of life, and tool iss	
2453	it.	fived	hana	Joel Sherrill	Aun-Ali Zaidi <admin@></admin@>	2 100000 0000
	4 years ago Remove h8300/h8si	fixed m BSP	bsps	JUEI SHEITIN		2 years ago
ummar y	Kennove nosoo/nosi	11 051				
escripti	Remove the h8300/	h8sim BSP per the	instructions at https:/	/devel.rtems.org/wiki/Developer,	/Removing_a_BSP	
on						
2454	4 years ago	fixed	unspecified	Joel Sherrill	Sebastian Huber	2 years ago
ummar	Warning in threadqo	ps.c				
У	This may apply to th	o 4 11 branch as y	well I am not sure			
escripti						h
on			score/src/threadqops.c	::202:29: warning: passing argur	nent 1 of '_RBTree_Initialize_empty' from incompatible pointer	туре
2455	This happens buildir 4 years ago	7	unspecified	Joel Sherrill	Sebastian Huber	2 years ago
ummar	Warning in spsimple		unspecified	JOEI 311611111	Sebastian huber	2 years ago
y	franning in opointpre	56116462				
	This may apply to th	e 4.11 branch as v	well. I am not sure.			
escripti	//////rter	ns/c/src///tests	uites/sptests/spsimple	sched02/init.c:84:5: warning: pa	ssing argument 1 of '_Objects_Name_to_id_u32' from incompa	tible pointer type
on	This happens buildir					
2457	4 years ago	fixed	arch/powerpc	Joel Sherrill	Aun-Ali Zaidi <admin@></admin@>	2 years ago
ummar	Remove powerpc/ep					,
У						
escripti	Remove the powerp	c/ep1a BSP per the	e instructions at https:	//devel.rtems.org/wiki/Develope	r/Removing_a_BSP	
on					longer in active development and is completely frozen. If that	situation changes, the
3450					than general maintenance in the last four years.	2 10250 200
2458 ummar	4 years ago Remove powerpc/sc	fixed ore603e BSP	arch/powerpc	Joel Sherrill	Ralph Holmes <ralph@></ralph@>	2 years ago
ummar y						
	Remove the powerp	c/score603e BSP p	er the instructions at	nttps://devel.rtems.org/wiki/Deve	eloper/Removing_a_BSP	
escripti on	Rationale: AFAIK thi	s BSP was only us	ed on a program suppr	orted by OAR. That program is no	o longer in active development and is completely frozen. If that	situation changes, the
	BSP can be resurred	ted. It was introdu	iced in 1999 and has n	ot has a modification other than	general maintenance in the last six years.	
2459	4 years ago	fixed	rtems	Sebastian Huber	Sebastian Huber	2 years ago
ummar y	Add rtems_chain_ge	t_first_unprotecte	a() to chain API			
2464	4 years ago	wontfix	tool	Sebastian Huber		2 years ago
ummar	RSB: Tool patches u			200000001110DCI		L years ago
y						
escripti	In rtems/conifg/rter	ns-urls.bset the to	ol patches are set to a	n RTEMS version dependent direc	tory. This makes re-use of the general purpose files quite diffic	ult.
on						_
2468	4 years ago	fixed	arch/i386	Joel Sherrill	Sebastian Huber	2 years ago
ummar	Add Thread Local St	orage (TLS) suppo	rt on x86			
У	The x86 is one of th	e architectures wh	ich does not support T	bread Local Storage Each archite	ecture requires target architecture specific support to provide th	his standard language
	feature.	e arenneetures WII	acco not support 1		sectore requires target arenitecture specific support to provide ti	standard language
	Details on the imple	mentation by the o	compiler may be found	at http://wiki.osdev.org/Thread	Local Storage.	
				at http://wiki.osdev.org/Thread_		
escripti on	Based on this inform	nation, I think a se	gment register needs t	to be added to the thread context	and some hooks to the TLS implemented.	
	Based on this inform	nation, I think a se	gment register needs t	to be added to the thread context		
escripti on	Based on this inform Architecture informa	nation, I think a se ition on TLS impler	gment register needs t mentation should be ac	to be added to the thread context	and some hooks to the TLS implemented.	2 years ago

l	DTEMO					
		, ,		ebug.h>. This feature is sparel	y used:	
			ems_debug_disable(rtems debug enabl	RTEMS_DEBUG_ALL_MASK);		
			ems_debug_control			
			rtems_debug_disab			
			ems_debug_control rtems debug is en			
			ems_debug_control			
					abled(RTEMS_DEBUG_REGION)) \	
				t32_t rtems_debug_contro N rtems debug control Deb		
			ebug.h:void rtems_		ag_hever,	
				ug_control to_be_enabled		
			ebug.h:void rtems_ ebug.h: rtems deb	debug_disable(ug control to be disabled		
	cpukit/score/i	nclude/rtems/de	ebug.h:bool rtems_	debug_is_enabled(
	cpukit/score/i c/src/lib/libb		ebug.h: rtems_deb	ug_control level rtems debug enable(RTEMS	DEDUC ALL MACK).	
escripti				ebug enable (RTEMS DEBUG A		
on	c/src/lib/libb	sp/shared/incl	ude/bootcard.h: *	- rtems_debug_enable(RT	'EMS_DEBUG_ALL_MASK)	
				s("TAl = rtems_debug_disa ms_debug_disable(RTEMS_DE	able - RTEMS_DEBUG_REGION"); BUG REGION):	
	testsuites/spt	ests/spregion_	err01/init.c: put	s("TA1 - rtems_debug_enab	le - RTEMS_DEBUG_REGION");	
				ms_debug_enable(RTEMS_DEB rtems debug is enabled -		
				s debug is enabled(0x1);		
				rtems_debug_enable - set	0x1");	
			.c: rtems_debug_e	nable(0x1); rtems_debug_is_enabled -	is 0x1 set? Yes"):	
				s_debug_is_enabled(0x1);		
				<pre>rtems_debug_disable - cle isable(0x1);</pre>	ar 0x1");	
			.c: rtems_debug_d .c: puts("Init -	rtems_debug_is_enabled -	is 0x1 set? No");	
				s_debug_is_enabled(0x1);		
				case RTEMS_DEBUG is defined.	Due to the heap protection support which is als	so available in case RTEMS_DEBUG is defined
	the expensive heap v		uous. o simplify the code ba	99		
	4 years ago	fixed	tool/website	Joel Sherrill	Chris Johns	2 years ago
			TBR/Delete/SpecBuild			
у						
	Chris you are the o	nly one who know	ws if this tool is obsol	ete or not. Please do what you	think is right with this page.	
on 2488 4	4 years ago	fixed	unspecified	Joel Sherrill	Ben Gras	2 years ago
	Vagrant Scripts	lixeu	unspecified	Joer Sherrin	Dell Glas	2 years ago
у						
escripti E	Ben someone poste	d on IRC that the	ey used your scripts b	out the clones point to your per	rsonal repos which are out of date I am guessing	g.
on A	Also is this discussed	l anywhere on th	e RTEMS wiki?			
2490 4	4 years ago	wontfix	tool/rsb	Sebastian Huber	Chris Johns	2 years ago
ummar F	RSB: Use SHA512 in	stead of MD5				
У						
escripti S	Since MD5 is not a s	ecure hash algori	ithm, we should chang	ge all hashes used by the RSB	configuration files to use SHA512.	
on						
	4 years ago	fixed	unspecified	Sebastian Huber	Joel Sherrill <joel.sherrill@></joel.sherrill@>	2 years ago
	Remove notepads					
y escripti ľ	Notenads were mark	ed as obsolete in	# 2265 Next sten is	to remove them. Documentation	on should mention that notepads are removed a	an list the alternatives e.g. POSIX keys or
	thread local storage.					
• 2494 4	4 years ago	fixed	unspecified	Sebastian Huber		2 years ago
ummar F	Remove task variable	es				/
						,
У						
escripti ľ	Notepads were mark thread local storage.		4.11. Next step is to	remove them. Documentation	should mention that notepads are removed and	
escripti f on t			a 4.11. Next step is to arch/powerpc	remove them. Documentation Aun-Ali Zaidi	should mention that notepads are removed and joel.sherrill@	
escripti r on t 2503 4 ummar r	thread local storage. 4 years ago	fixed		Aun-Ali Zaidi		d list the alternatives, e.g. POSIX keys or
escripti f on t 2503 4 ummar r y	thread local storage. 4 years ago mvme5500 BSP: Exc	fixed Ception Handler u	arch/powerpc ises deprecated Notep	Aun-Ali Zaidi vads.	joel.sherrill@	d list the alternatives, e.g. POSIX keys or 2 years ago
escripti f on t 2503 4 ummar r y escripti 1	thread local storage. 4 years ago mvme5500 BSP: Exc	fixed Ception Handler u	arch/powerpc ises deprecated Notep	Aun-Ali Zaidi vads.		d list the alternatives, e.g. POSIX keys or 2 years ago
escripti t on t 2503 4 ummar y escripti 1 on	thread local storage. 4 years ago mvme5500 BSP: Exc The MVME5500 BSP	fixed ception Handler u uses Notepads in	arch/powerpc uses deprecated Notep n its exception handler	Aun-Ali Zaidi vads. r and #2493 removes them. Th	joel.sherrill@	d list the alternatives, e.g. POSIX keys or 2 years ago ite.
escripti t 2503 4 ummar r y escripti 7 on 2509 4	thread local storage. 4 years ago mvme5500 BSP: Exc The MVME5500 BSP 4 years ago	fixed ception Handler u uses Notepads in fixed	arch/powerpc uses deprecated Notep n its exception handler tool/website	Aun-Ali Zaidi vads. r and #2493 removes them. Th Santosh Vattam	joel.sherrill@	d list the alternatives, e.g. POSIX keys or 2 years ago
escripti t on t 2503 4 ummar y escripti 1 on 2509 4 ummar 5	thread local storage. 4 years ago mvme5500 BSP: Exc The MVME5500 BSP 4 years ago	fixed ception Handler u uses Notepads in fixed	arch/powerpc uses deprecated Notep n its exception handler	Aun-Ali Zaidi vads. r and #2493 removes them. Th Santosh Vattam	joel.sherrill@	d list the alternatives, e.g. POSIX keys or 2 years ago ite.
escripti t on t 2503 4 ummar y escripti 1 on 2509 4 ummar y	thread local storage. 4 years ago mvme5500 BSP: Exc The MVME5500 BSP 4 years ago Should "https://deve	fixed ception Handler u uses Notepads in fixed el.rtems.org/wiki/	arch/powerpc uses deprecated Notep n its exception handler tool/website /TBR/Delete/BSP_Tem	Aun-Ali Zaidi pads. r and #2493 removes them. Th Santosh Vattam aplate" be replaced?	joel.sherrill@ nis is obviously not portable and requires a rewri Joel Sherrill	d list the alternatives, e.g. POSIX keys or 2 years ago ite. 2 years ago
escripti n on t 2503 4 ummar r y escripti n on 2509 4 ummar s y escripti a	thread local storage. 4 years ago mvme5500 BSP: Exc The MVME5500 BSP 4 years ago Should "https://deve As part of the GCI Ta	fixed ception Handler u uses Notepads in fixed cl.rtems.org/wiki/ cask https://codeir	arch/powerpc uses deprecated Notep h its exception handler tool/website /TBR/Delete/BSP_Terr n.withgoogle.com/das	Aun-Ali Zaidi aads. r and #2493 removes them. Th Santosh Vattam nplate" be replaced?	joel.sherrill@ is is obviously not portable and requires a rewri Joel Sherrill 33810781184/?sp-page=1, the student has crea	d list the alternatives, e.g. POSIX keys or 2 years ago ite. 2 years ago ated a new page with a corrected template
escripti n 2503 4 ummar n y escripti n y 2509 4 ummar s y escripti 4 escripti 4	thread local storage. 4 years ago mvme5500 BSP: Exc The MVME5500 BSP 4 years ago Should "https://deve As part of the GCI Ta	fixed ception Handler u uses Notepads in fixed cl.rtems.org/wiki/ cask https://codeir	arch/powerpc uses deprecated Notep h its exception handler tool/website /TBR/Delete/BSP_Terr n.withgoogle.com/das	Aun-Ali Zaidi aads. r and #2493 removes them. Th Santosh Vattam nplate" be replaced?	joel.sherrill@ nis is obviously not portable and requires a rewri Joel Sherrill	d list the alternatives, e.g. POSIX keys or 2 years ago ite. 2 years ago ated a new page with a corrected template
escripti on t til til til til til til til til til til	thread local storage. 4 years ago mvme5500 BSP: Exc The MVME5500 BSP 4 years ago Should "https://deve As part of the GCI Te and placed it under "	fixed ception Handler u uses Notepads in fixed cl.rtems.org/wiki/ cask https://codeir	arch/powerpc uses deprecated Notep h its exception handler tool/website /TBR/Delete/BSP_Terr n.withgoogle.com/das	Aun-Ali Zaidi aads. r and #2493 removes them. Th Santosh Vattam nplate" be replaced?	joel.sherrill@ is is obviously not portable and requires a rewri Joel Sherrill 33810781184/?sp-page=1, the student has crea	d list the alternatives, e.g. POSIX keys or 2 years ago ite. 2 years ago ated a new page with a corrected template
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#2536	4 years ago	fixed	tool/rsb	Sebastian Huber	Chris Johns <chrisj@></chrisj@>	2 years ago
ummar	RSB allows use of in	secure hash alg	orithms like MD5 and S	SHA1		
y	Support for these ba	ashes should be	removed. Hashes shou	Id he mandatory		
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*2537	4 years ago	fixed	unspecified	Joel Sherrill	Joel Sherrill	2 years ago
ummar	Use Newlib exec*()	variants and ren	nove RTEMS versions			
У	In reviewing RTEMS	+newlih POSIX	conformance. I noticed	that our newlib configuration (does include the exec*() variants. All of these call _	execve() which we already provided
escripti on					the user is still to return ENOSYS.	
	4 years ago	fixed	bsps	Joel Sherrill	joel.sherrill@	2 years ago
ummar	Review cxx_iostrear	n size change pe	er function-section cha	nges		
	that the KEEP() sect	tion requirement	ts in the linker scripts v	vere not correct and it was mis	rs to be 50% while some went to 75-80%. A secon sed. hes from this one back in time for cxx_iostream sh	
escripti on	commit 6d21c13e50)94d490280a94	1cf0e8333f91f85715 A	uthor: Ralph Holmes <ralph@< td=""><td>.> Date: Sat Jan 23 21:15:40 2016 +0000</td><td></td></ralph@<>	.> Date: Sat Jan 23 21:15:40 2016 +0000	
	powerpc/gen	5200: Add per-s	section compilation and	l linking support.		
	For the brs5l	BSP variant:				
2543	4 years ago	fixed	unspecified	Joel Sherrill	Joel Sherrill	2 years ago
ummar	Obsolete gen68302					, , , , , , , , , , , , , , , , , , ,
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escripti on	Delete the gen6830	2 BSP arter 4.11	and before 4.12.			
	4 years ago	duplicate	unspecified	Joel Sherrill	Joel Sherrill	2 years ago
ummar	Osolete m68k/ods68	3302				
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escripti on	Obsolete and remov	e the ods68302	BSP before 4.12.			
2545	4 years ago	fixed	unspecified	Joel Sherrill	Joel Sherrill	2 years ago
ummar	Obsolete mbx8xx B	SP				
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‡2546	4 years ago	fixed	arch/m68k	Joel Sherrill	Joel Sherrill	2 years ago
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‡2553	4 years ago	fixed	arch/powerpc	Nick Withers	Sebastian Huber	2 years ago
ummar v	[mvme3100] boot_o	:ard() broken by	/ 37030e38			
у	Backtrace with 3703	30e38* (with a f	atal exception handler	installed):		
	<pre>#1 0x000acd1t//# 2 0x000alc9!//cpukit/ #3 _Terminate//# 4 0x0007ad4 at// #5 0x0005cf8//.# 6 bsp_work_i//.#6 bsp_work_i</pre>	<pre>8 in _User_ext .//rtems/ 0 in _User_ext /7/mvme3 e (the_source= .//./ttems/ 0 in RTEMS_Mal /// 4 in bsp_work .///mvme310 area_initaliz ./7/// 4 in boot_card .////./</pre>	<pre>ensions_lterate (ar 'c/src//.cpukit/ ensions_Fatal (errc l100/lib/include/rte tthe_source@entry=IN c/src//.cpukit/ loc_Initialize (are //trems/c/src/.i./. area_initialize_def 0/lib/include/bsp/r e () at 'rtems/c/src/lib/lif (ccmdline<optimize rtems/c/src/lib/lif</optimize </pre>	<pre>g-arg@entTy=0xlabG348, vis score/src/userextiterate.cr =r23, is_internal=true, sc ms/score/userextimpl.h:254 TERNAL_ERROR_CORE, is_inte score/src/interr.c:44 as=areas@entry=0xlabG398, opukit/likosupport/src/mal ault (area_size=<optimized ootcard.h:183 bsp/powerpc/mvme3100// bsp/powerpc/mvme3100//.</optimized </pre>	<pre>urce=INTERNAL_ERROR_CORE) at rnal=is_internal@entry=true, the_error=the_ area_count=area_count@entry=1, extend=exter loc_initialize.c:53 out>, area_begin=<optimized out="">) at /powerpc/shared/startup/bspgetworkarea.c:23</optimized></pre>	_error@entry=23) at nd@entry=0x0 <preload>) 3</preload>
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19/130

Background

The watchdog handler uses delta chains. The insert operation has a O(n) worst-case time complexity with n being the count of watchdogs in the delta chain. In each step of the insert operation, the SMP lock of the corresponding watchdog header is acquired and released. The profiling data obtain by test program smptests/smpwakeafter01 showed that the current implementation leads to unacceptable latencies, thus it should be replaced by something else.

The use cases for the watchdog handler fall roughly into two categories

- Timeouts used to detect if some operations needs more time than expected. Since the unexpected happens hopefully rarely, timeout timers are usually removed before they
 expire. The critical operations are insert and removal. They are important for the performance of a network stack.
- Timers used to carry out some work in the future. They usually expire and need a high resolution. An example user is a time driven scheduler, e.g. rate-monotonic or EDF.

One approach is to use a red-black tree with the expiration time as the key. This leads to O(log(n)) worst-case insert and removal operations. For each operation it is sufficient to acquire and release the lock only once. The drawback is that a 64-bit integer type must be used for the intervals to avoid a potential overflow of the key values. With a system tick Descripti interval of 1ns the system could run more than 500 years before an overflow happens. The EDF scheduler would also profit from a 64-bit interval representation, see #2173.

An alternative is the use of a timer wheel based algorithm which is used in Linux and FreeBSD for example. A timer wheel based algorithm offers O(1) worst-case time complexity for insert and removal operations. The drawback is that the run-time of the watchdog tick procedure is somewhat unpredictable due to the use of a hash table or cascading.

Which approach should we choose? Since the watchdog serves the timeout and timer services in RTEMS we have to make some trade-offs. We recommend to use the red-black tree approach which offers a more predictable run-time behaviour and sacrifice the constant insert and removal operations offered by the timer wheel algorithms, see also https://www.kernel.org/doc/ols/2006/ols/2006v1-pages-333-346.pdf. We can reuse the red-black tree support already used for the thread priority queues.

The new watchdog handler implementation is a prerequisite to eliminate the Giant lock in the Classic Timer manager.

Implementation

Change the <u>Watchdog_Ticks_since_boot</u> to a 64-bit integer type. Keep the <u>Watchdog_Interval</u> at 32-bit for backward compatibility. Replace the delta chains with a red-black tree. Use the ticks for timers with a relative expiration time. Use <u>struct timespec</u> or <u>struct bintime</u> for timers with an absolute expiration time. This has the benefit that we do not have to adjust the data structures in case the absolute time changes, e.g. due to NTP. It simplifies the POSIX timer services, since no conversion to ticks is necessary.

#2555	4 years ago	fixed	score	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Eliminate the Giant lo	ock				

Background

The standard approach to turn a uni-processor operating system into an SMP-capable operating system is to encapsulate virtually the complete operating system state and protect it by one global recursive lock - the Giant lock. Thus, at most one processor can modify the operating system state at a time. Under Giant lock protection interrupt disable/enable critical sections still work. This approach is easy to realize and leads to something that runs on SMP with a minimal effort. Unfortunately, such an operating system does not scale with the processor count and offers very poor performance. It is quite useless for real applications.

The first steps to get rid of the Giant lock are tackled with the introduction of fine grained locking for the scheduler, watchdog handler, timestamps, thread queues, events, semaphores and message queues. The Giant lock is still used in a couple of places, e.g. all other objects using thread queues, thread life cycle changes (termination, restart) and scheduler changes. It is a straight forward task to eliminate it entirely, but it is also somewhat labour intensive since a lot of code must be changed.

Implementation

Eliminate all remaining code areas that use

- _ISR_Disable(),
- _ISR_Enable(), and
- ISR Flash().

Direct users of these macros are

- Chain_Append()
- Chain_Extract()
- _Chain_Get(),
- Chain_Insert(), and

Chain_Prepend()

Each spot must be dealt with individually. Once this is done, delete these macros since they are now superfluous. Rename [ISR_Disable_without_giant()] into [ISR_Locale_disable()]. Rename [ISR_Enable_without_giant()] into [ISR_Locale_enable()].

Eliminate all remaining code areas that use

Descripti

on

_Thread_Disable_dispatch() and
 _Thread_Enable_dispatch().

A prominent user of this functions is __Objects Get(). The following components are affected by these functions

- Classic barrier handler,
- Classic dual-ported memory handler,
- Classic message queue handler,
- Classic partition handler
- Classic rate-monotonic handler,
- Classic region handler,Classic semaphore handler,
- Classic timer handler,
- extension handler.
- IO manager,
- multi-processing management,
- objects management,
- POSIX barrier handler,
- POSIX condition handler,
- POSIX key handler,
 POSIX message queue handler.
- POSIX mutex handler,
- POSIX rwlock handler.
- POSIX semaphore handler,
- POSIX spinlock handler,
- POSIX timer handler,
- signals,
- thread cancellation,thread life-cycle changes, and
- thread scheduler changes.

Once this is done, delete Thread_Disable_dispatch () and Thread_Enable_dispatch (). As a side-effect the Giant lock will be removed.

 #2556
 4 years ago
 fixed
 score
 Sebastian Huber
 Sebastian Huber
 2 years ago

 Summar
 Implement the O(m) Independence-Preserving Protocol (OMIP)

Background

The O(m) Independence-Preserving Protocol (OMIP) is a generalization of the priority inheritance protocol to clustered scheduling which avoids the non-preemptive sections present with priority boosting. The m denotes the number of processors in the system. Its implementation requires an extension of the scheduler helping protocol already used for the MrsP semaphores. However, the current implementation of the scheduler helping protocol has two major issues, see Catellani, Sebastiano, Luca Bonato, Sebastian Huber, and Enrico Mezzetti: Challenges in the Imple- mentation of MrsP. In Reliable Software Technologies - Ada-Europe 2015, pages 179–195, 2015. Firstly, the run-time of some scheduler helping protocol in case an owner of a shared resource is somehow involved.

To illustrate the second issue, let us look at the following example. We have a system with eight processors and two L2 caches. We assign processor 0 to a partition P for latency sensitive real-time tasks (e.g. sensor and actuator handling), processors 1, 2 and 3 are assigned to a cluster C_A and the remaining processors are assigned to a cluster G_b for soft real-time worker tasks. The worker tasks use a shared resource, e.g. a file system for data storage. Let us suppose a task R of partition P sends a message to the workers. This may make a waiting worker ready, which in turn pre-empts the owner of a shared resource. In this case the scheduler helping protocol takes action and is carried out by the task R. This contradicts the intended isolation of scheduler instances.

The reason for this unfortunate coupling is a design issue of the scheduler helping protocol implementation. Some scheduler operations may return a thread in need of help. For example, if a thread is unblocked which pre-empts an owner of a shared resource, then the pre-empted thread is returned. Once a thread in need of help is returned, the ask for help operation of the scheduler is executed. An alternative to this return value based approach is the introduction of a pre-emption intervention during thread dispatching. Threads taking part in the scheduler helping protocol indicate this with a positive resource count value. In case a thread dispatch occurs and pre-empts an owner of a shared resource, the scheduler ask for help operation is invoked. So, the work is carried out on behalf of the thread which takes part in the scheduler helping protocol.

To overcome the first issue, an improved resource dependency tracking is required. One approach is to use a recursive red-black tree based data structure, see #2412.

Implementation

There are several steps necessary to implement OMIP.

- Introduce per-scheduler locks.
- Enable context switches with interrupts enabled.
- Add a pre-emption intervention to the thread dispatch.
 Add a table for priority nodes to the thread control block. For each scheduler instance there is one priority node.
- Update table to priority nodes to thread blocks on a resource, a timeout while waiting for a resource priority node.
- Use this table in the pre-emption intervention.
- Update the MrsP implementation to the new infrastructure.

Currently, only one scheduler lock for all scheduler instances is used. This simplified the MrsP implementation and due to the presence of a Giant lock, this was not an issue. With the elimination of the Giant lock, however, we need one scheduler lock per scheduler instance to really profit from a decoupled system due to clustered scheduling.

The current implementation of thread dispatching has some implications with respect to the interrupt latency. It is crucial to preserve the system invariant that a thread can execute on at most one processor in the system at a time. This is accomplished with a boolean indicator in the thread context. The processor architecture specific context switch code will mark that a thread context is no longer executing and waits that the heir context stopped execution before it restores the heir context and resumes execution of the heir thread (the boolean indicator is basically a TTAS lock). So, there is one point in time in which a processor is without a thread. This is essential to avoid cyclic dependencies in case multiple threads migrate at once. Otherwise some supervising entity is necessary to prevent deadlocks. Such a global supervisor would lead to scalability problems so this approach is not used. Currently the context switch for another processor to make progress.

If we add pre-emption intervention to the thread dispatch sequence, then there is an even greater need to avoid this issue with the interrupt latency. Interrupts normally store the context of the interrupted thread on its stack. In case a thread is marked as not executing, we must not use its thread stack to store such an interrupt context. We cannot use the heir stack before it stopped execution on another processor. If we enable interrupts during this transition, then we have to provide an alternative thread independent stack for interrupts in this time frame.

Descripti The pre-emption intervention should be added to Thread_Do_dispatch () before the heir is read and perform the following pseudo-code actions.

F	re_emption_intervention(executing): if executing.resource count > 0:
	executing.lock()
	if executing.is_ready():
	for scheduler in executing.schedulers:
	scheduler.lock()
	if !executing.is_scheduled():
	for scheduler in executing.schedulers:
	scheduler.ask_for_help(executing) for scheduler in executing.schedulers:
	scheduler,unlock()
	else if executing, active help level > 0:
	idle.use(executing.scheduler_node)
	executing.unlock()
	cheduler help operation affects multiple scheduler instances. In terms of locking we have only two options, use a global scheduler lock, or
•	obtain multiple per-scheduler locks at once.
	bal scheduler lock is not an option. To avoid deadlocks obtain the per-scheduler locks in a fixed order. However, in this case the per-scheduler locks will observe different worst-
	and average-case acquire times (depending on the order).
Jse a	recursive data structure to determine the highest priority available to a thread for each scheduler instance, e.g.
t	ypedef struct Thread_Priority_node {
	Priority_Control current_priority;
	Priority_Control real_priority;
	struct Thread Priority_node *owner;
	RBTree_Node Node; RBTree Control Inherited priorities;
3	Thread Priority node;
1	
t	ypedef struct {
	 Thread Priority node *priority nodes; /* One per scheduler instances */
	Interargeneoutry_houes, /- one per scheduler instances -/
3	Thread Control;
	_
	Ily a thread has a priority node reflecting its real priority. The [Thread_Priority_node::owner] is [NULL]. The [Thread_Priority_node::current_priority] is set to the real
51101	ty. The <pre>[Thread_Priority_node::Inherited_priorities]</pre> is empty.
In ca	se the thread must wait for ownership of a mutex, then it enqueues its priority node in Thread Priority node::Inherited priorities of the mutex owner.
	se the thread is dequeued from the wait queue of a mutex, then it dequeues its priority node in <u>Thread_Priority_node::Inherited_priorities</u> of the previous mutex owner
(own	ership transfer) or the current mutex owner (acquire timeout).
	se the minimum of the Thread Priority node::real priority and the Thread Priority node::Inherited priorities changes, then
In ca	
Thre	ad_Priority_node::current_priority is updated. In case the Thread_Priority_node::owner its not NULL, the priority change propagates to the owner, and so on. In
Thre	
Thre	ad_Priority_node::current_priority is updated. In case the Thread_Priority_node::owner its not NULL, the priority change propagates to the owner, and so on. In

#2557	4 years ago	fixed	doc	Amar Takhar	Amar Takhar	2 years ago
Summar	Add word splitting to	print output				
У						
#2559	4 years ago	fixed	score	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Delete the EXTERN p	attern				

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ize, uintptr_t alignment, uintptr_t boundary	Th		Illocate aligned with				
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					the year 2038 problem. There are only 22 years	left and this time span is within the
	realistic time frame					
			_t in Newlib. To mak	e sure that all integer operation	ons are carried out properly I suggest to temporar	ily do this
	{{{#include <sys _<="" th=""><th>stdint.h></th><th></th><th></th><th></th><th></th></sys>	stdint.h>				
	typedef struct {					
	int64_t _val;					
	} time_t;					
	static inline time_t _					
		aval + bval };	return r;			
Descripti on						
	static inline time_t _					
		aval - bval }; r	eturn r;			
	}					
	static inline time_t _					
		aval * bval };	return r;			
	}					
	static inline time_t _					
		aval / bval }; r	eturn r;			
	<pre>} }}</pre>					
#2625	4 years ago	fixed	posix	cases to highlight the time_t i Sebastian Huber	Sebastian Huber	2 years ago
Summar	Use one lookup tree	per-thread for the	POSIX keys			
y Descripti	Currently a global lo	okup tree is used f	or all the POSIX key	/value pairs. On SMP configura	ations this is a bottleneck. Use one lookup tree pe	r thread instead.
on #2626	4 years ago	fixed	score	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Unify thread cancel/	1	000.0			2 ,000 090
У	The stores task dal		nthroad cancel() al	anthroad icin() Unify the im	plementation and introduce a Thread Cancel()	and Thread loin() to be used by both
Descripti on	APIs. Get rid of the			is pthread_join(). Unity the im	plementation and introduce a _Thread_Cancel() a	ind _Inread_Join() to be used by both
#2627	4 years ago	fixed	score	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Fix CPU time used for	or threads on SMP				
У	The CPU time used o	of a thread is curre	ntly maintained per-	processor mostly during _Thre	ad_Dispatch(). However, on SMP configurations t	he actual processor of a thread is difficult
Descripti on					cessor interrupts). Only the intended processor of operations. Provide a function to get the CPU usage	
#2628	4 years ago	wontfix	score	Sebastian Huber	Sebastian Huber	2 years ago
Summar v	Avoid home-grown o	ondition variable in	mplementation in the	e Classic Regions		
, Descripti	5	nanager enables u	sers to wait until me	mory is available to satisfy an	allocation request. This is done through special p	urpose code that basically implements a
on	condition variable.		1			-
#2631 Summar	4 years ago Use an ISR lock to p	fixed	Score	Sebastian Huber	Sebastian Huber	2 years ago
y						
Descripti on	The state of Classic	Rate Monotonic is	currently protected b	y the Giant lock and ISR disat	le sections. Use a per-object ISR lock to protect s	state changes instead.
	4 years ago	fixed	tool/rsb	Joel Sherrill	Chris Johns	2 years ago
Summar	rtems-tester failure					
У	CentOS 7 on master					
			me-toole-/home/io	al/rtems-4 11-work/tools/4 12	rtems-bsp=sis findname "*hello.exe"	DTEMS Testing - Tester 4 12
	(a5d243d3f8e2)	er/items test - ite		A TELLS 4.11 WORK (0013) 4.12	-items bap-ais findname inerto.exe	TEND resting - rester, 4.12
					4.11-work/tools/4.12rtems-bsp=sis ./sparc-	
					2015, 02:00:19) [GCC 4.8.5 20150623 (Red Hat	.4.8.5-4)]
	[1/1] p:0 f:0 t:0 i:0					
Descripti on			s-test", line 40, in <r< td=""><td>nodule></td><td></td><td></td></r<>	nodule>		
	rt.test		let some de fitte son de la fitte			
			11-work/rtems-tools/	'tester/rt/test.py", line 287, in	run	
	tst.rer		let some de fitte son de la fitte			
				'tester/rt/test.py", line 123, in	reraise	
			result[1], self.result	[2])		
#2633	TypeError?: <u>init()</u> tal 4 years ago	kes exactly 2 argur fixed	network/legacy	joguin	Sebastian Huber	2 years ago
	waf build failed for r		,			2 70015 490
Summar		terns noosu				

			g with waf. Here is t			
	[488/845] Compiling rtems4.12/pc386/lib				sh/development/rtems/bsps/4.12/i386-	
	from//free	ebsd/sys/kern/s	ubr_sleepqueue.c:9	1:		
	/home/josh/develop	ment/rtems/bsp ment/rtems/bsp	s/4.12/i386-rtems4	.12/pc386/lib/include/rtems/sco	ore/watchdogimpl.h: In function '_Watchdog_Per_(ore/watchdogimpl.h:356:18: error: 'struct <anony< td=""><td></td></anony<>	
	cpu->Watchd	log.ticks + ticks				
					ern/subr_sleepqueue.c: In function 'sleepq_set_tin ka struct <anonymous>}' has no member named</anonymous>	
	BSD_ASSERT	(executing->Tir	ner.state == WATCH	IDOG_INACTIVE);		
Descripti on	//freebsd/sys/ker	n/subr_sleepqu	eue.c:424:2: note: i	n expansion of macro 'BSD_AS	SERT'	
011	BSD_ASSERT	(executing->Tir	ner.state == WATCH	IDOG_INACTIVE); ~		
	//freebsd/sys/ker	n/subr_sleepqu	eue.c:425:2: error:	too many arguments to function	n '_Watchdog_Initialize'	
	_Watchdog_I	nitialize(&execu	ting->Timer, sleepq	_timeout, ¯		
	In file included from	/home/josh/dev	velopment/rtems/bs	ps/4.12/i386-rtems4.12/pc386,	lib/include/rtems/score/threadimpl.h:36:0,	
	from//free	ebsd/sys/kern/s	ubr_sleepqueue.c:9	1:		
	/home/josh/develop	ment/rtems/bsp	s/4.12/i386-rtems4	.12/pc386/lib/include/rtems/sco	pre/watchdogimpl.h:178:27: note: declared here	
	RTEMS_INLIN	NE_ROUTINE voi	d _Watchdog_Initial	ize(
	-					
				rtems-libbsd/build/i386-rtems		2
#2634 Summar	4 years ago New warning in pc38	fixed B6 VESA driver	arch/i386	Joel Sherrill	Pavel Pisa <ppisa@></ppisa@>	2 years ago
У						
	Pavel can you look		han /:296 /no296 /aon	sole/fb_vesa_rm.c: In function	find mode using EDID's	
Descripti on					rning: dereferencing type-punned pointer will brea	k strict-aliasing rules [-Wstrict-aliasing]
	if (*(uint16_t	t*)&edid.STI[ind	ex] == EDID_STI_D	DescriptorUnused) ~		
#2638	4 years ago	fixed	unspecified	Joel Sherrill	Joel Sherrill <joel@></joel@>	2 years ago
Summar y	pc386: ld -r issue wi	ith per function	sections			
on on	prototypes -Wimplic ne2000.o /data/hom	pipe -B/// it-function-decla ne/joel/rtems-4.	'//./lib/ -B// iration -Wstrict-proto 11-work/tools/4.12/	///./pc386/lib/ -specs bs otypes -Wnested-externs -qnoli	specs -qrtems -mtune=i386 -O2 -g -ffunction-se nkcmds -nostdlib -r -WI,gc-sections -WI,-Ttext,0> .0.0////.386-rtems4.12/bin/ld: gc-sections r	(00100000 - o ne2000.rel ne2000_rel-
#2641	symbol collect2: erro 4 years ago	or: ld returned 1 fixed	exit status build	aurelio	Chris Johns	
Summar	configure: enable-rte		bana			2 years ago
У		emsbsp doesn't	warn if bsp does not		Chris Johns	2 years ago
	When running config			exist	1	
Descripti on		gure with an inco	prrect bsp name the	exist script does not cause an error.	You can ever run make without getting any warnin	ig message.
on		gure with an inco	prrect bsp name the	exist script does not cause an error.	1	ig message.
on #2644 Summar	The script should ch	gure with an inco eck the name of fixed	prrect bsp name the the bsp and continu tool/rsb	exist script does not cause an error. Ie only if it is a valid bsp. On th	You can ever run make without getting any warnin	ig message. nvalid the script should prompt a message.
on # 2644	The script should ch 4 years ago	gure with an inco eck the name of fixed gdb 7.11 but doo	the bsp and continue tool/rsb so n gdb 7.9	script does not cause an error. e only if it is a valid bsp. On th Joel Sherrill	You can ever run make without getting any warnin	ig message. nvalid the script should prompt a message
on # 2644 Summar y	The script should che 4 years ago sis does not run on o I know we reported	gure with an inco eck the name of fixed gdb 7.11 but doo this on the gdb	the bsp and continu tool/rsb es on gdb 7.9	script does not cause an error. e only if it is a valid bsp. On th Joel Sherrill	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i	ig message. nvalid the script should prompt a message.
on # 2644 Summar y Descripti on	The script should che 4 years ago sis does not run on o I know we reported Neither gdb nor run Not sure about other	gure with an inco eck the name of fixed gdb 7.11 but doo this on the gdb works for sis on r simulators.	brrect bsp name the the bsp and continu tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked	e exist script does not cause an error. ie only if it is a valid bsp. On th Joel Sherrill ive a ticket. against RTEMS 4.11 tools (gdb	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i 4.9) and it will run sis.	ig message. nvalid the script should prompt a message. 2 years ago
on #2644 Gummar y Descripti on #2649	The script should che 4 years ago sis does not run on o I know we reported Neither gdb nor run	gure with an inco eck the name of fixed gdb 7.11 but doo this on the gdb works for sis on r simulators. fixed	brrect bsp name the the bsp and continu tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked tool/rsb	exist script does not cause an error. le only if it is a valid bsp. On th Joel Sherrill live a ticket. against RTEMS 4.11 tools (gdb Chris Johns	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i	ig message. nvalid the script should prompt a message.
on #2644 Summar y Descripti on #2649	The script should che 4 years ago sis does not run on o I know we reported Neither gdb nor run Not sure about other 4 years ago	gure with an inco eck the name of fixed gdb 7.11 but doo this on the gdb works for sis on r simulators. fixed	brrect bsp name the the bsp and continu tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked tool/rsb	exist script does not cause an error. le only if it is a valid bsp. On th Joel Sherrill live a ticket. against RTEMS 4.11 tools (gdb Chris Johns	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i 4.9) and it will run sis.	ig message. nvalid the script should prompt a message. 2 years ago
on #2644 Summar y Descripti on #2649 Summar y	The script should che 4 years ago sis does not run on o I know we reported Neither gdb nor run Not sure about other 4 years ago RSB remove 4.11, 4	gure with an inco eck the name of fixed gdb 7.11 but doo this on the gdb works for sis on r simulators. fixed .10 and 4.9 from	brrect bsp name the the bsp and continu tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked tool/rsb n the master branch	e exist script does not cause an error. ie only if it is a valid bsp. On th Joel Sherrill ive a ticket. against RTEMS 4.11 tools (gdb Chris Johns	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i 4.9) and it will run sis.	nyalid the script should prompt a message. 2 years ago 2 years ago
on #2644 Summar y Descripti Summar y Descripti on	The script should che 4 years ago sis does not run on o I know we reported Neither gdb nor run Not sure about other 4 years ago RSB remove 4.11, 4 Having 4.11 on mass Leave 4.9 and 4.10	gure with an inco eck the name of fixed gdb 7.11 but door this on the gdb works for sis on r simulators. fixed .10 and 4.9 from ter is confusing until they are br	brrect bsp name the the bsp and continu tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked tool/rsb n the master branch users as they build 4 anched off master. N	exist script does not cause an error. e only if it is a valid bsp. On th Joel Sherrill eve a ticket. against RTEMS 4.11 tools (gdb Chris Johns . 4.11 tool on master and there n We will make these branches or	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i 4.9) and it will run sis. Chris Johns hay be issues in 4.11 configurations fixed on the 4. Ice 4.12 is stable again.	nyalid the script should prompt a message. 2 years ago 2 years ago 11 branch.
on #2644 Summar y Descripti on y Descripti on w 2663	The script should che 4 years ago sis does not run on o I know we reported Neither gdb nor run Not sure about other 4 years ago RSB remove 4.11, 4 Having 4.11 on mas	gure with an inco eck the name of fixed gdb 7.11 but doo this on the gdb works for sis on r simulators. fixed .10 and 4.9 from ter is confusing until they are br wontfix	brrect bsp name the the bsp and continu tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked tool/rsb n the master branch users as they build 4 anched off master. N arch/i386	e exist script does not cause an error. ie only if it is a valid bsp. On th Joel Sherrill ive a ticket. against RTEMS 4.11 tools (gdb Chris Johns 4.11 tool on master and there n	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i 4.9) and it will run sis. Chris Johns nay be issues in 4.11 configurations fixed on the 4.	nyalid the script should prompt a message. 2 years ago 2 years ago
on #2644 Summar y Descripti on y Descripti on w 2663	The script should che 4 years ago sis does not run on g I know we reported Neither gdb nor run Not sure about other 4 years ago RSB remove 4.11, 4 Having 4.11 on mass Leave 4.9 and 4.10 4 years ago pc386 BSP has comp	gure with an inco eck the name of fixed gdb 7.11 but dou this on the gdb works for sis on r simulators. fixed .10 and 4.9 from ter is confusing until they are br wontfix plex dependencie	brrect bsp name the the bsp and continu tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked tool/rsb n the master branch users as they build 4 anched off master. 1 arch/i386 es	e exist script does not cause an error. ue only if it is a valid bsp. On th Joel Sherrill ave a ticket. against RTEMS 4.11 tools (gdb Chris Johns 4.11 tool on master and there m We will make these branches or Joel Sherrill	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i 4.9) and it will run sis. Chris Johns hay be issues in 4.11 configurations fixed on the 4. ice 4.12 is stable again. Sebastian Huber	nyalid the script should prompt a message. 2 years ago 2 years ago 11 branch. 2 years ago
on #2644 Summar y Descripti on Summar y Descripti on #2663 Summar	The script should che 4 years ago sis does not run on g I know we reported Neither gdb nor run Not sure about other 4 years ago RSB remove 4.11, 4 Having 4.11 on mass Leave 4.9 and 4.10 4 years ago pc386 BSP has comp In 4.11, the minimu	gure with an inco eck the name of fixed gdb 7.11 but down this on the gdb works for sis on r simulators. fixed .10 and 4.9 from ter is confusing until they are br wontfix polex dependencie m executable di	brrect bsp name the the bsp and continu tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked tool/rsb n the master branch users as they build 4 anched off master. 1 arch/i386 es d not include open()	e exist script does not cause an error. ue only if it is a valid bsp. On th Joel Sherrill ave a ticket. against RTEMS 4.11 tools (gdb Chris Johns 4.11 tool on master and there m We will make these branches or Joel Sherrill	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i 4.9) and it will run sis. Chris Johns hay be issues in 4.11 configurations fixed on the 4. ice 4.12 is stable again. Sebastian Huber ds rtems_libio_post_driver() and rtems_libio_exit(nyalid the script should prompt a message. 2 years ago 2 years ago 11 branch. 2 years ago
on #2644 Summar y Descripti on #2663 Summar y	The script should che 4 years ago sis does not run on g I know we reported Neither gdb nor run Not sure about other 4 years ago RSB remove 4.11, 4 Having 4.11 on mass Leave 4.9 and 4.10 4 years ago pc386 BSP has comp In 4.11, the minimu the master, these two	gure with an inco eck the name of fixed gdb 7.11 but doo this on the gdb works for sis on r simulators. fixed .10 and 4.9 from ter is confusing until they are br wontfix plex dependencio m executable di vo methods are	brrect bsp name the tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked tool/rsb n the master branch users as they build 4 anched off master. 1 arch/i386 es d not include open() showing up in minim	e exist script does not cause an error. ie only if it is a valid bsp. On th Joel Sherrill ive a ticket. against RTEMS 4.11 tools (gdb Chris Johns 4.11 tool on master and there n We will make these branches or Joel Sherrill and close() because the methor um and pulling in these methor	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i 4.9) and it will run sis. Chris Johns hay be issues in 4.11 configurations fixed on the 4. ice 4.12 is stable again. Sebastian Huber ds rtems_libio_post_driver() and rtems_libio_exit(ig message. nvalid the script should prompt a message. 2 years ago 2 years ago 11 branch. 2 years ago) were not included in the executable. On
on #2644 Summar y Descripti on #2663 Summar y Descripti on	The script should che 4 years ago sis does not run on g I know we reported Neither gdb nor run Not sure about other 4 years ago RSB remove 4.11, 4 Having 4.11 on mass Leave 4.9 and 4.10 4 years ago pc386 BSP has comp In 4.11, the minimut the master, these two The dependency chaincluded. FWIW the minimum	gure with an inco eck the name of fixed gdb 7.11 but door this on the gdb works for sis on r simulators. fixed .10 and 4.9 from ter is confusing until they are br wontfix plex dependencie m executable di yo methods are sin used to be if size looks pretty	brrect bsp name the the bsp and continu tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked tool/rsb n the master branch users as they build 4 anched off master. 1 arch/i386 es d not include open() showing up in minim the console driver w y good on the master	e exist script does not cause an error. ie only if it is a valid bsp. On th Joel Sherrill ive a ticket. against RTEMS 4.11 tools (gdb Chris Johns 4.11 tool on master and there n We will make these branches or Joel Sherrill and close() because the methor au and pulling in these methor as installed, we needed to oper r for sis. Fixing this would likely	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i 4.9) and it will run sis. Chris Johns hay be issues in 4.11 configurations fixed on the 4. ice 4.12 is stable again. Sebastian Huber ds rtems_libio_post_driver() and rtems_libio_exit(ds. and close stdin, stdout, and stderr. Now even with r drop it at least another 5%.	ig message. nvalid the script should prompt a message. 2 years ago 2 years ago 11 branch. 2 years ago) were not included in the executable. On hout the console configured these are
Descripti on #2649 Summar y Descripti on #2663 Summar y Descripti	The script should che 4 years ago sis does not run on g I know we reported Neither gdb nor run Not sure about other 4 years ago RSB remove 4.11, 4 Having 4.11 on mass Leave 4.9 and 4.10 4 years ago pc386 BSP has comp In 4.11, the minimu the master, these two The dependency chaincluded.	gure with an inco eck the name of fixed gdb 7.11 but down this on the gdb works for sis on r simulators. fixed .10 and 4.9 from ter is confusing until they are br wontfix plex dependencie m executable di yo methods are shin used to be if	brrect bsp name the the bsp and continu tool/rsb es on gdb 7.9 list but we should ha gdb 4.11. Checked tool/rsb n the master branch users as they build 4 anched off master. 1 arch/i386 es d not include open() showing up in minim the console driver w	script does not cause an error. e only if it is a valid bsp. On th Joel Sherrill ave a ticket. against RTEMS 4.11 tools (gdb Chris Johns 4.11 tool on master and there n We will make these branches or Joel Sherrill and close() because the methor um and pulling in these methor as installed, we needed to oper	You can ever run make without getting any warnin e other hand if the bsp name given by the user is i 4.9) and it will run sis. Chris Johns hay be issues in 4.11 configurations fixed on the 4. ice 4.12 is stable again. Sebastian Huber ds rtems_libio_post_driver() and rtems_libio_exit(ds.	ig message. nvalid the script should prompt a message 2 years ago 2 years ago 11 branch. 2 years ago) were not included in the executable. On

New test failure on sis but likely all targets.

				INVALID_NUMBER TA1 - rtems_ ESSEUL TA1 - rtems_clock_get_t	od - 00:00:00 01/01/2001 - RTEMS_SUCCESSFUL	
	12/31/1999 - RTEM	IS_SUCCESSFUL T	A1 - rtems_clock_get		TEMS_SUCCESSFUL assertion "ticks < 0x4000000	
	Breakpoint 1, _Terr	ninate (the_source	e=the_source@entry=	=RTEMS_FATAL_SOURCE_ASSEF	IT,	
	is_internal=	is_internal@entry=	=false, the_error=the	_error@entry=33694096) at/.	.////rtems/c/src///cpukit/score/src/interr	.c:36
	36 { (gdb) bt #0 _	Terminate (the_so	urce=the_source@en	try=RTEMS_FATAL_SOURCE_AS	SERT,	
	is_internal=	is_internal@entry=	=false, the_error=the	_error@entry=33694096) at/.	.////rtems/c/src///cpukit/score/src/interr	.c:36
				EMS_FATAL_SOURCE_ASSERT,		
				ms/c/src///cpukit/sapi/src/fat	al2.c:34	
	#2 0x02004a9c in		, ac ii, ii, ii, ii, ii, ii, ii, ii,			
	file=file@en	try=0x201a650 "		lib/include/rtems/score/watchdo edexpr@entry=0x201a638 "ticks	gimpl.h", line=line@entry=316, func=func@entry= < < 0x400000000") at	=0x201a6d0 < <u>func</u> .3277>
			/cpukit/libcsupport/sr from_timespec (ts=0)			
Descripti	at//cpuk	it///sis/lib/ind	lude/rtems/score/wa	tchdogimpl.h:316		
on				s_timestamp@entry=0x2022280)	
			//cpukit/score/src/c		,	
			tod=tod@entry=0x20			
			-	-		
			//cpukit/rtems/src/c	CIOCKSET.C:42		
	#6 0x02001818 in		. ,			
				sts/spclock_err02/init.c:93		
	_	_ /_	ptor_numeric (execut	5 ,		
				hreadentryadaptornumericTyp	e <return> to continue, or q <return> to quit</return></return>	
	.c:25 #8 0x02012e	0c in _Thread_Har	ndler ()			
	at///	//rtems/c/src/	//cpukit/score/src/tl	hreadhandler.c:93		
	#9 0x02012d60 in	_Thread_Handler (0			
	at///	//rtems/c/src/	//cpukit/score/src/tl	hreadhandler.c:29		
	RTEMS_SUCCESSF	JL\n"); 91 92 buil rtems_clock_set -	d_time(&time, 12, 3	1, 2100, 23, 59, 59, 0); 93 stat	<pre>ms_clock_get_tod"); 90 print_time("TA1 - rtems us = rtems_clock_set(&time); 94 directive_failed tems_task_wake_after(rtems_clock_get_ticks_pe</pre>	l(status, "rtems_clock_set"); 95
		a (atime), (gab)				
#2669	4 years ago	fixed	tool/rsb	Stefan Wallentowitz		2 years ago
#2669 Summar	4 years ago Update OpenRISC t		tool/rsb	Stefan Wallentowitz		2 years ago
	Update OpenRISC t	oolchain in 4.12	: .	Stefan Wallentowitz		2 years ago
Summar		oolchain in 4.12 C toolchain to new 26	: .	Stefan Wallentowitz		2 years ago
Summar y Descripti on	Update OpenRISC t Bump the OpenRIS • Binutils to 2 • GCC to 4.9.3 • GDB to 7.11	oolchain in 4.12 C toolchain to new .26 3	er versions:			
Summar y Descripti on #2672	Update OpenRISC t Bump the OpenRIS Binutils to 2 GCC to 4.9. GDB to 7.11 4 years ago	oolchain in 4.12 C toolchain to new 26 3 fixed	er versions: unspecified	Serg Kruglov	Sebastian Huber <sebastian.huber@></sebastian.huber@>	
Summar y Descripti on	Update OpenRISC t Bump the OpenRIS Binutils to 2 GCC to 4.9. GDB to 7.11 4 years ago	oolchain in 4.12 C toolchain to new 26 3 fixed	er versions: unspecified			
Summar y Descripti on #2672 Summar	Update OpenRISC to Bump the OpenRIS Binutils to 2 GCC to 4.9.3 GDB to 7.11 4 years ago After latest patches	coolchain in 4.12 C toolchain to new 26 3 fixed with Objects_Get with Objects_Get	er versions: unspecified _by_name rtems-mas	Serg Kruglov ster not compiling withoutenal		> 2 years ago
Summar y Descripti on #2672 Summar y Descripti	Update OpenRISC t Bump the OpenRIS Binutils to 2 GCC to 4.9.; GDB to 7.11 4 years ago After latest patches enable-posix - all C 4 years ago	fixed with Objects_Get K. fixed	er versions: unspecified _by_name rtems-mas _by_name rtems-mas posix	Serg Kruglov ster not compiling withoutenal		> 2 years ago
Summar y Descripti on ¥2672 Summar y Descripti on #2674 Summar	Update OpenRISC to Bump the OpenRIS Binutils to 2 GCC to 4.9.3 GDB to 7.11 4 years ago After latest patches enable-posix - all C	fixed with Objects_Get K. fixed	er versions: unspecified _by_name rtems-mas _by_name rtems-mas posix	Serg Kruglov ster not compiling withoutenal ster not compiling if i usedisab	ble-posix le-posix. Type "Objects_Get_by_name_error" not	> 2 years ago resolved in posixapi.h in sapi folder. If
Summar y Descripti on #2672 Summar y Descripti on #2674	Update OpenRISC t Bump the OpenRISC Binutils to 2 GCC to 4.9.3 GDB to 7.11 4 years ago After latest patchese enable-posix - all C 4 years ago CORE spinlock imple Lets assume we have	oolchain in 4.12 C toolchain to new 26 fixed with Objects_Get K. fixed ementation is next ve two tasks A and	er versions: unspecified _by_name rtems-mas _by_name rtems-mas posix to be useless	Serg Kruglov ster not compiling withoutenal ster not compiling if i usedisab Sebastian Huber	ble-posix le-posix. Type "Objects_Get_by_name_error" not	> 2 years ago resolved in posixapi.h in sapi folder. If 2 years ago
Summar y Descripti on ¥2672 Summar y Descripti on #2674 Summar	Update OpenRISC t Bump the OpenRISC Binutils to 2 GCC to 4.9.3 GDB to 7.11 4 years ago After latest patchese enable-posix - all C 4 years ago CORE spinlock impl Lets assume we ha Alternative implement	fixed with Objects_Get K. fixed with Objects_Get with objects_Get k.	er versions: unspecified by_name rtems-mas by_name rtems-mas posix to be useless d B. Task A acquires a	Serg Kruglov ster not compiling withoutenal ster not compiling if i usedisab Sebastian Huber a CORE spinlock. Now B somehow	ble-posix le-posix. Type "Objects_Get_by_name_error" not Sebastian Huber	> 2 years ago resolved in posixapi.h in sapi folder. If 2 years ago
Summar y Descripti on ¥2672 Summar y Descripti on #2674 Summar	Update OpenRISC t Bump the OpenRISC Binutils to 2 GCC to 4.9.3 GDB to 7.11 4 years ago After latest patchese enable-posix - all C 4 years ago CORE spinlock impl Lets assume we ha Alternative implement	fixed with Objects_Get K. fixed with Objects_Get with objects_Get k.	er versions: unspecified by_name rtems-mas by_name rtems-mas posix to be useless d B. Task A acquires a	Serg Kruglov ster not compiling withoutenal ster not compiling if i usedisab Sebastian Huber	ble-posix le-posix. Type "Objects_Get_by_name_error" not Sebastian Huber	> 2 years ago resolved in posixapi.h in sapi folder. If 2 years ago
Summar y Descripti on #2672 Summar y Descripti Summar y	Update OpenRISC to Bump the OpenRISC Binutils to 2 GCC to 4.9. GDB to 7.11 4 years ago After latest patchess enable-posix - all C 4 years ago CORE spinlock imple Lets assume we ha Alternative implement Disable thread disp	oolchain in 4.12 C toolchain to new 26 mith Objects_Get with Objects_Get K. fixed ementation is next ve two tasks A and entation: atching and interm	er versions: unspecified by_name rtems-mas by_name rtems-mas posix to be useless d B. Task A acquires a	Serg Kruglov ster not compiling withoutenal ster not compiling if i usedisab Sebastian Huber a CORE spinlock. Now B somehow	ble-posix le-posix. Type "Objects_Get_by_name_error" not Sebastian Huber	> 2 years ago resolved in posixapi.h in sapi folder. If 2 years ago
Summar y Descripti on #2672 Summar y Summar y	Update OpenRISC to Bump the OpenRISC Binutils to 2 GCC to 4.9.3 GDB to 7.11 4 years ago After latest patchess enable-posix - all C 4 years ago CORE spinlock implement Lets assume we ha Alternative implement Disable thread disp Drawback: The test	oolchain in 4.12 C toolchain to new 26 3 fixed with Objects_Get K. fixed ementation is next we two tasks A and entation: atching and interru	er versions: unspecified _by_name rtems-mas _by_name rtems-mas posix to be useless d B. Task A acquires a upts while owning the k Test Project would fi	Serg Kruglov ster not compiling withoutenat ster not compiling if i usedisab Sebastian Huber a CORE spinlock. Now B somehow e spinlock. Forbid blocking calls w fail:	ble-posix le-posix. Type "Objects_Get_by_name_error" not Sebastian Huber	> 2 years ago resolved in posixapi.h in sapi folder. If 2 years ago
Summar y Descripti on #2672 Summar y Descripti Summar y	Update OpenRISC to Bump the OpenRISC Binutils to 2 GCC to 4.9.3 GDB to 7.11 4 years ago After latest patchess enable-posix - all C 4 years ago CORE spinlock impl Lets assume we ha Alternative implement Disable thread disp Drawback: The test https://github.com	fixed with Objects_Get K. fixed with Objects_Get k. fixed ementation is next ve two tasks A and entation: atching and interrut cases of the Linux (linux-test-project)	er versions: unspecified by_name rtems-mas by_name rtems-mas posix to be useless d B. Task A acquires a upts while owning the k Test Project would fr /ltp/blob/master/test	Serg Kruglov ster not compiling withoutenat ster not compiling if i usedisab Sebastian Huber a CORE spinlock. Now B somehow e spinlock. Forbid blocking calls w fail:	ele-posix le-posix. Type "Objects_Get_by_name_error" not Sebastian Huber v executes and tries to acquire the same CORE spin hile owning the spinlock.	> 2 years ago resolved in posixapi.h in sapi folder. If 2 years ago
Summar y Descripti on #2672 Summar y Descripti Summar y	Update OpenRISC to Bump the OpenRISC Binutils to 2 GCC to 4.9.3 GDB to 7.11 4 years ago After latest patchese enable-posix - all Co 4 years ago CORE spinlock imple Lets assume we have Alternative implement Disable thread disp Drawback: The test https://github.com, Optimization: User	fixed with Objects_Get K. fixed with Objects_Get k. fixed ementation is next we two tasks A and entation: atching and interru cases of the Linu: (linux-test-project, provided storage s	er versions: unspecified _by_name rtems-mas _by_name rtems-mas	Serg Kruglov ster not compiling withoutenat ster not compiling if i usedisab Sebastian Huber a CORE spinlock. Now B somehov e spinlock. Forbid blocking calls w fail: cases/open_posix_testsuite/conf	ele-posix le-posix. Type "Objects_Get_by_name_error" not Sebastian Huber v executes and tries to acquire the same CORE spin hile owning the spinlock.	> 2 years ago resolved in posixapi.h in sapi folder. If 2 years ago
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	The rtems_configu	ration_table h	ias:			
	#ifdef RTEMS bool	SMP	smp enabled;			
Descripti	#endif		* <u>-</u> ·			
on				d for 4.12 and always set to fa	use when RTEMS_SMP is not defined. It is impossible	to parse the configuration table v
	5		, 51	,	e configuration table must always be defined.	
	Chris					
‡2684	4 years ago	fixed	arch/sparc	David Binderman	Sebastian Huber	2 years ago
ummar y	rtems/c/src/lib/libbs	p/sparc/leon3/clc	ock/ckinit.c:122: duplic	cate ir		
	rtems/c/src/lib/libbs	p/sparc/leon3/clc	ock/ckinit.c:122]: (styl	e) Expression is always false b	ecause 'else if' condition matches previous condition at	line 116.
	Source code is					
	} else if (stat	e == 1) {				
escripti on	-	ed int ks = $1U <$	< 5;			
	state :					
			s s1_s2 (unsigned in	nt) clkirq;		
	} else if (stat					2
#2685 Summar	4 years ago c/src/lib/libbsp/arm/	fixed atsam/network/ii	arch/arm f atsam.c:409: possib	David Binderman le bad if statement	Sebastian Huber	2 years ago
у			_ ·			
		p/arm/atsam/net	work/if_atsam.c:409]	(style) Redundant condition:	If 'phy <= 0', the comparison 'phy <= 31' is always tru	e.
	Source code is) 0.0 (=h:	N (
	Ir ((pny <= 0) && (phy <= 31	<i>))</i> {			
	 invalid phy ni 	mber */				
)escripti on	Maybe better code	inder 7				
	if ((phy <= 0)				
	(phy >= 31					
	/*					
	 invalid phy nu 	ımber */				
¥2689	4 years ago	fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago
ummar y	POSIX key destructo	rs must be called	during thread restart			
, Descripti					andlers. This ensures that the TLS object destructors a	re called during thread restart for
on		-			Init task in a clean environment.	2 40070 000
# 2692 Summar	4 years ago User extensions exe	fixed cution order must	t be clarified	Sebastian Huber	Sebastian Huber	2 years ago
У						
Descripti on	The implemented an	a aocumentea ex	Recution order of some	user extensions disagree. Inte	nded behaviour must be tested. Documentation must h	be updated accordingly.
#2693	4 years ago	fixed	doc	Joel Sherrill	Chris Johns	2 weeks ago
Summar y	Update doc to reflec	obsoleting rtem	s_clock_get()			
, Descripti	Ticket to reflect docu	imentation chang	ge needed on the mast	er but not on 4.11. When new	documentation format is available for master, this need	is to be accounted for.
on	4		a should de se su	last Chamilt	Cohesting United	2
#2694 Summar	4 years ago linking issue for htor	worksforme II, etc when using	network/legacy g -std=c99	Joel Sherrill	Sebastian Huber	2 years ago
y						
				-	ntonl, htons, ntohl, and ntohs. This test case is just for	htonl but others should be simila
				out I was testing on master.		
			===== #include <ar< td=""><td>pa/inet.h></td><td></td><td></td></ar<>	pa/inet.h>		
	int main(2501				
	int argc, char) {	argv				
		(uint32_t) argc: (uint32_t rc = htonl(v);	return v		
	} ===============					
	} ====================================		nat caused the linking	error to go away.		
	This script was what	I used to find wh	-		k/tools/4.12/i386-rtems4.12/pc586/	
Descripti on	This script was what	I used to find wh	-		k/tools/4.12/i386-rtems4.12/pc586/	
	This script was what	I used to find wh ======== R -std=c99 \	RTEMS_MAKEFILE_PAT	H=/home/joel/rtems-4.11-wor	k/tools/4.12/i386-rtems4.12/pc586/ =600 -D <u>USE_SVID_main.c -lbsd -lm_lbsd</u>	
	This script was what	I used to find wh ====== R -std=c99 \ MAKEFILE_PATH}	RTEMS_MAKEFILE_PAT	H=/home/joel/rtems-4.11-wor		
	This script was what ====================================	I used to find wh 	TEMS_MAKEFILE_PAT	H=/home/joel/rtems-4.11-wor	=600 -D <u>USE_SVID_main.c -lbsd -lm -lbsd</u>	
	This script was what ====================================	I used to find wh 	TEMS_MAKEFILE_PAT	H=/home/joel/rtems-4.11-wor	=600 -D <u>USE_SVID_main.c -lbsd -lm -lbsd</u>	
	This script was what ====================================	I used to find wh 	RTEMS_MAKEFILE_PAT	H=/home/joel/rtems-4.11-wor	=600 -D <u>USE_SVID_main.c -lbsd -lm -lbsd</u> -lbsd -lm -lbsd	
	This script was what ====================================	I used to find wh 	RTEMS_MAKEFILE_PAT	H=/home/joel/rtems-4.11-wor -qrtems \ -D_XOPEN_SOURCE -qrtems \ -D <u>USE_SVID main.c</u>	=600 -D <u>USE_SVID_main.c -lbsd -lm -lbsd</u> -lbsd -lm -lbsd	
	This script was what ====================================	I used to find wh std=c99 \ MAKEFILE_PATH} -std=c99 \ MAKEFILE_PATH} -std=c99 \ MAKEFILE_PATH} \	RTEMS_MAKEFILE_PAT	H=/home/joel/rtems-4.11-wor -qrtems \ -D_XOPEN_SOURCE -qrtems \ -D <u>USE_SVID main.c</u>	=600 -D <u>USE_SVID_main.c -Ibsd -Im_Ibsd</u> <u>-Ibsd -Im_Ibsd</u> sd	
on	This script was what 	I used to find wh std=c99 \ MAKEFILE_PATH} -std=c99 \ MAKEFILE_PATH} -std=c99 \ MAKEFILE_PATH} \ MAKEFILE_PATH}	RTEMS_MAKEFILE_PAT //lib -specs bsp_specs //lib -specs bsp_specs //lib -specs bsp_specs //lib -specs bsp_specs	H=/home/joel/rtems-4.11-wor -qrtems \ -D_XOPEN_SOURCE -qrtems \ -D <u>USE_SVID main.c</u> -qrtems \ main.c -lbsd -lm -lbs -qrtems \ main.c -lbsd -lm -lbs	=600 -D <u>USE_SVID_main.c -Ibsd -Im_Ibsd</u> <u>-Ibsd -Im_Ibsd</u> sd	
on #2695	This script was what ====================================	I used to find wh 	RTEMS_MAKEFILE_PAT	H=/home/joel/rtems-4.11-wor -qrtems \ -D_XOPEN_SOURCE -qrtems \ -D <u>USE_SVID_main.c</u> -qrtems \ main.c -lbsd -lm -lbs	=600 -D <u>USE_SVID_main.c -Ibsd -Im_Ibsd</u> <u>-Ibsd -Im_Ibsd</u> sd	2 years ago
Descripti on #2695 Summar y	This script was what 	I used to find wh 	RTEMS_MAKEFILE_PAT //lib -specs bsp_specs //lib -specs bsp_specs //lib -specs bsp_specs //lib -specs bsp_specs	H=/home/joel/rtems-4.11-wor -qrtems \ -D_XOPEN_SOURCE -qrtems \ -D <u>USE_SVID main.c</u> -qrtems \ main.c -lbsd -lm -lbs -qrtems \ main.c -lbsd -lm -lbs	=600 -D <u>USE_SVID_main.c -Ibsd -Im_Ibsd</u> <u>-Ibsd -Im_Ibsd</u> sd	2 years ago

RTEMS 5.1 Release Notes Descripti _POSIX_Semaphore_Delete() used -1 for the thread queue flush status which in turn resulted in an invalid memory access in

27/130

	4 years ago GCC 6.1 is broken	fixed for microblaze	tool/gcc	Sebastian Huber		2 years ago
Summar y						
	The enabled libato	mic reveals a bu	g in the microblaze RTE	EMS configuration:		
Descripti on	newlib-6ee81f44ed 20160415-newlib- rtems4.12-gcc-6.0 source-builder/rte rtems4.12/newlib/ newlib-6ee81f44ed B/build/rtems-4.12	04848901c7b05c 6ee81f44e04848 0.1-RC-20160415 ms/build/microbl /targ-include -isy 04848901c7b05c 2/microblaze-rter	968564d34a7ceed06-x 9901c7b05c968564d34; 5-newlib-6ee81f44e048 laze-rtems4.12-gcc-6.0 stem /scratch/git-rtem 968564d34a7ceed06-x ms4.12/lib/ -isystem /b	86_64-linux-gnu-1/build/./gcc/x a7ceed06-x86_64-linux-gnu-1/b 48901c7b05c96856434a7ceed .1-RC-20160415-newlib-6ee81f s-source-builder/rtems/build/min .86_64-linux-gnu-1/gcc-6.0.1-R0 uuild/rtems-4.12/microblaze-rter	//git-rtems-source-builder/rtems/build/microblaze-rtems4.12 /gcc - B/scratch/git-rtems-source-builder/rtems/build/microbi uild/./gcc/ -nostdinc - B/scratch/git-rtems-source-builder/rte 06-x86_64-linux-gnu-1/build/microblaze-rtems4.12/newlib/ 44e04848901c7b05c968564d34a7ceed06-x86_64-linux-gnu- croblaze-rtems4.12-gcc-6.0.1-RC-20160415- C-20160415/newlib/libc/include -B/build/rtems-4.12/microbla ns4.12/include -isystem /build/rtems-4.12/microblaze-rtems file xilinx.ld: No such file or directory collect2: error: Id retur	laze-rtems4.12-gcc-6.0.1-Rd ms/build/microblaze- -isystem /scratch/git-rtems- 1/build/microblaze- aze-rtems4.12/bin/ - s4.12/sys-include -g -O2
	gcc/config/microbl	laze/microblaze.h	n: %{!T*: -dT xilinx.ld	%s}"		
			ne RTEMS GCC configur			_
#2700	4 years ago	fixed	unspecified c:524]: (style) Suspicio	David Binderman	Sebastian Huber <sebastian.huber@></sebastian.huber@>	2 years ago
Summar y		sellent, si c/ i pelo.e	c.524]. (style) Suspicio			
Descripti on	Source code is if ((len = c maybe better code	getgroups(NGRO	UPS, gids) < 0)) {	us condition (assignment + com	parison); Clarify expression with parentheses.	
	if ((len = <u>c</u>	jetgroups(NGROI	UPS, gids)) < 0) {			
#2701	4 years ago	fixed	build	printk	Amar Takhar	2 years ago
Summar y	Rename asm file v	vitn .5(upper cas	e) ext. name			
y Descripti on			name, .s and .S is diffe patch to devel, but blo		ed produce .s file from .S. In a word, .S can use #define .s c	an not. KBuild clean .s files
#2702	4 years ago	fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Remove descriptor	r objects for POS	IX message queues			
y		-				
	The mq_open() fu				by a name. This is similar to sem_open(). In contrast to the since the object identifier can be used directly for this purport Sebastian Huber <sebastian.huber@></sebastian.huber@>	
y Descripti on	The mq_open() fu message queues u semaphores. 4 years ago	ise a separate ob	pject for the descriptor.	This extra object is superfluous, Sebastian Huber	since the object identifier can be used directly for this purpo	ose, just like for the
y Descripti on #2706 Summar y	The mq_open() fu message queues u semaphores. 4 years ago Buffer allocation o	fixed for a fixed	oject for the descriptor. unspecified is broken on SMP confi	This extra object is superfluous, Sebastian Huber	since the object identifier can be used directly for this purpo	ose, just like for the
y Descripti on #2706 Summar y	The mq_open() fu message queues u semaphores. 4 years ago	fixed for a fixed	oject for the descriptor. unspecified is broken on SMP confi	This extra object is superfluous, Sebastian Huber	since the object identifier can be used directly for this purpo	ose, just like for the
y Descripti on #2706 Summar y Descripti on	The mq_open() fu message queues u semaphores. 4 years ago Buffer allocation o	fixed for a fixed	oject for the descriptor. unspecified is broken on SMP confi	This extra object is superfluous, Sebastian Huber	since the object identifier can be used directly for this purpo	ose, just like for the
y Descripti on Summar y Descripti on	The mq_open() fu message queues u semaphores. 4 years ago Buffer allocation o The capture engin 4 years ago	ise a separate ob fixed f capture engine e uses function s fixed	ject for the descriptor. unspecified is broken on SMP confi tatic variables.	This extra object is superfluous, Sebastian Huber gurations	since the object identifier can be used directly for this purpo Sebastian Huber <sebastian.huber@></sebastian.huber@>	ose, just like for the
y Descripti on Summar y Descripti on #2707 Summar y Descripti	The mq_open() fu message queues u semaphores. 4 years ago Buffer allocation o The capture engine 4 years ago Unsafe use of curr	ise a separate ob fixed f capture engine e uses function s fixed rent processor inc	ject for the descriptor. unspecified is broken on SMP confi tatic variables. unspecified	This extra object is superfluous, Sebastian Huber gurations Sebastian Huber	since the object identifier can be used directly for this purpo Sebastian Huber <sebastian.huber@></sebastian.huber@>	ose, just like for the 2 years ago
y Descripti on #2706 Summar y Descripti on Summar y Descripti on	The mq_open() fu message queues u semaphores. 4 years ago Buffer allocation o The capture engine 4 years ago Unsafe use of curr The current proces	ise a separate ob fixed f capture engine e uses function s fixed rent processor income ssor index is used	oject for the descriptor. unspecified is broken on SMP confi tatic variables. unspecified dex in capture engine d outside a thread dispa	This extra object is superfluous, Sebastian Huber gurations Sebastian Huber atch disabled section.	since the object identifier can be used directly for this purpo Sebastian Huber <sebastian.huber@> Sebastian Huber <sebastian.huber@></sebastian.huber@></sebastian.huber@>	2 years ago 2 years ago 2 years ago
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				····, ···j· ····,	online state and scheduler assignment.	
	[/] # cpuinf	o 				
			PER PROCESSOR INFO			
		INE SCHEDULER :	ID SCHEDULER NAME	2		
	0	1 0x0f01000)1 MPS			
	1 2	1 0x0f01000 1 0x0f01000				
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011	9 10	1 0x0f01000 1 0x0f01000				
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	14 15	1 0x0f01000 1 0x0f01000				
	16 17	1 0x0f01000 1 0x0f01000				
	18	1 0x0f01000	D1 MPS			
	19 20	1 0x0f01000 1 0x0f01000				
	21 22	1 0x0f01000	D1 MPS			
	22 23	1 0x0f01000 1 0x0f01000				
	years ago	fixed	score	Sebastian Huber e released by everyone	Sebastian Huber	2 years ago
У						
			it a locking protocol c no owner check in th		ne, e.g. in contrast to the POSIX mutexes (a	Il variants) or the Classic binary semphores with
Tł	his behaviour is a	a bit unexpected and	d not documented.			
Tł			n owner check is adde	:d:		
on	Exercising b		ne w/extract in FII			
	Exercising b TA00 - unblo		ne w/unblock in FII	?O order		
	rtems_semaph	ore_delete FAILE	D expected (RTEN	MS_SUCCESSFUL) got (RT	EMS_RESOURCE_IN_USE)	
T	his is actually a b	oug in the test, since	e an available mutex i	is released again.		
	years ago	fixed	arch/sparc	Sebastian Huber	Daniel Hellstrom	2 years ago
mmar gr y	rascs.c: Question	able use of binary s	semaphore			
	lse a simple binar	y semaphore or bin	ary semaphore with i	nherit priority instead.		
escripti on	c/src/lib/li c/src/lib/li c/src/lib/li c/src/lib/li		d/ascs/grascs.c- d/ascs/grascs.c: d/ascs/grascs.c- d/ascs/grascs.c-	if(rtems_semaphore_cre	or blocking ASCS_TC/TM functions */ ate(rtems_build_name('A','S','C','O') (RTEMS_FIFO RTEMS_BINARY_SEMAPHORE) RTEMS_NO_INHERIT_PRIORITY RTEMS_LOCA RTEMS_NO_PRIORITY_CEILING), 0, &cfg->tcseml) != RTEMS_SUCCESSFUL) {	
	<pre>c/src/lib/li c/src/lib/li c/src/lib/li c/src/lib/li c/src/lib/li</pre>	bbsp/sparc/shared bbsp/sparc/shared bbsp/sparc/shared bbsp/sparc/shared bbsp/sparc/shared bbsp/sparc/shared bbsp/sparc/shared	d/ascs/grascs.c- d/ascs/grascs.c- d/ascs/grascs.c: d/ascs/grascs.c- d/ascs/grascs.c-		ate(rtems_build_name('A','S','C','2') (RTEMS_FIFO RTEMS_BINARY_SEMAPHORE RTEMS_NO_INHERIT_PRIORITY RTEMS_LOCA RTEMS_NO_PRIORITY_CEILING), 0, &cfg->tcsem2) != RTEMS_SUCCESSFUL) {	
2727 4	years ago	fixed	fs/fat	Sebastian Huber	Sebastian Huber	2 years ago
ımmar F4 y	AT file systems u	se wrong semaphor	e for mutual exclusion	1		
		/src/dosfs/msdos		= rtems semaphore cre	ata (2	
	cpukit/libfs	/src/dosfs/msdos	initsupp.c-	- reems_semaphore_ere	1,	
escripti on	cpukit/libfs	/src/dosfs/msdos_ /src/dosfs/msdos_	initsupp.c-		RTEMS_BINARY_SEMAPHORE RTEMS_FI 0,	r0,
		/src/dosfs/msdos /src/dosfs/msdos			&fs_info->vol_sema);	
		ry semaphore with i		Sebastian Huber	Sebastian Huber	2
	years ago ipes use wrong se	fixed emaphore for mutua	al exclusion	Sebastian nuber	Sebastian Huber	2 years ago
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	hould use a bina	ry semaphore with i	nherit priority instead			
	years ago	fixed	score	Sebastian Huber	Sebastian Huber	2 years ago
2729 4	FIP Client uses w	rong semaphore for	mutual exclusion			
2729 4						
2729 4 mmar Tf	cpukit/libne cpukit/libne cpukit/libne cpukit/libne	tworking/lib/tftp tworking/lib/tftp tworking/lib/tftp tworking/lib/tftp tworking/lib/tftp	pDriver.c- 1, pDriver.c- RTEMS pDriver.c: RTEMS pDriver.c- RTEMS	s_build_name('T', 'F', S_FIFO S_BINARY_SEMAPHORE S_NO_FNICRIT_PRIORITY S_NO_PRIORITY_CELLING	1	
2 729 4 mmar TF y	cpukit/libne cpukit/libne cpukit/libne cpukit/libne cpukit/libne	tworking/lib/tftp tworking/lib/tftp tworking/lib/tftp	pDriver.c- 1, pDriver.c- RTEMS pDriver.c: RTEMS pDriver.c- RTEMS pDriver.c- RTEMS	S_FIFO S_BINARY_SEMAPHORE	1	

	4 years ago fixed	posix	Gedare Bloom	Gedare Bloom	2 years ago
Summar	Add clock_nanosleep()				
y Descripti	The clock_nanosleep function is p relative reference point via TIMEF		ifying the clock source (CLOCK_R	EALTIME or CLOCK_MONOTONIC) and to control whether or	not to use an absolute or
on	See also: http://pubs.opengroup	org/onlinenubs/00969	5399/functions/clock_nanosleen k	atml	
#2734	4 years ago fixed	posix	Sebastian Huber	Sebastian Huber <sebastian.huber@></sebastian.huber@>	2 years ago
Summar	pthread_setschedprio() is missing				2,00.0 090
У					
	See also				
	http://pubs.opengroup.org/online	epubs/9699919799/fun	ctions/pthread_setschedprio.htm	I	
Descripti	and				
on	http://pubs.opengroup.org/online	epubs/9699919799/fun	ctions/V2_chap02.html#tag_15_	08_04_01	
	In particular the distinction to pth	nread setschedparam()	(SCHED FIFO, item 7.).		
	Prototype is defined in Newlib pro				
#2735	4 years ago fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago
Summar	pthread_setschedparam() sets th				
У					
Docorinti	See also				
Descripti on	http://pubs.opengroup.org/online	epubs/9699919799/fun	ctions/V2_chap02.html#tag_15_	08_04_01	
	In particular the distinction to pth	nread_setschedprio() (S	SCHED_FIFO, item 7.).		
#2736	4 years ago fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago
Summar	pthread_getschedparam() return	s wrong priority values			
У	See also				
Descripti	http://pubs.opengroup.org/online	anube/0600010700/fum	ctions/nthread_cotschodnaram_h	tml	
on					
				by the most recent pthread_setschedparam(), pthread_setscl ity as a result of any priority inheritance or ceiling functions.	
#2737	4 years ago fixed	bsps	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Add CLOCK_DRIVER_USE_ONLY_				
У					
				do the clock tick processing on the boot processor on behalf	of all other processors.
on	Currently, this is intended as a w	orkaround for a Qemu	shortcoming on ARM.	· · ·	
on #2740	Currently, this is intended as a w 4 years ago fixed	orkaround for a Qemu		do the clock tick processing on the boot processor on behalf Sebastian Huber	of all other processors.
on #2740	Currently, this is intended as a w	orkaround for a Qemu	shortcoming on ARM.	· · ·	
#2740 Summar	Currently, this is intended as a w 4 years ago fixed	orkaround for a Qemu	shortcoming on ARM.	· · ·	
on #2740 Summar y Descripti	Currently, this is intended as a w 4 years ago fixed Suboptimal type for Timestamp_	orkaround for a Qemu score Score Control	shortcoming on ARM.	· · ·	
on #2740 Summar y	Currently, this is intended as a w 4 years ago fixed Suboptimal type for Timestamp Currently we have typedef struct bintime Timestamp	orkaround for a Qemu score Score Control	shortcoming on ARM. Sebastian Huber	Sebastian Huber	
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					 directive moves a task from one scheduler ins ned in the target scheduler instance. Add a third 	
	/** * @brief Sets	the scheduler	instance of a task	ς.		
	* * Initially, t	he scheduler i	nstance of a task	is set to the scheduler		
				e allows to move a task f fied by the scheduler ide		
	* @param[in] t * the execut		ier of the task.	Use @ref RTEMS_SELF to s	elect	
	* @param[in] s	cheduler_id Id	entifier of the sc sk priority with r	cheduler instance. respect to the new schedu	ler	
Descripti				of the task is set to thi _restart() for example.	s value.	
on			uccessful operatic	on. ctive is illegal on remot	e tasks	
	* @retval RTEN	IS_INVALID_ID_I		neduler identifier.		
	* @retval RTEN * change.	S_RESOURCE_IN_	USE The task owns	resources which deny a s	cheduler	
	* @see rtems_s */	cheduler_ident	().			
		de rtems_task_: task_id				
		schedul				
#2750); 4 yoars ago	fixed	unspecified	lool Shorrill	Cobaction Huber	2 voars ago
#2750 Summar	4 years ago Compile Error When I	fixed Aultiprocessing Er	unspecified nabled	Joel Sherrill	Sebastian Huber	2 years ago
У	This should impact ev	ery BSP with mu	Itiprocessing enabled	but I saw it on the sparc/leo	n3 and powerpc/psim	
					n failed: "Message_queue_MP_Packet"	
Descripti on	_Static_assert					
	//rtems/o		rtems/src/msgmp.c:2	28:1: note: in expansion of m	acro 'RTEMS_STATIC_ASSERT'	
#2751	4 years ago	fixed	score	Sebastian Huber	Sebastian Huber	22 months ago
Summar y	Thread dispatch via in	iterrupt is broken	at least on ARM and	PowerPC		
1	The ARM and PowerP	C interrupt epilog	ues call _Thread_Dis	patch() with interrupts disabl	ed (counter example: SPARC).	
Descripti on	On SMP configuration with interrupts disable		cessor interrupts set	the thread dispatch necessar	y indicator this prevents a thread dispatch notifi	cation in post-switch handlers (which all ru
#2752	On all configurations, 4 years ago	this is a serious i fixed	issue for the interrupt	t latency. Sebastian Huber	Sebastian Huber	2 years ago
Summar	Relax execution envir					
У						
I	/*	-			ere is an explanation for this in the code	
	/* * Take care * 'switch' e * disabled u	that 'begin' extensions can a	xtensions get to c run. This means m n' extensions comp	complete before nust keep dispatch	ere is an explanation for this in the code	
Descripti	/* * Take care * 'switch' e * disabled u */	that 'begin' extensions can and the straight that the second seco	xtensions get to c run. This means m	complete before nust keep dispatch	ere is an explanation for this in the code	
Descripti	/* * Take care * 'switch'e * disabled t */ _User_extensi However, the switch	that 'begin' e: extensions can : intil all 'begin .ons_Thread_begine extension is alway	<pre>xtensions get to c run. This means m n' extensions comp in(executing); ys invoked before the</pre>	complete before nust keep dispatch blete. e thread begin extension for a	ere is an explanation for this in the code Il threads except the initialization thread. A thre e not possible to call malloc(), create POSIX key	
Descripti	/* * Take care * 'switch' e * disabled u */ _User_extensi However, the switch limits the work which	that 'begin' e extensions can a until all 'begin cons_Thread_beg extension is alway can be carried ou	<pre>xtensions get to c run. This means m n' extensions comp in(executing); ys invoked before the ut in the thread begin</pre>	complete before must keep dispatch plete. e thread begin extension for a n extensions. It is for example	Il threads except the initialization thread. A thre	s or access C++ thread local storage.
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#2700	4 years ago	fixed	unspecified	Chris Johns	Chris Johns	2 years ago
Summar y	untar does not kee	p permissions corre	ctly.			
1	On disk I have 'x' v	with:				
	\$ ls -las x					
		1 chris caeng	48 Jul 14 11:46 x			
	the tar file shows:					
Descripti on	\$ tar tvf rod -rwxr-xr-x (otfs.tar) chris caeng	48 Jul 14 11:46 x			
011	and in the IMFS it :					
	[/] # ls -las 0 -rw-rr	1 root root 4	8 Jan 1 00:00 x			
	The makes adding	'joel' scripts difficult	t.			
#2769	4 years ago	invalid	unspecified	Chris Johns	Chris Johns	2 years ago
Summar y	rtems-syms does r	not clean up temp fil	es.			
1	I am seeing temps	files such as:				
	\$ ls -las /tr					
		 1 chris wheel 1 chris wheel 		/tmp/rld04lbaa.rldxx /tmp/rldOniaaa.rldxx		
		- 1 chris wheel		/tmp/rldOviaaa.rldxx /tmp/rld1Hhaaa.rldxx		
Dogoripti	88 -rw	- 1 chris wheel	87426 Jul 27 18:30			
Descripti on	0 -rw	- 1 chris wheel	0 Jul 29 17:11	/tmp/rld2rwaaa.rldxx		
	88 -rw		88148 Jul 29 17:40			
	88 -rw	- 1 chris wheel	87426 Jul 27 18:25 87426 Jul 27 18:27			
		- 1 chris wheel - 1 chris wheel		/tmp/rld4Wiaaa.rldxx /tmp/rld4bfaaa.rldxx		
	L				with 4.12 (master). This is on FreeBSD.	
#2770	4 years ago	fixed	doc	Christian Mauderer	Sebastian Huber	2 years ago
Summar	Missing documenta	ation for RTEMS_LIN	KER_ROSET_CONTENT	and RTEMS_LINKER_RWSET_(CONTENT	
У	Currently the two r	macros				
			NT RTEMS_LINKER_RW	SET CONTENT		
Descripti			ided as soon as the doc			
		a. This should be ac		repuis reauy fuilit.		
on		een introduced in the		Tepo is ready for it.		
UT	The macros have b	een introduced in th	nis commit:			
	The macros have b https://git.rtems.o 4 years ago Empty C++ file wit I have an application	een introduced in th rg/rtems/commit/?i wontfix ch just <rtems.h> d</rtems.h>	nis commit: d=5fe6d07ad5690e3d9c score oes not compile with HE	6445ca3a465a700a5a5015 Chris Johns		2 years ago
#2771 Summar	The macros have b https://git.rtems.o 4 years ago Empty C++ file wit I have an application The following C++	een introduced in th rg/rtems/commit/?i wontfix ch just <rtems.h> d</rtems.h>	nis commit: d=5fe6d07ad5690e3d9c score oes not compile with HE	6445ca3a465a700a5a5015 Chris Johns		2 years ago
#2771 Summar	The macros have b https://git.rtems.o 4 years ago Empty C++ file wit I have an application The following C++ \$ cat t1.cpp #include <ree< td=""><td>eeen introduced in th rg/rtems/commit/?i wontfix th just <rtems.h> d on that does not bui file: ems.h></rtems.h></td><td>his commit: d=5fe6d07ad5690e3d9c score oes not compile with HE. Ild.</td><td>6445ca3a465a700a5a5015 Chris Johns AD.</td><td>M. Build with:</td><td>2 years ago</td></ree<>	eeen introduced in th rg/rtems/commit/?i wontfix th just <rtems.h> d on that does not bui file: ems.h></rtems.h>	his commit: d=5fe6d07ad5690e3d9c score oes not compile with HE. Ild.	6445ca3a465a700a5a5015 Chris Johns AD.	M. Build with:	2 years ago
#2771 Summar	The macros have b https://git.rtems.o 4 years ago Empty C++ file wit I have an application The following C++ \$ cat t1.cpp #include <rte does not compile w</rte 	een introduced in th rg/rtems/commit/?i wontfix th just <rtems.h> d on that does not bui file: ems.h> vith git head 5fe6d0</rtems.h>	nis commit: d=5fe6d07ad5690e3d9c score oes not compile with HE ild. 7ad5690e3d9c6445ca3a	6445ca3a465a700a5a5015 Chris Johns	M. Build with:	2 years ago
#2771 Summar	The macros have b https://git.rtems.o 4 years ago Empty C++ file wit I have an application The following C++ \$ cat t1.cpp #include <rtd does not compile w \$ /opt/work/i _B/opt/work/i</rtd 	een introduced in th rg/rtems/commit/?i wontfix th just <rtems.h> d on that does not bui file: ems.h> with git head 5fe6d0 rtems/4.12/bin/ar k/rtems/4.12/arm-</rtems.h>	his commit: d=5fe6d07ad5690e3d9c score oes not compile with HE ild. 7ad5690e3d9c6445ca3a m-rtems4.12/stlinx_zync	465a700a5a5015 on Zynq AR	M. Build with:	2 years ago
#2771 Summar	The macros have b https://git.rtems.o 4 years ago Empty C++ file wit I have an application The following C++ \$ cat t1.cpp #include <rtext does not compile w \$ /opt/work/n -B/opt/work/n -B/opt/work/n -B/opt/work/n</rtext 	eeen introduced in th rg/rtems/commit/?i wontfix th just <rtems.h> d on that does not bui file: ems.h> with git head 5fe6d0 rtems/4.12/bin/ar k/rtems/4.12/bin/ar k/spss/rtems/4.12 specs -qrtems \</rtems.h>	his commit: d=5fe6d07ad5690e3d9c score oes not compile with HE ild. 7ad5690e3d9c6445ca3a m-rtems4.12/xilinx_zynu /arm-rtems4.12/xilinx_	465a700a5a5015 on Zynq AR q_zc706/lib \ x_zynq_zc706/lib \	M. Build with:	2 years ago
#2771 Summar	The macros have b https://git.rtems.o 4 years ago Empty C++ file wit I have an application The following C++ \$ cat t1.cpp #include <rte does not compile w \$ /opt/work/ -B/opt/work/ -B/opt/work/ -B/opt/work/ -g-o2 -DNN</rte 	eeen introduced in th rg/rtems/commit/?i wontfix th just <rtems.h> d on that does not bui file: ems.h> with git head 5fe6d0/ cttems/4.12/bin/ar k/rtems/4.12/arm= k/rtems/4.12/arm= k/rtems/4.12/arm= x/bsps/rtems/4.12 specs -qrtems \ y7-a -mthumb -mfp DEBUG -std=c++11</rtems.h>	his commit: d=5fe6d07ad5690e3d9c score oes not compile with HE iid. 7ad5690e3d9c6445ca3a m-rtems4.12/stlinx_zynt /arm-rtems4.12/xilinx_zynt /arm-rtems4.12/xilinx_zynt /arm-rtems4.12/xilinx	465a700a5a5015 on Zynq AR	M. Build with:	2 years ago
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#2771 Summar y	The macros have b https://git.rtems.or 4 years ago Empty C++ file with I have an application The following C++ \$ cat 11.cpp #include <rtd does not compile w \$ /opt/work/ref -B/opt/worl -B/opt/worl -B/opt/worl -B/opt/worl -B/opt/work/ref -B/opt/work/si, -c t1.cpp Some (too much to In file inclu /opt/work/bsg this scope timestamp_Sd /opt/work/si, _Timestamp_Sd /opt/work/si, Timestamp_Cd _time-resec In file inclu /opt/work/rtfe extern struc If '-std=c++11' is arm-rtems4.12</rtd 	<pre>eeen introduced in th rg/rtems/commit/?i wontfix ch just <rtems.h> d on that does not bui file: ems.h> vith git head Sfe6d0 rtems/4.12/bin/ar k/rtems/4.12/ain/ar k/spss/rtems/4.12/arm k/bsps/rtems/4.12/arm bDEBUG -std=c++11 and -wextra \ popst) of the output aded from /opt/wo from /opt/wo from /opt/wo from /opt/wo from 1.cpp: ps/rtems/4.12/arm ointime (&_ts, _t /rtems/4.12/arm- rte_t_2ero(Timest sofrtems/4.12/arm ointime (&_ts, _t /rtems/4.12/arm- te_t) (aka strue c = 0; bintime (from /opt/wo from /</rtems.h></pre>	<pre>his commit: d=5fe6d07ad5690e3d9c score oes not compile with HE idd. 7ad5690e3d9c6445ca3a m-rtems4.12/silinx_zynu /arm-rtems4.12/xilinx_zynu /arm-rtems4.12/xilinx_inx /arm-rtems4.12/xilinx_inx /arbsps/rtems/4.12/ai rk/bsps/rtems/4.12/ai rk/bsps/rtems/4.12/ai rk/bsps/rtems/4.12/ai rk/bsps/rtems/4.12/ai rtems4.12/xilinx_zynu amp_control*): -rtems4.12/xilinx_zynu ime_);</pre>	AD. AD. AD. AD. AD. AD. AGS aroto as a state of the	<pre>cc706/lib/include/rtems/score/thread.h:36:0, tc706/lib/include/rtems/score/heap.h:22, tc706/lib/include/rtems.h:31, s/score/timestamp.h: In function 'void s/score/timestamp.h: In function 'void s/score/timestamp.h: In function 'void s/score/timestamp.h:94:8: error: invalid use tc706/lib/include/rtems/score/timestamp.h:43 tc706/lib/include/rtems/score/thread.h:36, tc706/lib/include/rtems/score/thread.h:22, tc706/lib/include/rtems/trems/types.h:26, tc706/lib/include/rtems/rtems/types.h:26, tc706/lib/include/rtems.h:31,</pre>	<pre>2bintime' was not declared in 2 of incomplete type 3, 4 struct bintime}'</pre>

on	change. The table ca I suggest the MMU b	annot be changed (unless the MMU is d	isabled.	ooting no section change happen at run time b	
UII	Note, the issue only					
2776	4 years ago	fixed	score	Alexander Krutwig	Sebastian Huber	2 years ago
ummar y	SPI Framework			· · · ·		
escripti on	Development of a SF the Linux Userspace		h shall be used for f	urther SPI bus and device drivers	. The framework shall be developed using the	i2c framework as a template. It shall expo
2777	4 years ago	fixed	unspecified	Chris Johns	Chris Johns	2 years ago
ummar y escripti	Remove librtems++ This is old and there		patterns for threadi	ing and C++. We recommend you	use the new C++ standards based support.	
on • 2784 ummar y	4 years ago Add function to get t	fixed the current priority	rtems of a task by schedu	Sebastian Huber uler instance	Sebastian Huber	2 years ago
	<pre>* specified s * The current * protocols, * mechnisms. * * @param[in] * the execut * @param[in]</pre>	scheduler instan priority refle the rate-monoto task_id Identif iting task. scheduler_id Id	ce. cts temporary pri nic objects on sc ier of the task. entifier of the s	ecified task with respect to iority adjustments due to loc ome schedulers and other Use @ref RTEMS_SELF to sele scheduler instance. riority of the specified task	cking	
escripti on	<pre>* respect t * * @retval RTF * @scheduler * * @see rtems_ */ rtems_tatus_c rtems_id rtems_id</pre>	to the specified EMS_SUCCESSFUL S EMS_ILLEGAL_ON_R EMS_INVALID_ADDR EMS_INVALID_ID I	scheduler instar uccessful operati EMOTE_OBJECT Dire ESS The priority nvalid task or sc (). get_priority(d, ler_id,	nce.	asks.	
2788	4 years ago	wontfix	score	Chris Johns	Chris Johns	2 years ago
ummar	RTEMS I2C API only	defines Standard-	mode (Sm) speed a	is a default.		
у						
У					ed as Standard-mode (Sm) as defined by the I innected, and not this API.	2C standard. This is set as
У	I2C_BUS_CLOCK_DE The API should defin	EFAULT. The defau ne the speeds as de	It speed is defined b efined in the I2C sta	2c/i2c.h only defines the bus spea by the hardware, ie the devices co andard and there should be no def		
y escripti on	I2C_BUS_CLOCK_DI The API should defin speed be provided a	EFAULT. The defau ne the speeds as de nd all future driver	It speed is defined t efined in the I2C sta rs need to provide th	2c/i2c.h only defines the bus spee by the hardware, ie the devices co andard and there should be no def he speed.	onnected, and not this API. fault. Drivers like the Cadence driver for the Zy	rnq should be modified to require the bus
y escripti on	I2C_BUS_CLOCK_DE The API should defin	EFAULT. The defau ne the speeds as de nd all future driver fixed	It speed is defined b efined in the I2C sta	2c/i2c.h only defines the bus spea by the hardware, ie the devices co andard and there should be no def	onnected, and not this API.	
y escripti	I2C_BUS_CLOCK_DI The API should defin speed be provided a 4 years ago Linker sets broken w	EFAULT. The defau ne the speeds as de nd all future driver fixed	It speed is defined t efined in the I2C sta rs need to provide th	2c/i2c.h only defines the bus spee by the hardware, ie the devices co andard and there should be no def he speed.	onnected, and not this API. fault. Drivers like the Cadence driver for the Zy	rnq should be modified to require the bus
y escripti on 2790 ummar y	I2C_BUS_CLOCK_DI The API should defin speed be provided a 4 years ago	EFAULT. The defau ae the speeds as de ind all future driver fixed with GCC 7 'ml/gcc/2016-09/m	It speed is defined to efined in the I2C stats rs need to provide the score	2c/i2c.h only defines the bus spee by the hardware, ie the devices co andard and there should be no def he speed. Sebastian Huber	onnected, and not this API. fault. Drivers like the Cadence driver for the Zy	rnq should be modified to require the bus
y escripti on 2790 Immar y escripti on	I2C_BUS_CLOCK_DI The API should defin speed be provided a 4 years ago Linker sets broken w See also: https://gcc.gnu.org/ The #define MAKEGCCNU is probably the best	EFAULT. The defau ae the speeds as de ind all future driver fixed with GCC 7 ml/gcc/2016-09/m OTKNOWTHEADDR option. It works pr	It speed is defined to efined in the I2C stars need to provide the score nsg00114.html RESS(ptr) asm("":"+ robably also with lin	2c/i2c.h only defines the bus spee by the hardware, ie the devices co andard and there should be no define speed. Sebastian Huber -r"(ptr)) ik-time optimization.	onnected, and not this API. fault. Drivers like the Cadence driver for the Zy Sebastian Huber	rnq should be modified to require the bus 2 years ago
y escripti on 2790 ummar y escripti on 2795	I2C_BUS_CLOCK_DI The API should defin speed be provided a 4 years ago Linker sets broken w See also: https://gcc.gnu.org/ The #define MAKEGCCN0 is probably the best 4 years ago	EFAULT. The defau the the speeds as de ind all future driver fixed with GCC 7 (ml/gcc/2016-09/n OTKNOWTHEADDR option. It works pr fixed	III speed is defined to efined in the I2C stars read to provide the score nsg00114.html RESS(ptr) asm("":"+ robably also with lin score	2c/i2c.h only defines the bus spee by the hardware, ie the devices co andard and there should be no define speed. Sebastian Huber -r"(ptr))	onnected, and not this API. fault. Drivers like the Cadence driver for the Zy	rnq should be modified to require the bus
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y esscripti on 2790 y esscripti on 2795 y y	I2C_BUS_CLOCK_DI The API should defin speed be provided a 4 years ago Linker sets broken w See also: https://gcc.gnu.org/ The #define MAKEGCCN4 is probably the best 4 years ago Overrun Handling fo In the current impleic calling moment + th However, it may not a task are executed release pattern. The postponed jobs that Although there is no release shifting. This I refine the following cpukit/rtems/include Model B+ with corre	EFAULT. The defau are the speeds as de and all future driver fixed with GCC 7 ml/gcc/2016-09/m OTKNOWTHEADDR option. It works pr fixed r general real-time mentation, if a tass the next length of pe to a applicable for ge in a first-come-firm should be released s tandard requiren s way of handling i g four files and han e/rtems/rates/sponding BSPs.	III speed is defined to efined in the I2C stars read to provide the scor	2c/i2c.h only defines the bus spee by the hardware, ie the devices co andard and there should be no define the speed. Sebastian Huber sebastian Huber sebastian Huber where optimization. Kuan , the next call of rtems_rate_mon imption that implicit/constraint de s, e.g., soft real-time task, arbitra rus, it is sufficient to release the size se pattern of periodic/sporadic tas to to the system. addine misses, with this enhancer nsidered in academia from 90s [2] at individually. The overhead seem tems/src/ratemontimeout.c cpukil	Annected, and not this API. fault. Drivers like the Cadence driver for the Zy Sebastian Huber Gedare Bloom <gedare@> otonic_period() will only release one following adline and hard real-time model, the above model and hard real-time model, the above model the moment the first task joi sks. Since there maybe more than one postpore ment, the postponed jobs will be released with until now [3] or even on multicores as well [4] is to me negligible. cpukit/rtems/include/rtems</gedare@>	2 years ago 2 years ago 2 years ago 2 years ago 2 years ago 3 year
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y escripti 2790 mmmar y 2795 mmmar y	I2C_BUS_CLOCK_DI The API should defin speed be provided a 4 years ago Linker sets broken w See also: https://gcc.gnu.org/ The #define MAKEGCCNW is probably the best 4 years ago Overrun Handling fo In the current impleic calling moment + th However, it may not a task are executed release pattern. The postponed jobs that Although there is no release shifting. This I refine the following cpukit/rtems/include Model B+ with corre I believe this patch a in normal cases. This To demonstrate the that task deadline is 1 has a higher priori job left, and the tas! [1] Buttazzo et al., § scheduling of period Dynamic Real-Time	EFAULT. The defau the the speeds as de and all future driver fixed with GCC 7 COTKNOWTHEADDR option. It works pr fixed r general real-time mentation, if a tas the applicable for general real-time mentation, if a tas the applicable for general real-time is a first-come-first current design in should be released standard requirent s way of handling i g four files and hand e/rtems/rtems/rate sponding BSPs. as a basis is requir s enhancement is a differences, a heuu cifferences, a heuu than task 2. Tas k period will still ke Soft Real-Time Sys ic task sets with an Guarantees in an Guarantees in ang	It speed is defined to efined in the I2C stars read to provide the score	2c/i2c.h only defines the bus spee by the hardware, ie the devices co andard and there should be no def he speed. Sebastian Huber Sebastian Huber 	onnected, and not this API. fault. Drivers like the Cadence driver for the Zy fault. Drivers like the Cadence driver for the Zy Sebastian Huber Sebastian Huber Gedare Bloom <gedare@> otonic_period() will only release one following adline and hard real-time model, the above may deadline, mixed-criticality system [1-4]. It i econd task job at the moment the first task joi sks. Since there maybe more than one postport ment, the postponed jobs will be released with until now [3] or even on multicores as well [4] is to me negligible. cpukit/rtems/include/rtemst/rtems/src/ratemonperiod.c I have tested the badels. This enhancement only affect those time long with RTSS'16 this year [5]. withe Diagnation of the enhancement on the form the form the form the securities of the enhancement on the form the securities of the enhancement on the securities of the enhancement on the securities of the securities of the enhancement on the securities of the secures of the second the second the securities of the securities of th</gedare@>	2 years ago 2 years ago 2 years ago job and manipulate the task period with the echanism is okay. s usually assumed that multiple task jobs b finishes according to a strictly periodic teed jobs due to the preemption, these the correct number without periodic cl. s/rtems/ratemon.h enhancement on Qemu and Raspberry Pi out cases without changing any behaviour nt: Given two tasks with implicit deadline tks, and task 2 has 1000 ticks. Assume Ta tinuously released till there is no postpone 15 [2] Lehoczky et al., Fixed priority eorg von der Brüggen et al., Systems with nds for sporadic arbitrary-deadline tasks
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```
The scheduler configuration is done at link-time. In order to support run-time re-configuration add functions to dd/remove processors to/from a scheduler instance.
                   * @brief Adds a processor the set of processors owned by the scheduler.
                  * Must be called from task context. This operation obtains and releases the
                  * objects allocator lock
                  * @param[in] scheduler_id Identifier of the scheduler.
* @param[in] cpu_index Index of the processor to add.

    Øretval RTEMS_SUCCESSFUL Successful operation.
    Øretval RTEMS_INVALID_ID Invalid scheduler identifier.
    Øretval RTEMS_NOT_CONFIGURED The processor is not configured to be used by

                  * the application.
* @retval RTEMS_INCORRECT_STATE The processor is configured to be used by
                       the application, however, it is not available.
                  rtems_status_code rtems_scheduler_add_processor(
                   rtems_id scheduler_id,
uint32_t cpu_index
                 );
Descripti
   on
                   * @brief Removes a processor from set of processors owned by the scheduler.
                  * Must be called from task context. This operation obtains and releases the
                  * objects allocator lock. Removing a processor from a scheduler is a complex
* operation that involves all tasks in the system.
                  * @param[in] scheduler_id Identifier of the scheduler.
* @param[in] cpu_index Index of the processor to add.
                   * @retval RTEMS_SUCCESSFUL Successful operation.

    * @retval NTEMS_INVALID_ID Invalid scheduler identifier.
    * @retval RTEMS_INVALID_NUMBER The processor is not owned by the scheduler.
    * @retval RTEMS_RESOURCE_IN_USE The set of processors owned by the scheduler

                       would be empty after the processor removal and there exists a non-idle
                 rtems_status_code rtems_scheduler_remove_processor(
    rtems_id scheduler_id,
                   uint32_t cpu_index
#2798 3 years ago
                                    fixed
                                                         posix
                                                                                  Sebastian Huber
                                                                                                                        Sebastian Huber
                                                                                                                                                                                             2 years ago
            Fix POSIX timer interval
Summar
            See also:
            https://lists.rtems.org/pipermail/users/2016-October/030714.html
                Just back to RTEMS after a long time. I've build the last version (Git) with last compiler (4.12) and looks like a simple POSIX timer doen't work anymore... It should display "Signal 14" every second but period is veryyyy short...and display very fast :(
Descripti
                Any idea? any change in the way to use POSIX with RTEMS?
                See my program attached (it works fine with Linux).
                 I have tested with Raspberry Pi and QEMU/i386.
                thx by advance
#2800 3 years ago
                                                 arch/powerpc
                                                                                 Joel Sherrill
                                                                                                                        Sebastian Huber
                                   fixed
                                                                                                                                                                                             2 years ago
            qoriq variants failing to build
Summar
   У
            The following qoriq variants fail to build:
            powerpc-gorig core 0 powerpc-gorig core 1 powerpc-gorig p1020rdb
            All fail with this error:
                  ../../../../../.qoriq_core_0/lib/include/bsp/qoriq.h:407:3: error: conflicting types for 'qoriq_gpio'
                  } qoriq_gpio;
                  ../../../../.././qoriq_core_0/lib/include/bsp/qoriq.h:184:3: note: previous declaration of 'qoriq_gpio' was here
                  } qoriq_gpio;
Descripti
           Found during full build sweep based on this commit:
   on
                 commit bb9f09f34c9bdcf4d2631a1fd317bcefd8426efb
                 Author: Sebastian Huber <sebastian.huber@embedded-brains.de>
Date: Mon Oct 31 13:07:34 2016 +0100
                      posix: Fix timer interval
                     Do not overwrite timer interval with initial interval in
                      _POSIX_Timer_Insert().
                     Close #2798.
                                                  lib/dl
#2802 3 years ago fixed
                                                                                  Sebastian Huber
                                                                                                                        Chris Johns
                                                                                                                                                                                             2 years ago
Summar Test "libdl (RTL) 5" fails on SPARC targets
```

	On GR740 I get:					
			6edc in /dl-o5.o rame, syms:.symtab			
				dec type:3 off:00000013 ad	dend:0	
	CPU 0: IU ir	n error mode (t	$t = 0 \times 07$, mem addr	ess not aligned)		
	0x000	lfa9c: c404000		<rtems_rtl_elf_relocate_re< th=""><th>La+204></th><th></th></rtems_rtl_elf_relocate_re<>	La+204>	
	CPU 1: Power CPU 2: Power					
escripti)	CPU 3: Power	down mode				
on	On GR712RC I get:					
			0x40087108 in /dl-	05.0		
	rtl: relocati	ion: .rela.gcc_	except_table.excep	tion_dl, syms:.symtab		
			tion(32)=40060ae8 0x400871b4 in /dl-	type:3 off:00000034 addend	: 0	
	rtl: relocati	on: .rela.eh_f	rame, syms:.symtab		1 1- 0	
	[rt1: re1a: s]	/m:gxx_person	aiity_v0(20)=40001	dec type:3 off:00000013 ad	iena:U	
	Target resets now.					
+2803	3 years ago	fixed	score	Sebastian Huber	Sebastian Huber	2 years ago
ummar y	Get rid of CPU_BIG	_ENDIAN and CP	U_LITTLE_ENDIAN			
escripti	The remaining uses	s of the CPU port	defines CPU_BIG_ENI	DIAN and CPU_LITTLE_ENDIAN	hould be replaced by the BSD (also available in	n glibc) BYTE_ORDER.
on	_	-				
+2805	3 years ago	fixed	arch/powerpc	Sebastian Huber	Sebastian Huber	2 years ago
ummar	Use SPRG0 on Pow	erPC for current p	per-CPU control (SMP	only)		
y	Add CPU Get our	rent per CPU co	ntrol() on SMP config	urations as an ontimization for P	owerPC. Use SPRG0 for the current per-CPU co	ntrol. This reduces the code size a bit and is
escripti on	slightly faster in so				enter di ese en la di la carrent per ero tu	
+2806	3 years ago	fixed	doc	Joel Sherrill	Sebastian Huber	4 weeks ago
ummar	Undocumented con	fdefs.h Configure	Options			
У						
	The following const	ants in confdefs.	n that are available fo	r users to use are not defined in	the Sphinx documentation.	
					CATION_NEEDS_IDE_DRIVER turns into this. S E_APPLICATION_NEEDS_ATA_DRIVER should it	
					IDE task priority" configure option.	- proce
escripti			_SERVERS - documen			
on			M_SIZE - do we want ECEIVE_SERVER_STA	users to define this anymore? CK		
			IX_KEY_VALUE_PAIRS		a old and new TCD/ID stacks?	
				ed by telnetd. Is this the same of nent for 4.11, not master		
				/ER - internal only, do not docum	nent	
2807	3 years ago	fixed	admin	Sebastian Huber	Amar Takhar	2 years ago
ummar	rtems-docs reposit	ory is not known	to trac			
У			:			
2808	3 years ago	fixed	score	Sebastian Huber	Sebastian Huber	2 years ago
		de rtems_interrup	ot_frame			
	Conditionally provid					
У		rrupt_frame only	if CPU_ISR_PASSES_	FRAME_POINTER is defined to T	RUE.	
У		rrupt_frame only	if CPU_ISR_PASSES_	FRAME_POINTER is defined to T	RUE.	
y escripti on	Provide rtems_inte 3 years ago	fixed	score	Sebastian Huber	RUE. Sebastian Huber	2 years ago
y escripti on •2809 ummar	Provide rtems_inte 3 years ago	fixed		Sebastian Huber	-	2 years ago
y escripti on 2809	Provide rtems_inte 3 years ago Reduce interrupt la	fixed tency on SMP cor	score	Sebastian Huber	-	2 years ago
y escripti on •2809 ummar	Provide rtems_inte 3 years ago Reduce interrupt la Currently we have	fixed tency on SMP cor this situation:	score figurations during th	Sebastian Huber read dispatch	Sebastian Huber	
y escripti on •2809 ummar	Provide rtems_inte 3 years ago Reduce interrupt la Currently we have	fixed tency on SMP cor this situation:	score figurations during th	Sebastian Huber read dispatch	-	
y escripti on •2809 ummar	Provide rtems_inte 3 years ago Reduce interrupt la Currently we have https://docs.rtems Dispatch-Details	fixed tency on SMP cor this situation: .org/doc-current/	score sfigurations during the share/rtems/html/c_u	Sebastian Huber read dispatch user/Symmetric-Multiprocessing-	Sebastian Huber	etric-Multiprocessing-Services-Thread-
y escripti on •2809 ummar	Provide rtems_inte 3 years ago Reduce interrupt la Currently we have https://docs.rtems Dispatch-Details "On SMP systems, on another process	fixed tency on SMP cor this situation: .org/doc-current/ scheduling decisi or happens not ir	score share/rtems/html/c_u	Sebastian Huber read dispatch user/Symmetric-Multiprocessing- must be propagated to other pro everal thread dispatch requests r	Sebastian Huber Services-Thread-Dispatch-Details.html#Symm pressors through inter-processor interrupts. So hight be in the air and it is possible that some of	etric-Multiprocessing-Services-Thread- , a thread dispatch which must be carried or
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y escripti on •2809 ummar	Provide rtems_inte 3 years ago Reduce interrupt la Currently we have https://docs.rtems Dispatch-Details "On SMP systems, on another process corresponding proc • the executir • the heir thre • an boolean	fixed tency on SMP cor this situation: .org/doc-current/ scheduling decision tor happens not in tessor has time to ng thread, ead, and flag indicating if a	score figurations during the share/rtems/html/c_u ons on one processor istantaneous. Thus see deal with them. The thread dispatch is ne	Sebastian Huber read dispatch user/Symmetric-Multiprocessing- must be propagated to other pro everal thread dispatch requests n thread dispatch mechanism uses	Sebastian Huber Services-Thread-Dispatch-Details.html#Symm decessors through inter-processor interrupts. So hight be in the air and it is possible that some of three per-processor variables,	etric-Multiprocessing-Services-Thread- , a thread dispatch which must be carried or of them may be out of date before the
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y escripti on 2809 y y escripti on	Provide rtems_inte 3 years ago Reduce interrupt la Currently we have https://docs.rtems Dispatch-Details "On SMP systems, on another process corresponding proc • the executir • the heir thre • an boolean Updates of the heir most one processo post-switch actions The current implem on at most one pro mark that a thread (the boolean indica threads migrate at used. Currently the prolonged since on It is difficult to avo executing, we muss interrupts during th The problematic sit another processor) scheduler (an exce In case the assigned instance, it will rear requests, however, also be provoked b Add an interrupt fr 3 years ago Remove sparc/sis E	fixed tency on SMP cor this situation: .org/doc-current/ scheduling decisid for happens not in tessor has time to the system. T is use a per-process the thread and the t is thread and the t is use a per-process tor is basically a once. Otherwise - te ontext switch is to rose sor in the syst to ris the system. T is use a per-process the system. T is concervent to system to ris the system. T is concervent to system to ris basically a once. Otherwise - to ris concervent to is transition, then the thot use its thread is transition, then the this case the deprocessor differ sign the process can lead to this a y interrupts which ame to the per-Cl fixed 3SP variant following thread, org/pipermail/dev	score figurations during the share/rtems/html/c_u ons on one processor istantaneous. Thus so a deal with them. The thread dispatch is ne haread dispatch necess haread dispatch necess haread context is par- sor lock. This implem d dispatching has sor- tem at a time. This is ger executing and wa TTAS lock). So, there some supervising enti- s performed with inter o wait for another pro- the interrupt latency d stack to store such- n we have to provide case we have a thread interrupts on this oth luler with thread processor or sor to keep the alread- situation. Explicit threan n suspend and resume PU control which can latency arch/sparc	Sebastian Huber read dispatch user/Symmetric-Multiprocessing- must be propagated to other pro- everal thread dispatch requests re- thread dispatch mechanism uses eccessary or not. sary indicator are synchronized wo protected by a TTAS lock embedd tentation turned out to be quite of me implications with respect to the accomplished with a boolean inc its that the heir context stopped is one point in time in which a p ity is necessary to prevent deadi rrupts disabled. Thus in case the occessor to make progress. since interrupts normally store the an interrupt context. We cannot an alternative thread independent d which executes with thread dis er processor are disabled until the essor affinity support) tries to a on which the thread already exet by executing thread in place. The ad migrations may occur due to the threads multiple times and pro- be used during context switches Joel Sherrill	Sebastian Huber Services-Thread-Dispatch-Details.html#Symm coessors through inter-processor interrupts. So hight be in the air and it is possible that some of the per-processor variables, is explicit memory barriers without the use of fed in the context to ensure that it is used on a efficient and no lock contention was observed in the interrupt latency. It is crucial to preserve the icator in the thread context. The processor arc execution before it restores the heir context an crocessor is without a thread. This is essential to books. Such a global supervisor would lead to so heir thread is currently executing on another p the context of the interrupted thread on its stack use the heir stack before it stopped execution th stack for interrupts in this time frame. This is boatching disabled and should execute on anoth the thread enables thread dispatching and starts oid such a situation and checks if a new sched duce stale asynchronous thread dispatch reque on SMP configurations. Joel Sherrill	etric-Multiprocessing-Services-Thread- a thread dispatch which must be carried or of them may be out of date before the locks. A thread can be an heir thread on at at most one processor at a time. The thread n the test suite. e system invariant that a thread can execute hitecture specific context switch code will nd resumes execution of the heir thread o avoid cyclic dependencies in case multiple alability problems so this approach is not rocessor, the time of disabled interrupts is k. In case a thread is marked as not on another processor. If we enable ssue needs further investigation. er processor (e.g. it is an heir thread on the thread dispatch sequence. The uled thread already executes on a processor to such a situation. Explicit thread migration luler instance changes. The situation can sts in the system."
escripti on 2809 ummar y 2810 ummar y escripti on	Provide rtems_inte 3 years ago Reduce interrupt la Currently we have https://docs.rtems Dispatch-Details "On SMP systems, on another processo corresponding proc • the executir • the heir thre • an boolean Updates of the heir most one processo post-switch actions The current implem on at most one pro mark that a thread (the boolean indica threads migrate at used. Currently the prolonged since on It is difficult to avo executing, we muss interrupts during th The problematic sit another processor) scheduler (an exce In case the assigned instance, it will rea requests, however, also be provoked b Add an interrupt fr 3 years ago Remove sparc/sis f	fixed tency on SMP cor this situation: .org/doc-current/ scheduling decisid for happens not in tessor has time to the system. T is use a per-process the thread and the t is thread and the t is use a per-process tor is basically a once. Otherwise - te ontext switch is to rose sor in the syst to ris the system. T is use a per-process the system. T is concervent to system to ris the system. T is concervent to system to ris basically a once. Otherwise - to ris concervent to is transition, then the thot use its thread is transition, then the this case the deprocessor differ sign the process can lead to this a y interrupts which ame to the per-Cl fixed 3SP variant following thread, org/pipermail/dev	score figurations during the share/rtems/html/c_u ons on one processor istantaneous. Thus se deal with them. The thread dispatch is ne hread dispatch necess he thread context is j sor lock. This implem d dispatching has sor- tem at a time. This is ger executing and wa TTAS lock). So, there some supervising enti- s performed with inter o wait for another pro- the interrupt latency ad stack to store such- n we have to provide case we have a thread interrupts on this oth luler with thread proc- rs from the processor ors to keep the alread situation. Explicit thread neuronal and resume- PU control which can l arch/sparc	Sebastian Huber read dispatch user/Symmetric-Multiprocessing- must be propagated to other pro- everal thread dispatch requests re- thread dispatch mechanism uses eccessary or not. sary indicator are synchronized wo protected by a TTAS lock embedd tentation turned out to be quite of me implications with respect to the accomplished with a boolean inc its that the heir context stopped is one point in time in which a p ity is necessary to prevent deadi rrupts disabled. Thus in case the occessor to make progress. since interrupts normally store the an interrupt context. We cannot an alternative thread independent d which executes with thread dis er processor are disabled until the essor affinity support) tries to a on which the thread already exet by executing thread in place. The ad migrations may occur due to the threads multiple times and pro- be used during context switches Joel Sherrill	Sebastian Huber Services-Thread-Dispatch-Details.html#Symm coessors through inter-processor interrupts. So hight be in the air and it is possible that some of the per-processor variables, is explicit memory barriers without the use of fed in the context to ensure that it is used on a efficient and no lock contention was observed in the interrupt latency. It is crucial to preserve the icator in the thread context. The processor arc execution before it restores the heir context an crocessor is without a thread. This is essential to books. Such a global supervisor would lead to so heir thread is currently executing on another p the context of the interrupted thread on its stack use the heir stack before it stopped execution th stack for interrupts in this time frame. This is boatching disabled and should execute on anoth the thread enables thread dispatching and starts oid such a situation and checks if a new sched duce stale asynchronous thread dispatch reque on SMP configurations. Joel Sherrill	etric-Multiprocessing-Services-Thread- of them may be out of date before the locks. A thread can be an heir thread on at it most one processor at a time. The thread in the test suite. e system invariant that a thread can execute hitecture specific context switch code will nd resumes execution of the heir thread o avoid cyclic dependencies in case multiple ialability problems so this approach is not rocessor, the time of disabled interrupts is k. In case a thread is marked as not on another processor. If we enable ssue needs further investigation. er processor (e.g. it is an heir thread on the thread dispatch sequence. The uled thread already executes on a processor rocessor set managed by this scheduler to such a situation. Explicit thread migratior luler instance changes. The situation can sts in the system."

У						
					pts disabled, since this prevents delivery of inter-pr	ocessor interrupts. This could lead to
	executing threads	which are not all	owed to execute resultir	ng in undefined behaviour.		
	The ARM Cortex-I	4 port has a simila	ar problem, since the int	errupt state is not a part o	f the thread context.	
	Add a new CPU p	ort function:				
	/**					
		eturns true if e returns false		ed in the specified ISR	level,	
Descripti	* * @param[i	n] level The IS	R level.			
on	*					
		true Interrupts false Otherwise	are enabled in the .	ISR level.		
	*/	F POUTINE bool	CPU ISR Is enabled(wint32 + level)		
	{	-		dimesz_c level)		
	return fal	lse;				
	lise this function	to ensure that T	hread Do dispatch() is (called with an interrupt leve	el with enabled interrupts, otherwise call _Terminate	
#2816		fixed	arch/arm	Joel Sherrill	Sebastian Huber	2 years ago
Summar	Many ARM BSPs h	ave Static Assert				, 3
У						
	Hi					
	With the recent c	ommits, many BS	Ps on the master do not	build. They error out with	this static assert:	
	In file included from	om///cpuk	it///gumstix/lib/inc	lude/rtems/score/types.h:2	23:0,	
					///cpukit///gumstix/lib/include/rtems/sys	tem.h:23, from
			src///cpukit/score/cpu			DICRATCH DICLOS
escripti				seaets.h:241:5: error: stat	c assertion failed: "ARM_CONTEXT_CONTROL_ISR_	DISPATCH_DISABLE"
on	_Static_as	sert(cond, # msg)			
	/////////////	/rtems/c/src//	/cpukit/score/cpu/arm/c	pu.c:54:3: note: in expans	ion of macro 'RTEMS_STATIC_ASSERT'	
	RTEMS_ST	ATIC_ASSERT(
	The list of BSPs is	:				
					637_v6 lpc2362 lpc23xx_tli800 lpc24xx_ea lpc24xx	
	lpc24xx_ncs_rom smdk2410	_int lpc24xx_plx8	00_ram lpc24xx_plx800	_rom_int lpc32xx_mzx lpc	32xx_mzx_stage_1 lpc32xx_mzx_stage_2 lpc32xx_	phycore raspberrypi rtl22xx rtl22xx_t
#2817		fixed	unspecified	Joel Sherrill	Sebastian Huber	2 years ago
Summar	All Blackfin BSPs	do not Compile or	n Master			
У	Recent changes to					
	Wimplicit-function	-declaration -Wst	<pre>trict-prototypes -Wneste/rtems/c/src// p/lib/include -DASM -O2</pre>	d-externs -MT libscorecpu_ /cpukit/score/cpu/bfin 2 -g -ffunction-sections -fda	Stamp/lib/include -O2 -g -ffunction-sections -fdata- a-cpu.o -MD -MP -MF .deps/libscorecpu_a-cpu.Tpo '_ cpu.c bfin-rtems4.12-gccpipe -DHAVE_CONFIC ta-sections -Wall -Wmissing-prototypes -Wimplicit- s - Wimplicit-function-declaration -Wstrict-prototype	-c -o libscorecpu_a-cpu.o test -f 'cpu. 5_H -I// function-declaration -Wstrict-prototypes -
Descripti on	Wimplicit-function	n-declaration -Wst //bf537Stam -O2 -g -ffunction- MP -MF .deps/libs //.bf537Stamp //bf537Stamp pukit///bf537Stamp	<pre>rrict-prototypes -Wneste //rtems/c/src// p/lib/include -DASM -02 sections -fdata-sections corecpu_a-cpu_asm.Tpo a/c/src///cpukit/ b/lib/include/rtems/score 7Stamp/lib/include/rtem</pre>	d-externs -MT libscorecpu_ /epukit/score/epu/bfin 2 g -ffunction-sections -fda - wall -Wmissing-prototypu 0 - c - o libscorecpu_a-cpu_a (score/epu/bfin/')cpu_as e/cpu.h:670: Error: syntax (cpu.h:670: Error: ./././ hs/score/cpu.h:671: Error:	a-cpu.o -MD -MP -MF.deps/libscorecpu_a-cpu.Tpo ''cpu.c bfin-rtems4.12-gccpipe -DHAVE_CONFIC ta-sections -Wall -Wmissing-prototypes -Wimplicit- iss -Wimplicit-function-declaration -Wstrict-prototype sm.o test -f 'cpu_asm.S' echo m.S///cpukit///bf537Stamp/lib/include error. Input text was static. /cpukit///bf537Stamp/lib/include/rtems/score ///cpukit///bf537Stamp/lib/include/rtem	-c -o libscorecpu_a-cpu.o test -f 'cpu. j_H -I.// function-declaration -Wstrict-prototypes - es -Wnested-externs -MT libscorecpu_a- /rtems/score/cpu.h: Assembler messages e/cpu.h:671: Error: syntax error. Input te: ns/score/cpu.h:672: Error: syntax error.
	Wimplicit-function echo '// Wnested-externs cpu_asm.o -MD - '///cpukit/ 	n-declaration -Wst //.bf537Stam O2 -g -ffunction- MP -MF .deps/libs //./bf537Stam //.bf537Stam pukit//.s/bf53	<pre>trict-prototypes -Wneste //ttems/c/src// p/lib/include -DASM -02 sections -fdata-sections corecpu_a-cpu_asm.Tpo s/c/src//.cpukit/ j/lib/include/rtems/score b/lib/include/rtems/score 7Stamp/lib/include/rtem ukit///bf537Stamp/</pre>	d-externs -MT libscorecpu_ /cpukit/score/cpu/bfin 2 -g -ffunction-sections -fda -wall -Wmissing-prototype -c -o libscorecpu_a-cpu_a (score/cpu/bfin/' cpu_as /cpu.h:670: Error: syntax /cpu.h:670: Error://. lib/include/rtems/score/cpi	a-cpu.o -MD -MP -MF.deps/libscorecpu_a-cpu.Tpo ''cpu.c bfin-rtems4.12-gccpipe -DHAVE_CONFIC ta-sections -Wall -Wmissing-prototypes -Wimplicit- iss -Wimplicit-function-declaration -Wstrict-prototype sm.o test -f 'cpu_asm.S' echo m.S/.//.pt537Stamp/lib/include error. Input text was static. ./cpukit///bf537Stamp/lib/include/rtems/score	-c -o libscorecpu_a-cpu.o test -f 'cpu. j_H -I/ function-declaration -Wstrict-prototypes - as -Wnested-externs -MT libscorecpu_a- /rtems/score/cpu.h: Assembler messages e/cpu.h:671: Error: syntax error. Input te: ns/score/cpu.h:672: Error: syntax error. /lib/include/rtems/score/cpu.h:673: Error:
on	Wimplicit-function echo '// Wnested-externs cpu_asm.o -MD - '///cpukit/ 	n-declaration -Wst //.bf537Stam O2 -g -ffunction- MP -MF .deps/libs //./bf537Stam //.bf537Stam pukit//.s/bf53	<pre>trict-prototypes -Wneste //ttems/c/src// p/lib/include -DASM -02 sections -fdata-sections corecpu_a-cpu_asm.Tpo s/c/src//.cpukit/ j/lib/include/rtems/score b/lib/include/rtems/score 7Stamp/lib/include/rtem ukit///bf537Stamp/</pre>	d-externs -MT libscorecpu_ /cpukit/score/cpu/bfin 2 -g -ffunction-sections -fda -wall -Wmissing-prototype -c -o libscorecpu_a-cpu_a (score/cpu/bfin/' cpu_as /cpu.h:670: Error: syntax /cpu.h:670: Error://. lib/include/rtems/score/cpi	a-cpu.o -MD -MP -MF.deps/libscorecpu_a-cpu.Tpo 'cpu.c bfin-rtems4.12-gccpipe -DHAVE_CONFIG ta-sections -Wall -Wmissing-prototypes -Wimplicit- ss -Wimplicit-function-declaration -Wstrict-prototype sm.o [test -f 'cpu_asm.S' echo m.S///cpukit///bf537Stamp/lib/include error. Input text was static. /cpukit///bf537Stamp/lib/include/rtems/score ///cpukit///bf537Stamp/lib/include/rtem 1.h:672: Error://./cpukit//./bf537Stamp/lib/include/rtem	-c -o libscorecpu_a-cpu.o test -f 'cpu. j_H -I/ function-declaration -Wstrict-prototypes - as -Wnested-externs -MT libscorecpu_a- /rtems/score/cpu.h: Assembler messages e/cpu.h:671: Error: syntax error. Input te: ns/score/cpu.h:672: Error: syntax error. /lib/include/rtems/score/cpu.h:673: Error:
on # 2818 Gummar	Wimplicit-function echo '// I/././cpukit/ Wnested-externs cpu_asm.o -MD - '//./cpukit/ ,./././cpukit/ was {//./cpukit/ Input text was re syntax error. Inpu	n-declaration -Wst //bf537Stam -O2 -g -ffunction- MP -MF .deps/libs //./bf537Stam //./bf537Stam pukit//./bf537stam pukit//./bf53 turn//.bf53 turn//.bf53 turn//.bf53 turn//.bf53	rrict-prototypes -Wneste //rtems/c/src// p/lib/include -DASM -02 sections -fdata-sections corecpu_a-cpu_asm.Tpo s/c/src///opukit/ b/lib/include/rtems/score 7Stamp/lib/include/rtems/ kit///bf537Stamp/ //./cpukit///bf53	d-externs -MT libscorecpu_ /epukit/score/epu/bfin 2 -g -ffunction-sections -fda -Wall -Wmissing-prototypu -c -o libscorecpu_a-cpu_a /score/cpu/bfin' /cpu_as /cpu.h:670: Error: syntax /cpu.h:670: Error://. is/score/cpu.h:671: Error: lib/include/rtems/score/cpi /7stamp/lib/include/rtems/	<pre>a-cpu.o -MD -MP -MF.deps/libscorecpu_a-cpu.Tpo ''cpu.c bfin-rtems4.12-gccpipe -DHAVE_CONFIC ta-sections -Wall -Wmissing-prototypes -Wimplicit- iss -Wimplicit-function-declaration -Wstrict-prototype sm.o test -f 'cpu_asm.S' echo m.S/./././cpukit//././bf537Stamp/lib/include error. Input text was static. /cpukit///bf537Stamp/lib/include/rtems/score ///cpukit///bf537Stamp/lib/include/rtems/ /bf537Stamp/lib/include/rtems/score ///pukit///bf537Stamp/lib/include/rtems/ </pre>	-c -o libscorecpu_a-cpu.o test -f 'cpu. _H -I.// function-declaration -Wstrict-prototypes - ss -Wnested-externs -MT libscorecpu_a- /rtems/score/cpu.h: Assembler messages e/cpu.h:671: Error: syntax error. Input ten ts/score/cpu.h:672: Error: syntax error. /lib/include/rtems/score/cpu.h:673: Error: _a-cpu_asm.o] Error 1
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on #2818 Summar y Pescripti on #2819 Summar y Pescripti	<pre>Wimplicit-function II echo '// I//.cpukit// Wnested-externs cpu_asm.o -MD - '//./.vpukit/ was {///cpukit/ was {///cpukit/ was {///cpukit/ was {///cpukit/ as yntax error. Input 3 years ago NIOS2 Does Not 0 3 years ago NIOS2 Does Not 0 3 years ago powerpc-ss555 do Recent changes b gmake[6]: Enteri rtems4.12-gcc -B -g -fno-keep-inlin/////.r from/./////////</pre>	I-declaration -Wst //.bf537Stam -02 - g -ffunction- MP -MF .deps/libs //bf537Stam pukit//.bf537Stam pukit//.bf537Stam pukit//.bf537Stam pukit//./cpu it text was }/. fixed Compile on Master /rtems/c/src// /rtems/c/src// atus & NIOS2_ST /rtems/c/src// fixed Des not compile on roke this configur ng directory '.dat //.ss555/lib/ii /lib/include/rtems errupt_frame; om///ss555/lib/ii	rict-prototypes -Wneste (/rtems/c/src// p/lib/include -DASM -O2 sections -fdata-sections corecpu_a-cpu_asm.Tpo s/c/src///cpukit/ j/lib/include/rtems/score 7stamp/lib/include/rtems/score r /cpukit/score/cpu/nios2/ /cpukit/score/cpu/nios2/ /cpukit/score/cpu/nios2/ ATUS_IL_MASK) >> NIC /cpukit/score/cpu/nios2/ ATUS_IL_MASK) >> NIC /cpukit/score/cpu/nios2/ /cpukit/score/cpu/nios2/ arch/powerpc n master ration: rathome/joel/rtems-4.11 /ib/ -specs bsp_specs -Wmissing-prototypes stsuites/samples/hello/i nclude/rtems/confdefs.h s/score/cpuimpl.h:196:3 555/lib/include/bsp/irq.h nclude/bsp.h:31, from	d-externs -MT libscorecpu_ /~pukit/score/cpu/bfin 2-g-ffunction-sections-fda -wall -Wmissing-prototyp -c-o libscorecpu_a-cpu_a 'score/cpu/bfin'/cpu_as /cpu.h:670: Error://. ks/score/cpu.h:671: Error: lib/include/rtems/score/cpi /rstamp/lib/include/rtems/ Joel Sherrill //nios2-isr-get-level.c: In fu /nios2-isr-get-level.c: 26:16 /nios2-isr-get-level.c: 26:16 /	a-cpu.o -MD -MP -MF .deps/libscorecpu_a-cpu.Tpo ''cpu.c bfin-rtems4.12-gccpipe -DHAVE_CONFIC ta-sections -Wall -Wmissing-prototypes -Wimplicit- is -Wimplicit-function-declaration -Wstrict-prototype sm.o test -f 'cpu_asm.S' echo m.S/.//./pt537Stamp/lib/include/rtems/score .///.pukit///bf537Stamp/lib/include/rtems/score .///.pukit///bf537Stamp/lib/include/rtems/score .///.pukit///bf537Stamp/lib/include/rtems/score .///pukit///bf537Stamp/lib/include/rtems/score .///pukit///bf537Stamp/lib/include/rtems/ score/cpu.h:673: Error: gmake[7]: * [libscorecpu .h:673: Error: gmake[7]: * [libscorecpu .sebastian Huber score/cpu.h:673: undeclared (first use in this function == 0; :: note: each undeclared identifier is reported only c warning: control reaches end of non-void function Sebastian Huber /build-powerpc-ss555-rtems/powerpc-rtems4.12/c/ .i. r.1././././././rtems/c/src//testsuites/sa on -Wstrict-prototype -Wnested-externs -MT init.o .///.s555/lib/include/rtems/score/percpu.h: tems/c/src//.testsuites/samples/hello/init.c:51: r 'CPU_Interrupt_frame' ///testsuites/samples/hello/init.c:17:	-c -o libscorecpu_a-cpu.o test -f 'cpu.
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	Recent changes broke all b	uilds:					
					iii/lib/include -mcpu=ultrasparc3 -DUS3 -DSUN4U -g -O2 -ffunction-se		
	Wall -Wmissing-prototypes -Wimplicit-function-declaration -Wstrict-prototypes -Wnested-externs -MT libscorecpu_a-sparc64-exception-frame-print.o -MD -MP -MF .deps/libscorecpu_a-sparc64-exception-frame-print.Tpo -c -o libscorecpu_a-sparc64-exception-frame-print.o test -f 'sparc64-exception-frame-print.c' echo						
					c64-exception-frame-print.c sparc64-rtems4.12-gccpipe -DHAVE_CC		
	I././//usiii/lib/include -mcpu=ultrasparc3 -DUS3 -DSUN4U -g -O2 -ffunction-sections -fdata-sections -Wall -Wmissing-prototypes -Wimplicit-function-declaration - Wstrict-prototypes -Wnested-externs -MT/no_cpu/libscorecpu_a-cpucounterread.o -MD -MP -MF/no_cpu/.deps/libscorecpu_a-cpucounterread.Tpo -c -o/no_cpu/libscorecpu_a- cpucounterread.o test -f '/no cpu/cpucounterread.c' echo						
Descripti							
on	·////////.	./rtems/c,	/src///cpukit	/score/cpu/sparc64/'/no	p_cpu/cpucounterread.c In file included from		
					d.c:15:0:///cpukit///usiii/lib/include/rtems/score/cpu.h: In 0:12: error: 'psr' undeclared (first use in this function)	n function	
				iciude/items/score/cpu.ii.759	.12. error. psr undeclared (inst use in this function)		
	return (psr & SPAR)						
					red identifier is reported only once for each function it appears in gmak		
	////cpukit////				ontext.Tpo .deps/libscorecpu_a-context.Po In file included from		
#2821	3 years ago inva		unspecified	Joel Sherrill	Sebastian Huber	2 years ago	
	No BSPs Build on Master						
У	* 1						
	I have the latest tools.						
	All BSPs appear to fail like	:his:					
					clude -meabi -mcpu=603e -msdata=sysv -fno-common -Dppc603e -O2		
					<pre>nction-declaration -Wstrict-prototypes -Wnested-externs -MT src/libscon ait.o test -f 'src/smpbarrierwait.c' echo</pre>	re_a-smpbarrierwait.o -	
	'////rtem	us/c/src/.	.//cpukit/score	<pre>src/smpbarrierwait.c powe</pre>	erpc-rtems4.11-gccpipe -DHAVE_CONFIG_H -II//cpukit//		
					s -ffunction-sections -fdata-sections -Wall -Wmissing-prototypes -Wimp deps/libscore_a-kern_tc.Tpo -c -o src/libscore_a-kern_tc.o test -f 's		
					pc-rtems4.11-gccpipe -DHAVE_CONFIG_H -II//cpukit///p		
	mcpu=603e -msdata=sysv	-fno-comm	non -Dppc603e -O2	-g -fno-keep-inline-functions	-ffunction-sections -fdata-sections -Wall -Wmissing-prototypes -Wimp	licit-function-declaration	
					'.deps/libscore_a-libatomic.Tpo -c -o src/libscore_a-libatomic.o test - nv -f src/.deps/libscore_a-semaphore.Tpo src/.deps/libscore_a-semaph		
					//cpukit/score/src/libatomic.c:19:32: fatal error: machine/_libatomi		
Descripti	directory						
on	#include <machine <="" td=""><td>_libatomic.</td><td>.h></td><td></td><td></td><td></td></machine>	_libatomic.	.h>				
	compilation terminated. gm	nake[6]: *	[src/libscore_a-lib	batomic.o] Error 1 In file ir	ncluded from /data/home/joel/rtems-4.11-work/tools/4.11/p	owerpc-	
	rtems4.11/include/sys/	param.h:8	39:0,				
	from/////r	tems/c/src,	:///cpukit/score/sr	rc/kern_tc.c:48:			
	//cpukit///psim/lib/	include/sys	s/uio.h:41:9: error:	unknown type name 'ssize t'			
	typedef <u>ssize_t_ssize</u>						
			aluia hulfulu arraru	unknown twno namo loff t			
	//cpukit///psim/lib/	include/sys	5/010.11.46.9. 01101.	unknown type name <u>on t</u>			
	typedef <u>off_t off_t;</u>						
	//cpukit///psim/lib/	include/sys	s/uio.h:46:17: error	: conflicting types for 'off_t'			
	typedef <u>off_t off_t;</u>						
#2822	3 years ago fixed	1	unspecified	Joel Sherrill	Sebastian Huber	2 years ago	
Summar	m32csim does not build on	master				, ,	
У							
	In file included from//cp	oukit///	./m32csim/lib/includ	de/rtems/score/types.h:22:0,			
	from//cpukit//.	.//m32csi	m/lib/include/rtems	/score/cpu.h:40, from///	///rtems/c/src///cpukit/score/src/percpuasm.c:19:		
	//cpukit///m32csim	ı/lib/include	e/rtems/score/based	defs.h:244:17: error: size of a	array 'rtems_static_assert_PER_CPU_OFFSET_EXECUTING' is negative		
	typedef int rtems_st	atic_assert	t_ ## msg [(cond) ?	? 1 : -1]			
	/////rtems/c/src/.	.//cpukit/	'score/src/percpuasn	n.c:98:1: note: in expansion	of macro 'RTEMS_STATIC_ASSERT'		
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		n/lib/include			array 'rtems_static_assert_PER_CPU_OFFSET_HEIR' is negative		
	//cpukit///m32csim typedef int rtems_st	n/lib/include			array 'rtems_static_assert_PER_CPU_OFFSET_HEIR' is negative		
	typedef int rtems_st	n/lib/include atic_assert	t_ ## msg [(cond) ?	? 1 : -1]	array 'rtems_static_assert_PER_CPU_OFFSET_HEIR' is negative n of macro 'RTEMS_STATIC_ASSERT'		
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#28 Sum Desc 0 #28

Summar	Improve the fatal error	handling cha	pter of the user manu	lal		
y Descripti on	At least replace the "Do	ocument me"	markers with someth	ing useful.		
¥2826	3 years ago f	fixed	arch/arm	Chris Johns		2 years ago
ummar y	arm_cp15_get_translat	tion_table_ba	se_control_register w	arning.		
1	arm_cp15_get_translat	tion_table_ba	se_control_register ir	c/src/lib/libcpu/arm/shared/inc	lude/arm-cp15.h returns a pointer however ttb_	cr is not a pointer"
escripti on		//xilin			: In function 'arm_cp15_get_translation t :401:10: warning: return makes pointer f;	
#2829	3 years ago f	fixed	unspecified	Joel Sherrill	Chris Johns	2 years ago
ummar	xz git URL in README i	s broken				
y escripti on	Use http://git.tukaani.o	org/?				
+2835	3 years ago	duplicate	tool/gcc	Sebastian Huber	Needs Funding	2 years ago
ummar	Ada support is broken	•				_ ,
y escripti on	The Ada support is the thread-local storage.	last user of a	ı task variable: rtems_	_ada_self. This doesn't work on	SMP configurations. The Ada support in GCC sho	uld be changed to use a function call or C1
¥2836	3 years ago f	fixed	score	Joel Sherrill	Joel Sherrill	2 years ago
ummar	Add posix_devctl()					
	Technical Standard.			nd required by the FACE POSIX included in the FACE Profiles.	profiles. The only use case that needs to be sup	ported is FIONBIO on sockets per the FACE
escripti on	Technical Standard. ioctl() is not a standard Making operations non- in the Safety Base prof This requires adding th	dized method -blocking can ïle which mat e <devctl.h></devctl.h>	per POSIX and is not also be done with fcn cches the RTEMS POSI file to newlib. That ha	included in the FACE Profiles. tl() but due to RTOS qualificatio X capabilities. as been done. I am testing my i	n concerns, fcntl() is not included in the more st mplementation but a tool update will be needed	ringent FACE profiles. Specifically, it is not
on	Technical Standard. ioctl() is not a standard Making operations non- in the Safety Base prof This requires adding th community. This is OK	dized method -blocking can ïle which mat e <devctl.h></devctl.h>	per POSIX and is not also be done with fcn cches the RTEMS POSI file to newlib. That ha	included in the FACE Profiles. tl() but due to RTOS qualificatio X capabilities.	n concerns, fcntl() is not included in the more st mplementation but a tool update will be needed	ringent FACE profiles. Specifically, it is not
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name	ratio 1%2%5%10%20%50%100
CPU_Thread_Idle_body	22.454%
atsam spi setup transfer	6.767% ============
Objects Get	5.8598 ==================
atsam spi interrupt	4.483%
Event Seize	3.867% ==============
rtems termios enqueue raw characters	3.804% ==============
Timecounter Binuptime	3.715% ===============
Scheduler priority Block	3.104% ============
rtems semaphore release	3.018% ===========
Scheduler priority Unblock	2.901% ==========
rtems termios read tty	2.7778 =========
ARMV7M NVIC Interrupt dispatch	2.750% ==========
rtems semaphore obtain	2.627% ========
Thread Do dispatch	2.351% =======
ARMV7M Interrupt service leave	2.086% =======
iproc	1.919% ======
CPU Context switch	

Event_ ARMV7M Items_I Timecou Thread Schedul ARMV7M Schedul Mutex_ Iproc CPU_Con The change 2841 3 years ag mmar Add NXP SC Y	ermios device d	driver for NXP SC16IS752	(RS232/RS4	85 over SPI or I2C). Sebastian Huber	Sebastian Huber	
Event_ ARMV7M rtems_i Timecou Thread Schedui ARMV7M Schedui Mutex_ iproc CPU_Con The change 2841 3 years ag mmar Add NXP SC y			(2000) (200)			
Event_ ARM/7M rtems_i rtems_i Timeco Thread Schedul ARM/7M Schedul Mutex_ iproc CPU_Con The change 2841 3 years ago						
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Event_ ARMV7M rtems_t Timecol Thread Schedul ARMV7M Schedul Mutex_r iproc	ange resulted	in 10% more total idle ti	me on the svs	tem		
Event_ ARM/7M rtems_1 Timeco Thread Schedu Mutex_1 iproc	U_Context_swi	itch	I			
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Event_ ARMV7M_ rtems_1 Timeco Thread_ Schedu ARMV7M_	tex_recursive	e_Acquire		.876% =====		
Event_s ARMV7M rtems_t Timecou Thread Schedu	heduler_prior	rity_Unblock	1.	.919% ======		
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Event_S ARMV7M rtems_t rtems_t Timecou	heduler prior		2.	.1128 =======	i i	
Event_S ARMV7M rtems_t rtems_t	read Do dispa		2.	.240% =======		
Event_S ARMV7M rtems_t	mecounter Bir			653% =======		
Event_S ARMV7M	ems_termios_e			.7228 =========		
Event_S		terrupt_dispatch enqueue raw character		. 7718 ===================================		
	-	tonunt dianotah		.9558 ===================================		
Mutex 1	tex_recursive	e_Release		.011% ===================================		
	sam_spi_inter			.690% ======		
	sam_spi_setup			.061% ======		

Summar	Use self-contained of		Classic API for drivers	and support libraries		
У	The Classic API has s	some weaknesse	s:			
	 Objects are control The objects reference The object op 	reated via functionside in a table, e	on calls which return a e.g. they are suspect t	n object identifier. The object ope o false sharing of cache lines.	mplex configuration with heavy use of the C pre-pr rations use this identifier and map it internally to a on these parameters must be evaluated and valida	an object representation.
	The overhead for Cla	ssic API mutexes	s used for example in ⁻	Termios and the SPI framework is	significant, see discussion:	
	https://lists.rtems.or	rg/pipermail/deve	el/2016-December/016	6543.html		
	There are some API	options available	:			
Descripti on	3. Use FreeBSD	IX synchronization				
	Option 1. and 2. lack	support for bina	ary semaphores which	are used for task/interrupt synch	ronization, e.g. Termios.	
	Option 2. needs run-	time evaluation	to figure out the actua	l object variant, e.g. non-recursiv	e, recursive, ceiling, error-checking, robust POSIX	mutex.
	Option 3. uses hash	tables, thus it is	not suitable for real-ti	me systems.		
	Option 1. and 2. lack	support for use	r-defined object names	s that may help for system diagn	ostic, tracing and debugging.	
	Option 4. could be us	sed to avoid all s	hortcomings of options	s 1-3. It would be trivial to imple	nent, test and document.	
	In order to enable us	ser-defined object	t names one option is	to add a const char *name mem	per to Thread_queue_Queue.	
#2844	3 years ago	fixed	fs	Sebastian Huber	Sebastian Huber	2 years ago
Summar v	JFFS2: Add IO contro	ois to get filesyst	em instance informatio	on and force a garbage collection		
, Descripti on	Some applications ne	eed to control the	e garbage collection of	the JFFS2 filesystem. For examp	e during bootloader to application transitions with	execute in place flashes (XIP).
#2845	3 years ago	fixed	doc	Sebastian Huber		2 years ago
Summar	Add I2C framework	documentation				
y	The new I2C framew	ork lacks desure	entation			
Descripti on	The new 12C framew	UTK JOCUM	entation.			
#2849	3 years ago	fixed	doc	Sebastian Huber		2 years ago
Summar	ATA/IDE support in F	RTEMS is out-dat	ed			
y .	The ATA/IDE support	t in RTEMS is out	-dated. New platforms	s should consider to use the SATA	support provided by FreeBSD via libbsd.	
Descripti on	Update the documen					
#2850	3 years ago	fixed	doc	Sebastian Huber		2 years ago
Summar	Driver manual cover	s non-existent Ar	nalog Driver			
y Descripti	Remove this chapter	from the docum	entation.			
on						
#2851	3 years ago	fixed	doc	Sebastian Huber		2 years ago
Summar y	Driver manual cover	S HOH-EXISCENC DI	Screte Driver			
Descripti	Remove this chapter	from the docum	entation.			
on #2853	3 years ago	fixed	doc	Sebastian Huber		2 years ago
Summar y	•	s non-existent No	on-Volatile Memory Dr			,
Descripti on	Remove this chapter		entation.			
#2858	3 years ago	fixed	score	Sebastian Huber	Sebastian Huber	2 years ago
Summar y	Add user defined thr	ead names				
	Add user defined thr	ead names to ea	se debugging enhance	e the system diagnostics and imp	rove compatibility to other systems, e.g. Linux and	I FreeBSD
Descripti					_THREAD_NAME_SIZE to the application configura	
on				the thread control block.		
#2859	3 years ago	fixed	posix	Gedare Bloom	Gedare Bloom	2 years ago
Summar y	Implement POSIX Sh	iared Memory Ot	bjects			
	POSIX Shared Memo	ry is a widely us	ed API for inter-proces	s communication. The functions	n the API include:	
	 shm_open 					
	ftruncatemmap					
Descripti	 munmap 					
on	shm_unlinkclose					
	fstatfchown					
	 fchmod 					
#2862	3 years ago	fixed	doc	Chris Johns	Chris Johns	2 years ago
Summar	docs.rtems.org Add	support to ReST	format releases.			
y	Add support to the n	eleases section o	f the web site to hand	le ReST packages. The catalogue	have a legacy field for texinfo docs.	
Descripti on				dded because this did not exist w		
#2863	3 years ago	duplicate	doc	Joel Sherrill	Joel Sherrill	2 years ago
Summar	Update POSIX 1003.	· ·	ide for ReST			
У	The DOCTY 1002 1 C	ompliance Cold	should be sute	ated from a correctlobact into the	PeST format. My yaque recellection is that we	d shell coripte to de this for touinf
	output.	ompliance Guide	snoulu pe auto-genera	ated from a spreadsheet into the	ReST format. My vague recollection is that we use	a shell scripts to do this for texinfo
Descripti on	I will have to deciphe	er what we used	to do and define a nev	v procedure.		
011		d newer. One iss	sue is having correct in	formation for what methods are	present on a branch. The FACE Conformance Test	Suite can be used for ~800 of the
	methods.					
						4
2864	3 years ago	fixed	doc	Chris Johns		2 years ago

	Add support to auton	natically update	the branches when a	git commit happens.		
on 2865	3 years ago	fixed	doc	Chris Johns	Chris Johns	2 years ago
ummar	Coverpage installed v					2 years ago
У	The setal set and set					
escripti on	The catalogue repeat	s entries.				
2867	3 years ago	fixed	tool	Stavros Passas	Chris Johns	2 years ago
ummar	Fix exclude rule in rte	ems-test-check				
У	rtems-test-check is n	esponsible of ch	ecking the testsuite o	configuration of a given BSP and	adapt it based on a given "command". Currently for the "exc	clude" command, we never
escripti on				hole list of the input tests.		
					st the tests that are not excluded, so the output is always all	
2868	3 years ago src/c/src/lib/libbsp/ar	fixed	arch/arm	David Binderman	Gedare Bloom <gedare@></gedare@>	2 years ago
ummar y	SIC/C/SIC/IID/IIDDSP/a	111/SITIUK2410/S	sinc/sinc.c. 5 · pointi			
					ut its new value is never used. [src/c/src/lib/libbsp/arm/smdk <2410/smc/smc.c:246]: (style) Variable 'cnt3' is modified but	
	\$ egrep "cnt1 cnt2 c					its new value is never used.
				2, cnt3; cnt1 = 0; cnt2 = 0; cnt	3 - 0.	
escripti on	cnt1+4		one, count, chti, chti	$2, c_{1}c_{2}, c_{1}c_{2} = 0, c_{1}c_{2} = 0, c_{1}c_{2}$	5 – 0,	
011		, cnt2++; cnt3+	±•			
	\$		• /			
	→ Maybe someone left:	some debug cor	de in ?			
2873	3 years ago	fixed	arch/arm	David Binderman	Gedare Bloom <gedare@></gedare@>	2 years ago
ımmar	src/c/src/lib/libbsp/a	rm/raspberrypi/	i2c/i2c.c:320: defecti	ive error checking ?		
У	src/c/src/lib/libbsp/a	rm/raspberrypi/	'i2c/i2c.c:320]: (style) Checking if unsigned variable	'rv' is less than zero.	
	Source code is			, , ,		
	rv = rpi i2c s	etup_transfer(b	ous);			
	if (rv < 0) {	、				
escripti	but					
on	uint32_t rv =	0;				
	and	,				
	static int rpi_i2c_setu	up transfer(rpi	i2c bus *bus)			
	Suggest put return v	alue into an int		anity check it, then assign it to i	rv.	
2874	Suggest put return v 3 years ago	alue into an int fixed		anity check it, then assign it to David Binderman	rv. Sebastian Huber <sebastian.huber@></sebastian.huber@>	2 years ago
2874 ummar		fixed	local variable, then sa arch/powerpc	David Binderman		2 years ago
	3 years ago src/c/src/lib/libbsp/p [src/c/src/lib/libbsp/p	fixed owerpc/beatnik, oowerpc/beatnik	local variable, then sa arch/powerpc /marvell/gt_timer.c: < <td>David Binderman 4 * pointless check ? 102]: (style) Checking if unsign</td> <td>Sebastian Huber <sebastian.huber@> ed variable 'timer' is less than zero.</sebastian.huber@></td> <td>2 years ago</td>	David Binderman 4 * pointless check ? 102]: (style) Checking if unsign	Sebastian Huber <sebastian.huber@> ed variable 'timer' is less than zero.</sebastian.huber@>	2 years ago
ummar	3 years ago src/c/src/lib/libbsp/p [src/c/src/lib/libbsp/p [src/c/src/lib/libbsp/p	fixed owerpc/beatnik, powerpc/beatnik	local variable, then sa arch/powerpc /marvell/gt_timer.c: < <td>David Binderman 4 * pointless check ? 102]: (style) Checking if unsign 109]: (style) Checking if unsign</td> <td>Sebastian Huber <sebastian.huber@> ed variable 'timer' is less than zero. ed variable 'timer' is less than zero.</sebastian.huber@></td> <td>2 years ago</td>	David Binderman 4 * pointless check ? 102]: (style) Checking if unsign 109]: (style) Checking if unsign	Sebastian Huber <sebastian.huber@> ed variable 'timer' is less than zero. ed variable 'timer' is less than zero.</sebastian.huber@>	2 years ago
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39/130

				:1306]: (style) Redundant	condition: extended. '!extended (extended && o	check_pid(pid))' is	
		'!extended c	:heck_pid(pid)'				
	Suggest simplify.						
	2. src/cpukit/libdebugger/rtems-debugger-server.c:18581: (warning) Possible null pointer dereference: rtems debugger						
	<pre>src/cpukit/libdebugger/rtems-debugger-server.c:1858]: (warning) Possible null pointer dereference: rtems_debugger</pre>						
	Source code is						
	free(rtem	ntf(printer, "er	rror: rtems-db: rem ger->remote->name,	mote begin: %s: %s\n", strerror(errno));			
	*/	the end of the	session.				
	rtems_debug	ger->flags = 0;					
cripti n	Suggest adding re-	urn -1 inside the	! if.				
	3.						
		bdebugger/rtems- '!extended c		:906]: (style) Redundant c	ondition: extended. '!extended (extended && cl	neck_pid(pid))' is	
	Duplicate.						
	4.						
		bdobuggor/rtoma-	-dobuggor_corver_c	.9561. (warning) Char lite	ral compared with pointer 'p'. Did you intend to	dereference it?	
		odebugget/icenis	debugger server.c.	. (warning) char rice	fur compared with pointer p. Did you intend to	defetetence it.	
	Source code is						
	while (p !=	NULL && p != '\	(0') {				
	Maybe better code						
	while (p !=	NULL && *p != '	\0') {				
380	3 years ago	wontfix	unspecified	David Binderman		2 years ago	
ımar V	src/cpukit/libfs/src/	jffs2/src/readinode	e.c:189: faulty logic				
	else if (tn->1	ofs < offset) = tn->rb.rb_right; in->ofs >= offset) = tn->rb.rb_left;					
		= the providence,					
cripti	else						
on	break Maybe better code	,					
	if (tn->fn->c	ofs < offset)					
	-	= tn->rb.rb right;					
		n->ofs > offset)					
	-	= tn->rb.rb left;					
	else						
	break	:					
883	3 years ago	fixed	arch/arm	David Binderman	Pavel Pisa	2 years ago	
mar				strange expression ?		2 years ago	
/	are le le milite d'il t		ala/tmsE20 0.102	le (atula) Como ann	both sides of !!!		
cripti		a111/01155/0/CONSC	ne/unso/u-sci.c:248]]: (style) Same expression on	JULI SIDES UF .		
n	Source code is	ty roady - TMCCT	O OCT ELD TY END		лтv.		
				TY TMS570_SCI_FLR_TX_EMF		_	
885	3 years ago Fix rtems rate mor	fixed	job_count() prototyp	Sebastian Huber	Sebastian Huber	2 years ago	
ımar Y		postponed					
cripti				rn an RTEMS status code. It sl ems_rate_monotonic_get_stat	nould be renamed to rtems_rate_monotonic_get_postpon- ictics())	ed_job_count() or	
n 889	3 years ago	fixed	score	Stavros Passas	Stavros Passas <stavros.passas@></stavros.passas@>	2 years ago	
mar			N has incomplete defir			2,5015 090	
y							
cripti	The extension for the Werror, or similar fl		erines 8 entries, while	tne structure for RTEMS exter	nsions gets 9 arguments. This causes warnings to appear		
n					5 5	on applications compiled with	
890	3 years ago	missing is for the t	erminate callback, so	a 0 entry would be enough to		on applications compiled with	
mar		fixed	score	a 0 entry would be enough to Stavros Passas		on applications compiled with 2 years ago	
/		fixed	score		fix this error.		
	_RBTree_Initialize_	fixed node generates wa	score arnings	Stavros Passas	fix this error.	2 years ago	
y cripti on	_RBTree_Initialize_ Currently, when _R	fixed node generates wa BTree_Initialize_no	score arnings ode is used, it generat	Stavros Passas	fix this error. Sebastian Huber <sebastian.huber@></sebastian.huber@>	2 years ago	

Summar	Remove CONFIGURE_SM							
У								
escripti on	SMP support must be en CONFIGURE SMP MAXIN			LICATION configuration opti	on. Remove this option and enable the SMP support if			
#2894		red	config	Sebastian Huber	Sebastian Huber	2 years ago		
Summar	, ,			IGURE_MAXIMUM_PROCESS		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
y escripti	Rename CONFIGURE SM	1P MAXIMUM F	PROCESSORS to CONF	IGURE MAXIMUM PROCESS	ORS since the SMP part is superfluous.			
on								
2895	, ,	ed	config	Sebastian Huber	Sebastian Huber	2 years ago		
ummar y	Prefix the confdefs.h inte	ernal defines wi	ith an underscore					
escripti	Prefix the confdefs.h inte	ernal defines wi	ith an underscore to si	mplify the review of this extr	remely complex header file.			
on	3 400 000	mtfix	tool/rob	Joel Sherrill				
+2896 ummar	3 years ago wo RSB requirements are m	ontfix issing pax	tool/rsb	Joel Shernii	Chris Johns	2 years ago		
У								
escripti				oned in the RSB manual at a				
on				ssume that advice applies to	Centos 6, Fedora, and RHEL.			
2897	Can users of other distrib 3 years ago fix	outions check a ced	score	Kevin Kirspel	Needs Funding	2 years ago		
ummar	Update termios.h to mat			: · · · · · · · · · · · · · · · · · · ·	······	_ ,		
У	The EREERSD struct tore	nios conaratos	out the input and outp	ut haud rates into individual	speed_t variables. It also supports more flag options. The big benefit	is that the stoms-		
escripti on	libbsd tty code can be po	orted cleanly wi	thout the need to fixu	p the input and output baud	rates. This should be a transparent change unless someone was man			
2905		e c_cflags (not ced	using the cfgetispeed, arch/sparc	cfgetospeed, cfsetispeed, cf Tanu Hari Dixit	setospeed functions). Daniel Hellstrom	2 years ago		
ummar	Merge LEON		2. 0., 00010					
У								
	Merge LEON							
	Students: Past, Present	, and Potential	Students					
	Status: Some work may	be done.						
	Introduction: Merge Le	on RTEMS and	mainstream RTEMS m	erge the Leon DriverManage	r, samples, and LEON-RTEMS into mainstream RTEMS.			
					include: project deliverables (code, docs, testing), required/suggester	d methodology,		
	standards of quality, pos				the developer tackling this project will have to have: Required level	of programming		
corinti	language(s), specific are	as of RTEMS or	tools, level of familiar			or programming		
	language(s), specific areas of RTEMS or tools, level of familiarity with RTEMS, cross-development, GNU/Linux, etx., development/documentation/testing tools, mathematical/algorithmic background, other desirable skills.							
on	Resources: Current RTEMS developers, papers, etc that may help you in this project.							
	Resources: Current RTE Acknowledgements	EMS developers		help you in this project.				
				help you in this project.				
	Acknowledgements	lid work		help you in this project.				
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https://devel.rtems.org/wiki/Release//5/5.1

	Coverity URL:				
	https://scan5.coverity.com	m/reports.htm#v29808/p10069/	fileInstanceId=108958257&de	efectInstanceId=30877219&mergedDefectId=1399	703
	linkable between the order			an array pointer. I think the code is likely doing w d in the object IDs. Is there a way to improve this o	
		address with &snapshot->activ active = &snapshot->active_pos		n pointer.	
escripti	142 active = &snapshot->		/		
on		/* sizeof (objects_info_table) / s	izeof (obiects info table[0]) *	/, taking true branch.	
	144 for (i = 0; i < RTEMS_		e); ++i) { 145 const Objects_	Information *information; 146 147 information = _	Objects_Get_information(148
	4. Condition information	ion != NULL, taking true branch.			
	152 if (information != NUL	LL) { CID 1399703 (#1 of 1): Ou	ut-of-bounds access (ARRAY_V	'S_SINGLETON)	
	5. ptr_arith: Using act	tive as an array. This might corr	upt or misinterpret adjacent m	nemory locations.	
		Active_count(information);		I	
+ 2924	3 years ago fixe Warnings in SPARC BSPs	ed arch/sparc	Joel Sherrill	Daniel Hellstrom	2 years ago
ummar y	Waitings III SPARC DSPS				
	As of today, the following	warnings exist for SPARC BSPs.			
escripti on	function-declaration] log/s 'printk' [-Winested-externs function 'printk' [-Wimplici nested extern declaration leon2.log:////./ function-declaration] log/s declaration of 'printk' [-Wr declaration of function 'pri warning: nested extern de leon2.log://// declaration] log/sparc-leor Wnested-externs] log/span type 'unsigned int', but arg leon3.log://// declaration] log/sparc-leor Wnested-externs] log/span 'printk' [-Wimplicit-function declaration of 'printk' [-Wr declaration of function 'pri leon3.log:///// log/sparc-ngmp.log:/.// Wimplicit-function-declaration	sparc-leon2.log:///////// sparc-leon2.log://// it-function-declaration] log/spar of 'printk' [-Wnested-externs] log sparc-leon2.log:////// sparc-leon2.log:////// nested-externs] log/sparc-leon2 intk' [-Wimplicit-function-declara calaration of 'printk' [-Wnested-e //rtems/c/src/lib/libbs/sparc/l n2.log:////.rtems/ rc-leon2.log:////.rtems/ rc-leon2.log:////.rtems/ rcrclipp.log:////.rtems/ rcrengp.log://///rtems/ rcrengp.log:////.rtems/ rcrengp.log:////.rtems/ intk' [-Wimplicit-function-declaration] log/sparc-leon3 intk' [-Wimplicit-function] log/sparc-leon3 intk' [-Wimplicit-function] log/sparc-leon3 intk' [-Wimplicit-function] log/sparc-leon3 intk' [-Wimplicit-function] log/sparc-leon3 intk' [-Wimplicit-function] log/sparc-leon3 intk' [-Wimplicit-function] log/spa	/rtems/c/src/lib/libbsp/sparc ///rtems/c/src/lib/libbsp/ eon2///sparc/shared/drvmg /rtems/c/src/lib/libbsp/sparc log://sparc/shared/drvmg eon2///sparc/shared/arbwg c/src/lib/libbsp/sparc/leon2/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ tems/c/src/lib/libbsp/sparc/leon3/ sp/sparc/leon3//.sparc/shared/drvmg sp/sparc/leon3//.sparc/shared/trimes/ 	<pre>/ahbstat.c:156:3: warning: implicit declaration of fr /sparc/shared/amba/ahbstat.c:156:3: warning: r on3//.sparc/shared/amba/ahbstat.c:156:3: warni src/lib/libbsp/sparc/leon3///sparc/shared/amba c/src/lib/libbsp/sparc/leon3///sparc/shared/drvm gr/ambapp_bus.c:647:4: warning: nested extern do red/drvmgr/ambapp_bus.c:647:4: warning: implic bbsp/sparc/leon3///sparc/shared/drvmgr/ambapp</pre>	varning: nested extern declaration of us.c:647:4: warning: implicit declaration ed/drvmgr/ambapp_bus.c:647:4: warnin aration of function 'printk' [-Wimplicit- .c:168:3: warning: implicit //sparc/shared/irq/genirq.c:244:3: of function 'printk' [-Wimplicit-function- ng: nested extern declaration of 'printk' ning: format '%x' expects argument of unction 'printk' [-Wimplicit-function- nested extern declaration of 'printk' [- ning: implicit declaration of 'printk' [- ning: implicit declaration of function /ahbstat.c:156:3: warning: nested extern gr/ambapp_bus.c:647:4: warning: implicit eclaration of 'printk' [-Winested-externs] it declaration of function 'printk' [-
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1 Release Notes	43/130
The plugin needs to be fixed for new trac template changes. I usually don't have to modify this often but we made a huge version jump.	
Right now all we see is	
assign to <default></default>	
There should be a list of developers	

Descripti	The plugin needs t	o be fixed for fiew	w trac template change	es. I usually don't have to modify	this often but we made a huge version jump.	
Descripti	Right now all we s	ee is				
on	assign to $<$	default>				
	There should be a	list of developers				
#2935	3 years ago	wontfix	score	Martin Aberg	joel.sherrill@	2 years ago
Summar	Termios task drive	n mode not comp	patible with SMP			
y Descripti on	RTEMS_NO_PREEM rtems_task_create Termios starts the	1PT is not support () returns RTEMS RX and TX tasks	ted on SMP. 5_UNSATISFIED in this	SMP scenario and Termios ends RTEMS_NO_PREEMPT is remove	tems_task_create() with RTEMS_NO_PREEMPT in up in rtems_fatal_error_occurred(). ed from the initial task modes of these tasks. How	
#2941		invalid	tool/rsb	DHANPAL SINGH		2 years ago
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#2959	3 years ago		lib/dl			2 years ago
Summar	arm/libdl: C++ exce	eption index table	s may not be ordere	ed correctly		
y escripti on	functions in the add	ress map.		g set and this is not honored by	libdl which means the section order in the ELF file	needs to be the correct order of the
2963	Add support to libdl 3 years ago	fixed	unspecified	Chris Johns	Chris Johns	2 years ago
ummar	Add a testsuite top					
У						
escripti on	Add the file testsu For example filei Note, user-input	o] is user-input).	common to all BSPs. This lets	us globally set a test state.	
2965	3 years ago	fixed	unspecified	Gedare Bloom	Gedare Bloom <gedare@></gedare@>	2 years ago
ummar	bootstrap sort incon	sistent with sb-b	ootstrap for acinclud	e		
y	The output of boots	tran does not use	a consistent sort or	der with sh-bootstrap. The diffe	erence appears to be in the default behavior of the	sort command versus Python's sorted P
escripti on	forcing the locale to					Sore command versus rythort's sorted. E
2967	3 years ago	fixed	tool/gcc	Sebastian Huber	Sebastian Huber	2 years ago
ımmar	ARM: Change ABI to	o not use short er	nums			
У						
	Whether enums are	short or not is le	ft open in the ARM E	ABI. On Linux and FreeBSD no	short enums are used. Otherwise short enums are	e enabled by default.
	Short enums may c	ause hard to find	issues with 3rd part	y software, since the are quite	unusual in general, e.g.	
0.001-11	https://git.rtems.or	g/rtems-libbsd/co	ommit/freebsd/incluc	de/rpc?id=9880635f2e642380b	69b85e00271649b3a2fc2de	
scripti on					ed/removed. The benefit of short enums is probab	bly not worth the trouble, since the packe
	compiler attribute c					
	The reason for not o	choosing no short	enums during the A	RM EABI introduction was an is	sue with Newlib. This is addressed with the followi	ng patch:
	https://sourceware.	org/ml/newlib/20	17/msg00238.html			
2968	3 years ago	fixed	unspecified	Joel Sherrill		2 years ago
ımmar	newlib inttypes.h is	missing some me	ethods			
У	inttynos h dofinos s	ama mathada wh	ich are not present k	out required for DOCIV complian	nce. They are also included in the FACE General Pu	rnaga Drafila
	char restrict, int); i	ntmax_t wcstoim			ax(const char *restrict, char restrict, int); uintm uintmax_t wcstoumax(const wchar_t *restri	
				() ((
	(https://sourceware				ntml) with follow up discussion here	
escripti on	(https://sourceware	e.org/ml/newlib/2 ns to be that the	017/msg00240.html	1).	tml) with follow up discussion here ress the concerns raised in that email thread.	
	(https://sourceware The consensus seen This ticket is comple • source for th • methods are • RSB is updat • tests are add	e.org/ml/newlib/2 ns to be that the ete when: ese methods is m documented in n ted appropriately led to RTEMS	017/msg00240.html methods as currentl nerged into newlib ewlib). y implemented in FreeBSD addr		
on	(https://sourceware The consensus seen This ticket is comple • source for th • methods are • RSB is updat • tests are adc • RTEMS POSI	e.org/ml/newlib/2 ns to be that the ete when: ese methods is m documented in n red appropriately led to RTEMS X compliance spr	017/msg00240.html methods as currenth nerged into newlib ewlib eadsheet (https://go). y implemented in FreeBSD addr bo.gl/AXrnxO) is updated	ress the concerns raised in that email thread.	2
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gmake[6]: * [fstest_support.o] Error 1 gmake[6]: Leaving directory `/data/home/joel/rtems-work/rtems-testing/rtems/build-i386-pc586-sse-rtems/i386rtems4.12/c/pc586-sse/testsuites/fstests/fsjffs2gc01

#2981	3 years ago	fixed	unspecified	Joel Sherrill	Chris Johns	2 years ago
Summar y	testdata exclude	s on included tcfg	files does not work			
	It looks like the i	ncludes do not alw	ays work for .tcfg file	es. So far these BSPs do not ap	pear to honor the excludes in an included file:	
Descripti						
on	5.	5 5. 1	urricane.log log/mips-	rbtx4925.log log/mips-rbtx493	8.log log/moxie-moxiesim.log	
	log/m32c-m32cs mips and moxie	5 5. 1	urricane.log log/mips-	rbtx4925.log log/mips-rbtx493	8.log log/moxie-moxiesim.log	
	5.	5 5. 1	urricane.log log/mips-	rbtx4925.log log/mips-rbtx493	8.log log/moxie-moxiesim.log Sebastian Huber	2 years ago

The example code is:

Descripti on

5 5.	1 Release Notes 48
	\$ cat t.c /*
	/opt/work/rtems/4.12/bin/arm-rtems4.12-gcc -qrtems -B/opt/work/si/rtems/4.12/arm-rtems4.12/lib -B/opt/work/si/rtems/4.12/arm- rtems4.12/xilinx_zynq_zc706/lib/specs bsp_specs -march=armv7-a -mthumb -mfpu=neon -mfloat-abi=hard -mtune=cortex-a9 -ffunction-sections -fdata-
	sections -DHAVE_RTEMS_SCORE_CPUOPTS_H=1 t.c -c -o t.o
	*/
	<pre>#include <rtems.h></rtems.h></pre>
	int main(int argc, char **argv) {
	(void)argc; (void)argv;
	return 0; }
	<pre>\$ /opt/work/rtems/4.12/bin/arm-rtems4.12-gcc -qrtems -B/opt/work/si/rtems/4.12/arm-rtems4.12/lib -B/opt/work/si/rtems/4.12/arm- rtems4.12/xilinx_zynq_zc706/lib/specs bsp_specs -march=armv7-a -mthumb -mfpu=neon -mfloat-abi=hard -mtune=cortex-a9 -ffunction-sections -fdata- sections -DHAVE_RTEMS_SCORE_CPUOPTS_H=1 t.c -c -o t.o In file included from /opt/work/rtems/4.12/arm-rtems4.12/include/signal.h:6:0, from /opt/work/rtems/4.12/arm-rtems4.12/include/signal.h:6:0, from /opt/work/rtems/4.12/arm-rtems4.12/include/sys/time.h:268, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/score/timestamp.h:43, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/score/thread.h:36,</pre>
	<pre>from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/score/heap.h:22, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/rtems/types.h:26, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems.h:31, from t.c.7:</pre>
	/opt/work/rtems/4.12/arm-rtems4.12/include/sys/signal.h:53:3: error: unknown type name 'pthread_attr_t' pthread_attr_t *sigev notify attributes; /* Notification Attributes */
	In file included from /opt/work/rtems/4.12/arm-rtems4.12/include/string.h:10:0,
	<pre>from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/score/basedefs.h:49, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/score/types.h:23, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/score/cpu.h:32, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/system.h:23, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems.h:29, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems.h:29,</pre>
	/opt/work/rtems/4.12/arm-rtems4.12/include/sys/signal.h:202:5: error: unknown type name 'pthread_t' int _EXFUN(pthread_kill, (pthread_t thread, int sig));
	^ In file included from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/config.h:25:0, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/config.h:57, from /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx zynq zc706/lib/include/rtems.h:33,
	from t.c:7: /opt/work/si/rtems/4.12/arm-rtems4.12/xilinx_zynq_zc706/lib/include/rtems/rtems/tasks.h:425:3: error: unknown type name 'cpu_set_t' cpu_set_t *cpuset
	<pre>^~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~</pre>
	^
	\$ cat t.cpp
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nmar	Create- <freens h="" inttypes:="">: to consolidate-extensions-to-sinttypes: h&gt;: to consolidate-extensions - fdata- sections - OBAVP Statistics - Formation - - For</freens>
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	building: lm32-rtems4.11-gcc-4.9.3-newlib-2.2.0.20150423-x86 64-w64-mingw32-1
	Build Set: Time 0:29:19:809228
	Build Set: Time 3:47:43.385503
	Traceback (most recent call last):
	File"/source-builder/sb-set-builder", line 29, in <module></module>
	setbuilder.run()
	File "/source-builder/sb/setbuilder.py", line 502, in run
	b.build(deps)
	File "/source-builder/sb/setbuilder.py", line 340, in build
	bs.build(deps, nesting count)
	File "/source-builder/sb/setbuilder.py", line 354, in build
	self.build package (configs[s], b)
	File "/source-builder/sb/setbuilder.py", line 194, in build package
	<pre>build.config.expand('%{ tmproot}'))</pre>
	File "/source-builder/sb/setbuilder.py", line 155, in root copy
	self.copy(src, dst)
	File "/source-builder/sb/setbuilder.py", line 95, in copy
	path.copy_tree(src, dst)
	File "/source-builder/sb/path.py", line 232, in copy_tree
	copy_tree(srcname, dstname)
	File "/source-builder/sb/path.py", line 232, in copy_tree
	copy_tree(srcname, dstname)
	File "/source-builder/sb/path.py", line 232, in copy_tree
	copy_tree(srcname, dstname)
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	File "/source-builder/sb/path.py", line 232, in copy_tree
	copy_tree(srcname, dstname)
	File "/source-builder/sb/path.py", line 232, in copy_tree
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	File "/source-builder/sb/path.py", line 232, in copy_tree
	copy_tree(srcname, dstname) File "/source-builder/sb/path.py", line 232, in copy tree
	copy tree (schame, dsthame)
	<pre>File "/source-builder/sb/path.py", line 232, in copy tree</pre>
	copy tree (scname, dstname)
	File "/source-builder/sb/path.py", line 232, in copy tree
	copy tree(srcname, dstname)
	File "/source-builder/sb/path.py", line 232, in copy tree
	copy tree(srcname, dstname)
	File "/source-builder/sb/path.py", line 232, in copy tree
	copy tree(srcname, dstname)
	File "/source-builder/sb/path.py", line 191, in copy tree
	names = os.listdir(hsrc)
	TypeError: encoded string too long (269, maximum length 259)

joel.sherrill@...

Chris Johns

#2993 3 years ago fixed score Summar SMP assert in _Thread_Executing in libdebugger

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2 years ago

The target code in libdebugger has support to recover from exceptions related to invalid memory accesses. GDB may request the server access memory on the target that results in an exception. The exception occurs on the server's remote connection thread and the server needs to recover and return and error to GDB.

Running the debugger01 test with an SMP build of RTEMS and libbsd for xilinx_zedboard and issuing bt in GDB results in: ** LIBBSD DEBUGGER 1 TEST *** [144/1950] shell:cannot set terminal attributes(/dev/console) RTEMS Shell on /devn/ecxounss0o:l e<.R TUEsMeS 'Nheexlups' dteov ilcies>t ccogmemmaOn:d s<.C adence CGEM Gigabit Ethernet Interface> on nexus0 mibus0: <MII bus> on cgem0
[/] # el000phy0: <Marvell 88E1512 Gigabit PHY> PHY 0 on miibus0 e1000phy0: none, 10baseT, 10baseT-FDX, 10 cgem0: Ethernet address: fa:69:35:9e:04:2f 100baseTX, 100baseTX-FDX, 1000baseT-FDX, 1000baseT-FDX-master, auto zy7_slcr0: <Zynq-7000 slcr block> on nexus0 [zone: udpcb] kern.ipc.maxsockets limit reached notice: cgem0: link state changed to DOWN add host 10.10.5.1: gateway cgem0 add net default: gateway 10.10.5.1 rtems-db: remote running rtems-db: tcp remote: listing on port: 1122 notice: cgem0: link state changed to UE rtems-db: tcp remote: connect host: 10.10.5.2
rtems-db: arm debug: (v3.0) ARMv7 [v7, all CP14 registers] breakpoints:5 watchpoints:3
assertion "cpu_self->thread_dispatch_disable_level != 0 || _ISR_Get_level() != 0" failed: file
"../../cpukit/../../.xilinx_zynq_zedboard/lib/include/rtems/score/percpu.h", line 630, function: _Per_CPU_Get If I enable TARGET_DEBUG in libdebugger and apply the attached patch I can create the assert with DIE_ON_ASSERT set to 1. The output is: rtems-db: tcp remote: connect host: 10.10.5.2 ttems-db: tcp remote: connect host: 10.10.5.2
rtems-db: arm debug: (v3.0) ARWv7 [v7. all CP14 registers] breakpoints:5 watchpoints:3
[} frame = 005664EC sig=1 vector=4 ifsr=00000000 pra=0024173A
[} R0 = 00000158 R1 = 00000004 R2 = 00000000 R3 = 0041Ab64
[} R4 = 0000016 R5 = 000000004 R1 = 00000000 R7 = 005606A4
[} R8 = 0000016 R9 = 00566540 LR = 00000006 R11 = 0041Ab64
[] CPSR = 08010173 ----Q--A-FT GE:0 IT:01 M:13 SVC
[] target exception: 0 0 0
corection 'Conv solution to convert dispatch dispatc Descripti assertion "cpu_self->thread_dispatch_disable_level != 0 || _ISR_Get_level() != 0" failed: file "../../cpukit/../../.xilinx_zyng_zedboard/lib/include/rtems/score/percpu.h", line 630, function: _Per_CPU_Get and set to  $\{\{0\}\}$ : rtems-db: tcp remote: connect host: 10.10.5.2
rtems-db: arm debug: (v3.0) ARMv7 [v7, all CP14 registers] breakpoints:5 watchpoints:3
[} frame = 005664EC sig=1 vector=4 ifsr=00000000 pra=0024173A
[} R0 = 00000158 R1 = 00000004 R2 = 00000001 R3 = 0041AB64
[} R4 = 00000158 R5 = 00000004 R6 = 00000000 R7 = 005606A4
[} R8 = 0000016 R9 = 00000001 R10 = 00000006 R11 = 0041AB64
[} R12 = 00560658 SP = 00566540 LR = 000000FD PC = 00241736
[} CPSR = 08010173 ---Q-A-FT GE:0 IT:01 M:13 SVC
[} target exception: 0 0 0
[] tid:001000A thread:0014F5R0 frame:005664FC tid:0A01000A: thread:0041F5B0 frame:005664EC server access fault frame = 005664EC sig=1 vector=4 ifsr=00000000 pra=0024173A Frame = 005664EC sig=1 vector=4 ifsr=00000000 pra=00241/3A R0 = 00000158 R1 = 00000004 R2 = 00000001 R3 = 0041AB64 R4 = 00000158 R5 = 00000004 R6 = 00000000 R7 = 005606A4 R8 = 00000016 R9 = 00000001 R10 = 0000000F6 R11 = 0041AB64 R12 = 0056058 SP = 00556540 LR = 00000FFD PC = 00241736 CPSR = 08010173 ----Q--A-FT GE:0 IT:01 M:13 SVC target exception: 0 0 0 tid:0A01000A: thread:0041F5B0 frame:005664EC server access fault The following lines first two values are cpu self->thread dispatch disable level and ISR Get level() which are both 0 so I cannot see a reason the assert is happening: [} target exception: 0 0 0 #2994 3 years ago unspecified Joel Sherrill Chris Johns fixed 2 years ago tar01 XZ error Summar У This shows up on erc32 and psim. /dest3/home/test_script: mode: 0755 want: 0755 Descripti on Untaring chunks from txz - XZ file is corrupt (data) ../../../../../../../../../testsuites/libtests/tar01/init.c: 272 status == UNTAR_SUCCESSFUL_ 3 years ago fixed tool/rsb Hassan Karim joel.sherrill@... #2995 3 years ago joel.sherrill@... 2 years ago Missing bsets Summar When I went to rebuild based on 4.12, I cloned from github. I am no longer getting all of the b-sets that I was expecting. Specifically, 4.12/rtems-sparc Chris Johns said to submit it as a bug. It must have happened within the last couple of weeks. As my scripts that automated these tasks were working as of around 3/1/2017 Descripti git clone git://git.rtems.org/rtems-source-builder.git \$SETBLDRSCRIPTDIR/sb-set-builder --list-bsets on RTEMS Source Builder - Set Builder, 4.12 (2074bd1168ee) Examining: config Examining: ../rtems/src/rtems-source-builder/source-builder/config Examining: ../rtems/src/rtemssource-builder/bare/config devel/autotools-base.bset devel/autotools-internal.bset devel/autotools.bset devel/libtool.bset devel/libtool.bset devel/libtool.bset devel/or1ksim.bset devel/qemu.bset gnu-tools-4.6.bset gnu-tools-4.8.2.bset lang/gcc491.bset #2997 3 years ago fixed score Chris Johns Chris Johns 2 years ago Monitor config command does not handle unlimited objects. Summar У Running the console's config command with unlimited objects gives: [/] # config INITIAL (startup) Configuration Info Descripti start: 0x800f0173; size: 0x374c8
usec/tick: 10000; tick/timeslice: 50; tick/sec: 100
tasks: -2147483614; timers: -2147483616; sems: -2147483609; que's: -2147483616; ext's: 1
partitions: -2147483616; regions: -2147483616; ports: -2147483616; periods: -2147483616 WORKSPACE on TIME MAXIMUMS #2998 3 years ago fixed doc Chris Johns chrisj@... 7 weeks ago RTEMS User Manual Quick Start does not cover releases. Summar The guick start documents using git and does not cover a release. This is confusing because the releases tools and the git master may not work. Descripti #2999 3 years ago wontfix tool/rsb Joel Sherrill Chris Johns 2 years ago

### https://devel.rtems.org/wiki/Release//5/5.1

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	It looks like there a	ire two issues in wi	ndows.py			
escripti on		rograms like bison must be on mingw.		d. to exist on cygwin.		
+2000					ere were a lot more required programs in this file the Joel Sherrill	
<b>3000</b> ummar	3 years ago Setting interrupt lev	fixed vel in the mode arg	score on SMP returns R	Chris Johns TEMS_UNSATISFIED	Joel Sherrin	16 months ago
У	If for any reason a	upper pote the interv	unt lovel in the me	do on an CMD build the error	RTEMS_UNSATISFIED is returned. The documentati	on indicator this is a lack of stack and this
escripti	confusing.					
on	assert and check	s be added to the C	Classic API to catch	these errors and report suitab		preemptible check be converted to an
3001	There is no meanin 3 years ago	gful error code ava fixed	ilable without abus	ing an existing one so I propos	e adding RTEMS_INVALID_MODE . Sebastian Huber	2 years ago
ummar		and the second se	A CONTRACTOR OF	MAXIMUM_PROCESSORS		2 years ago
У	The default setting processor count to				configuration_is_smp_enabled() returns [false	). Only the smptests set the maximum
escripti on	If SMP is not runnir	ng in an SMP build v	when running the t	tests are the tests really report	ing a true indication of the of the system?	
				stests running with SMP enab		
<b>3003</b> ummar y	3 years ago FAT does not suppo	fixed ort clusters bigger t	fs/fat han 32K	munster	Sebastian Huber	2 years ago
escripti on	When used with 64 uint16_t bpc, whe			orever in cpukit/libfs/src/dosfs,	/fat.c, line 580. This happens because struct fat_vo	ol_s declares bytes per cluster variable as
<del>3006</del>	Here is a link for Lin 3 years ago	nux FAT driver whic fixed	ch doesn't make ar arch/sparc	ny assumption about cluster siz	e: http://lxr.free-electrons.com/source/fs/fat/inod Daniel Hellstrom	e.c?v=2.6.24#L1262 2 years ago
ummar v	SPARC LEON3 BSP	SMP build is broker	n.			
1	The rtems-bsp-bu	ilder failure outp	ut is:			
		ug sparc/leon3 bu				
				<pre>l/rtems.git/configureta n3prefix=/opt/rtems/4.1</pre>		
	enabl	e-debugenable	e-smpenable-te			
escripti				ese interrupt lock primiti	ves	
on	The BSP builder cor	mmand line is:				
	RTEMS Tools P	roject - RTEMS K	(ernel BSP Builde	er, 4.12.not_released		
	tool	s=/build/rtems/t		builderrtems-\		
	10		nris/rtems/kerne	l/rtems.gitbuild=smp-de	pnd/	
<del>3007</del>			arch/arm	l/rtems.gitbuild=smp-de munster	joel.sherrill@	12 days ago
ummar	10	^{rg=x} fixed				12 days ago
	lo 3 years ago	fixed	arch/arm			12 days ago
ummar y	lo 3 years ago ARM caching issues There are two prob • In cases whe to both L1 a • The L2C-310	fixed fixed lems with the cachi ere the buffer is no nd L2 caches.	arch/arm ing on ARM: t aligned to line bo nt operations use e	munster undary at the beginning or the		ions done on the adjacent data. This appli
ummar y escripti	lo 3 years ago ARM caching issues There are two prob • In cases whe to both L1 a • The L2C-310	fixed fixed lems with the cachi ere the buffer is no nd L2 caches. 0 cache manageme nd do not require l	arch/arm ing on ARM: t aligned to line bo nt operations use e	munster undary at the beginning or the	joel.sherrill@ end, the invalidate operation would lose modificat	ions done on the adjacent data. This appli
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y escripti on	lo 3 years ago ARM caching issues There are two prob • In cases who to both L1 a • The L2C-310 are atomic a I have attached the	fixed fixed fixed find L2 caches. cache manageme and do not require l proposed patch. fixed	arch/arm ing on ARM: t aligned to line bo nt operations use e ocking.	munster undary at the beginning or the excessive locking. According to	joel.sherrill@ end, the invalidate operation would lose modificat manual, the used operations (Clean Line by PA, Cl	ions done on the adjacent data. This appl ean and Invalidate Line by PA, Cache Syn
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https://dc.qnu.org/vbewcy/gc/tnuk/lbddd%28%2B-v3/vfc/%28%2B98/bs_jnlt.c?Vew=markup#85 is a placement new. The "stdout" etc. is thread-local in New10 definestdoutREUT+stdout; Using this new. The "stdout" etc. is thread-local in New10 definestdoutREUT+stdout; Using this new. The "stdout" etc. is thread-local in New10 to a plobal object like stdi::cou is quice broken. Which FLE object should be used instead? Potential fix: finder: cbbcs3%Y0044 100644	<pre>bttps://gc.gou.org/idewcv/gc/truh/lbstdc%28%28-v3/src/c%28%28%36_bit_c2view=markup#85 vs a lacement new. The 'stdout' etc. is thread-local in NewIB      feftine</pre>	
<pre>via a placement new. The "stdout" etc. is thread-local in Newlib fdefine stdout (PEENT&gt;_stdout) Large this for a global object like std::cout is guide broken. Which FLE object should be used instead? Potential fix:</pre>	<pre>via a placement new. The "stdout" etc. is thread-local in Newlb  fdefine stdout (REENT&gt;_stdout)  Using this for a global object like std::cout is quite broken. Which FLE object should be used instead? Potential fix:  fiff =-git a/liketder++v3/arc/e+199/los_linit.co</pre>	
<pre>test is its its is is its is its</pre>	#define       stdout (_REENT-s_stdout)         Using this for a global object like std::cout is quite broken. Which FILE object should be used instead? Potential fix:            [fff-split s/liberds+-r=3/src/c++99/los_init.co +	
<pre>using this for a global object like std::cout is quite broken. Which FLE object should be used instead? Potential fix: diff =-qit a/libatder++v3/src/c++99/log_init.co b/libatder++v3/src/c++99/log_init.co index c5bcc33,.r170c44 100644 incidex c5bcc33,.r170c44 100644 incidex cst/stdio_filebut.b&gt; finclude cst/stdio_filebut.b</pre>	<pre>Bescript Sung this for a global object like std::cout is quite broken. Which FILE object should be used instead? Potential fix: diffgit a/libstdc++-v3/arc/c++98/ios_init.co index c5bce37470c4 100644 index c5bce37470c4 10064 index c5bce37470c4 10064 index c5bce37470c4 10064 index c5bce37470c4 10064 index c5bce37470c4 10064 index c5bce37470c4 index c5bce3.</pre>	
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<pre>#include <xt stdi_gync_filebuf.h=""> #include <xt stdi_gync_filebuf.h=""> #</xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></xt></pre>	<pre>#include <ext stdio_ginc_filebuf.h=""> #include <ext stdio_ginc_<="" td=""><td></td></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></ext></pre>	
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<pre>static void _attribute ((_constructor_(0))) + static void _attribute (_constructor_(0))) + static void _attribute (_constructor_(0))) + static void _attribute (_constructor_(0))) + static void _attribute (_static, _sfp_recursive_mutex); </pre>	<pre>namespace _gnu_internal _GLIBCXX_VISIBILITY(hidden) {     using namespace _gnu_cxx;  diffgit a/newlib/libc/stdio/findfp.c b/newlib/libc/stdio/findfp.c index 83d3d5.736051 100644 a/newlib/libc/stdio/findfp.c     efe -259, e 259, 12 69 _DEFU(_sinit, (s),    </pre>	
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Hello, during reset password I had an internal error. This card was automatically generated.

#### How to Reproduce

While doing a POST operation on  $\ensuremath{/reset_password}\xspace$  , Trac issued an internal error.

(please provide additional details here)

#### Request parameters:

{u'__FORM_TOKEN': u'56888d70c5e5799302935f97', u'email': u'nikolay.komashinskiy@yandex.ru', u'register_phone': u'', u'rtems_user_phone': u'', u'username': u'nikolaykomashinskiy')

User agent: Mozilla/5.0 (X11; Linux x86_64) AppleWebKit/537.36 (KHTML, like Gecko) Chrome/57.0.2987.98 Safari/537.36

#### System Information

System information not available

#### Enabled Plugins

#### Plugin information not available

Interface Customization

Interface customization information not available

#### Python Traceback

Т	aceback (most recent call last):
	File "/data/src/trac/trac/web/main.py", line 620, in _dispatch_request dispatcher.dispatch(reg)
	disparenter.unsparenterg/
	resp = chosen handler.process request (req)
	File "/data/trac/plugins/TracAccountManager-0.5.dev0-pv2.7.egg/acct mgr/web ui.pv", line 168, in process request
	self. do reset password(reg)
	File "/data/trac/plugins/TracAccountManager-0.5.dev0-py2.7.egg/acct_mgr/web_ui.py", line 256, in _do_reset_password self. reset password(req, username, email)
	File "/data/trac/plugins/TracAccountManager-0.5.dev0-py2.7.egg/acct_mgr/web_ui.py", line 301, in _reset_password
	set user attribute (self.env, username, 'force_change_passwd', 1)
	File "/data/trac/plugins/TracAccountManager-0.5.dev0-py2.7.egg/acct_mgr/model.py", line 509, in set_user_attribute (value, username, attribute))
	(value, username, attribute)) File "/data/src/trac/db/util.py", line 128, in execute
	cursor.execute(query, params if params is not None else [])
	File "/data/src/trac/tha/thil.pv", line 72, in execute
	return self.cursor.execute(sql escape percent(sql), arqs)
	File "/usr/local/lib/python2.7/site-packages/MySOLdb/cursors.py", line 205, in execute
	self.errorhandler(self, exc, value)
	File "/usr/local/lib/python2.7/site-packages/MySOLdb/connections.py", line 36, in defaulterrorhandler
	raise errorclass, errorvalue
P	ogrammingError: (1064, "You have an error in your SQL syntax; check the manual that corresponds to your MySQL server version for the right
s	ntax to use near 'sid='nikolaykomashinskiy' AND authenticated=1 AND name='force change passwd'' at line 1")
s	ntax to use near 'sid='nikolaykomashinskiy' AND authenticated=1 AND name='force_change_passwd'' at line 1")

<del>#3014</del>	3 years ago	fixed	bsps	pnongvanpnam	joei.snerriii@	2 years ago
ummar y	interrupt vector in	dexing is assuming	BSP_INTERRUPT_VEC	$CTOR_MIN = 0$ for this code.		
escripti on	Someone impleme attachment.	ent bsp_interrupt_h	andler_index() forgot	to update this delta in rtems\c	\src\lib\libbsp\shared\src\irq-generic.c:bsp_interr	rupt_allocate_handler_index(). See
<del>3015</del>	3 years ago	fixed	arch/powerpc	phongvanpham	joel.sherrill@	2 years ago
ımmar y	Add support for IB	M PPC 750 chip				
escripti on	Currently MPC750	chip is supported.	However, PPC750 (fro	om IBM) is very close to MPC75	0 except minor differences. Enclosed is the delta t	co support PPC750.
<del>3016</del>	3 years ago	fixed	bsps	phongvanpham	joel.sherrill@	2 years ago
ummar y	missing a couple r	egister names + a	#ifndef <u>ASM</u> around s	erial.h inclusion		
У				erial.h inclusion ouple register and #ifndef arou	nd serial.h	
y scripti on					ind serial.h	2 years ago
y escripti on <b>3017</b>	In rtems\c\src\libo	hip\serial\ns16550	p.h, need to add a c	ouple register and #ifndef arou		2 years ago
escripti on <b>•3017</b> ummar	In rtems\c\src\libe 3 years ago improvement in po In pci.h, there are detect_raven_brid defined. The rest o all stuffed in pci.c I currently do not	worksforme ci.h references to BSP- ge.c. For folks that of the stuff in pci.c need this functiona	p.h, need to add a c score pci_configuration dat are just interested in should be separate. O lity for my BSP (nor d	ouple register and #ifndef arou phongvanpham a structure which is in pci.c. H pci_read_config_dword() + its r in another word, data structu lo I able to test it), so I cannot		tect_host_bridge () in tent for where BSP_pci_configuration ation needs to be in separate files rath
y escripti on <b>3017</b> ummar y escripti on	In rtems\c\src\libe <b>3 years ago</b> improvement in po In pci.h, there are defined. The resto all stuffed in pci.c I currently do not change. Or else, ju	thip\serial\ns16550 worksforme ci.h references to BSP ge.c. For folks that of the stuff in pci.c need this functiona ust shelf it under th	p.h, need to add a c score pci_configuration dat are just interested in should be separate. O lity for my BSP (nor d e table and/or close t	ouple register and #ifndef arou phongvanpham a structure which is in pci.c. H pci_read_config_dword() + its r in another word, data structu lo I able to test it), so I cannot his ticket.	joel.sherrill@ wever, in this file, there are also references to de brothers, all they need is to include pci.h and cor res and #defines involving with BSP_pci_configur modify code and submit. It is best someone who	tect_host_bridge () in tent for where BSP_pci_configuration ation needs to be in separate files rath can test the code to make the code
y escripti on <b>3017</b> ummar y	In rtems\c\src\lide 3 years ago improvement in po In pci.h, there are defined. The rest of all stuffed in pci.c I currently do not change. Or else, ju 3 years ago	worksforme ci.h references to BSP- ge.c. For folks that of the stuff in pci.c need this functiona	_p.h, need to add a c score pci_configuration dat are just interested in should be separate. O lity for my BSP (nor d te table and/or close ti tool/rsb	ouple register and #ifndef arou phongvanpham a structure which is in pci.c. H pci_read_config_dword() + its r in another word, data structu lo I able to test it), so I cannot	joel.sherrill@ owever, in this file, there are also references to de brothers, all they need is to include pci.h and con res and #defines involving with BSP_pci_configur	tect_host_bridge () in tent for where BSP_pci_configuration ation needs to be in separate files rath

			ne. but in centos 7. in	does not. Initial investigation (	I did a while back around New Year time) looks like later version of	texinfo has an issue wi
	autoconf. Enclosed	is the email Chr			tched to CentOS 6.8 for my work.	
	"Looks to me like t	he RSB is trying	to download autoconf 2	2.69-1 and from https://bugs.d	lebian.org/cgi-bin/bugreport.cgi?bug=711297	
Descripti					; however, from what I am reading, RSB will download its own vers	ion independent of wha
on	user has."			,		
	Chris John replies:					
			as exposed the issue T	suggest you get the patch from	n the link in the bug report, create a patch for rtems-tools.git to add	d the autoconf patch th
					the patches to devel@ for review."	
<del>#3023</del>	3 years ago	fixed	tool/newlib	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Parameter of CPU_	COPY() are in w	rong order			
У	According to the Fu	CORCD man nag	a wa hava			
	According to the Fi					
	https://www.freeb	sd.org/cgi/man.o	cgi?query=cpuset&sekti	ion=9&apropos=0&manpath=F	reeBSD+11.0-RELEASE+and+Ports	
	CPU_COPY (cpu:	set_t *from, c	puset_t *to);			
Descripti	However, in Newlik	we have:				
on	static inl	ine void CPU C	OPY ( cou set t *dest	t, const cpu set t *src )		
	{	_	orr( opa_bec_c acoc	, conse opu_see_e sico,		
	*dest = *s:	rc;				
<del>#3025</del>	, 5	fixed	unspecified	Chris Johns	joel.sherrill@	2 years ago
Summar y	m32c/m32csim do	es not build linpa	ack-pc.c			
У						
	1 tests m37	c/m32csim builc	1:			
				ms ait/configuretarget)	2c-rtems4.12enable-rtemsbsp=m32csimprefix=/opt/rtems/4.	12\
		gure: /opt/work, le-tests	, crins, i terns, kernel/rtei	ms.gr/ configuretarget\ =m3	20 remover.12enable-remousp=m32csimprefix=/opt/rtems/4.	±
Descripti			s/linpack/linpack-pc.c:2	253:33: error:		
on			f 'a' isn't constant			
				252.21		
	error: tests		s/linpack/linpack-pc.c:2	253:21: error:		
		storage size o	f 'aa' isn't constant			
<del>#3027</del>	3 years ago	fixed	tool/rsb	Worth Burruss	Chris Johns	2 years ago
Summar	RTEMS source buil	der fails when bu	uilding gcc documentation	on with newer versions of gcc		
У	Originally die	od with MOVOD	n windows, Duilletter of	a acc compiler f-ll- f-	reions of acc (io 4.9.3) when building this and a 20 at 19 at 19	The acc maintain
Descripti on				e gcc compiler fails for older ve nfiguration. A possible solution	rsions of gcc (ie 4.8.3) when building bfin and m32c architectures. is attached.	me gcc maintainers
<del>#3032</del>	3 years ago	fixed	tool/newlib	Sebastian Huber	Sebastian Huber	2 years ago
Summar			not in line with FreeBSD		ocoustion hubbl	2 years ago
у						
	https://www.freeb	sd.org/cgi/man.o	cgi?query=cpuset&sekti	ion=9&apropos=0&manpath=F	reeBSD+11.0-RELEASE+and+Ports	
		macro removes C	CPUs in src from dst. (It	t is the cpuset(9) equivalent of	the scalar: dst &= ~ src.)	
	The CPU_NAND() r					
	The CPU_NAND() r However, in Newlit	we had:				
	However, in Newlik	ine void CPU_N		e, cpu_set_t *destset,		
Descripti	However, in Newlik	ine void CPU_N	AND_S(size_t setsize , const cpu_set_t *s			
Descripti on	However, in Newlik staticinl: const cpu_s { cpu_set_wost	ine void CPU_N. set_t *srcset1 rd_t *wdest =	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0];</pre>	srcset2)		
	However, in Newlit staticinl: const cpu_s; { cpu_set_wo: const cpu_s;	ine void CPU_N. set_t *srcset1 rd_t *wdest = set_word_t *ws	, const cpu_set_t *s	srcset2);; ; its[0];		
	However, in Newlit staticinl: const cpu_: { const cpu_: const cpu_: size_t n =	ine void CPU_N. set_t *srcset1 rd_t *wdest = set_word_t *ws	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rc1 = &amp;srcset1-&gt;bi rc2 = &amp;srcset2-&gt;bi</pre>	srcset2);; ; its[0];		
	However, in Newlit staticinl: const cpu_i { cpu_set_wo: const cpu_i const cpu_i	ine void CPU_N. set_t *srcset1 rd_t *wdest = set_word_t *ws set_word_t *ws	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rc1 = &amp;srcset1-&gt;bi rc2 = &amp;srcset2-&gt;bi</pre>	srcset2);; ; its[0];		
	However, in Newlit staticinl: const cpu_: { const cpu_: const cpu_: size_t n = size_t i; for (i = 0,	<pre>ine void CPU_N set_t *srcsetl rd_t *wdest = set_word_t *ws set_word_t *ws set_vord_t *vs set_vord_t *; i &lt; n; ++i)</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rcl = &amp;srcsetl-&gt;bi rc2 = &amp;srcset2-&gt;bi eof(*wdest);</pre>	srcset2);; ; its[0];		
	However, in Newlit staticinl: const cpu_: { const cpu_: const cpu_: size_t n = size_t i; for (i = 0,	ine void CPU_N set_t *srcset1 rd_t *wdest = set_word_t *ws set_word_t *ws setsize / siz	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rcl = &amp;srcsetl-&gt;bi rc2 = &amp;srcset2-&gt;bi eof(*wdest);</pre>	srcset2);; ; its[0];		
on	<pre>However, in Newlit staticinl:     const cpu_:     {         cpu_set_wo:         const cpu_:         size_t n =         size_t i;         for (i = 0,             wdest[i]      } </pre>	<pre>ine void CPU_N set_t *srcset1 rd_t *wdest = set_word_t *ws set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i]</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rcl = &amp;srcsetl-&gt;bi rc2 = &amp;srcset2-&gt;_bi eof(*wdest); &amp; wsrc2[i]);</pre>	<pre>srcset2) ; its[0]; its[0];</pre>	icel sherrill@	2 vezro zos
on #3036	<pre>However, in Newlit staticinl: const cpu_: { cpu_set_wo: const cpu_: size_t n = size_t i; for (i = 0, wdest[i] } 3 years ago</pre>	<pre>ine void CPU_N set_t *srcsetl rd_t *wdest = set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rcl = &amp;srcsetl-&gt;_bi rc2 = &amp;srcsetl-&gt;_bi eof(*wdest); &amp; wsrc2[i]); tool/newlib</pre>	srcset2);; ; its[0];	joel.sherrill@	2 years ago
on #3036	<pre>However, in Newlit staticinl: const cpu_: { cpu_set_wo: const cpu_: size_t n = size_t i; for (i = 0, wdest[i] } 3 years ago</pre>	<pre>ine void CPU_N set_t *srcsetl rd_t *wdest = set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rcl = &amp;srcsetl-&gt;bi rc2 = &amp;srcset2-&gt;_bi eof(*wdest); &amp; wsrc2[i]);</pre>	<pre>srcset2) ; its[0]; its[0];</pre>	joel.sherrill@	2 years ago
on #3036 Summar	<pre>However, in Newlit staticinl: const cpu_: { cpu_set_wo: const cpu_: size_t n = size_t i; for (i = 0, wdest[i] } 3 years ago</pre>	<pre>ine void CPU_N set_t *srcset1 rd_t *wdest = set_word_t *ws set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed nentation is not i</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;_bits[0]; rc1 = &amp;srcset1-&gt;_bi rc2 = &amp;srcset2-&gt;_bi eof(*wdest); &amp; wsrc2[i]);      tool/newlib n line with FreeBSD</pre>	<pre>srcset2) ; its[0]; its[0];</pre>	joel.sherrill@	2 years ago
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on #3036 Summar	<pre>However, in Newlit staticinl: const cpu_: { cpu_set_wo: const cpu_: const cpu_: const cpu_: const cpu_: isize_t n = size_t i; for (i = 0 wdest[i] } 3 years ago CPU_CMP() implem According to the Fin https://www.freeb</pre>	<pre>ine void CPU_N set_t *srcsetl rd_t *wdest = set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed mentation is not i reeBSD man pag sd.org/cgi/man.org/</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;_bits[0]; rcl = &amp;srcsetl-&gt;_bi rc2 = &amp;srcsetl-&gt;_bi eof(*wdest); &amp; wsrc2[i]);       tool/newlib n line with FreeBSD e we have: cggi?query=cpuset&amp;sekti</pre>	<pre>srcset2) ; ; its[0]; its[0]; Sebastian Huber ion=9&amp;apropos=0&amp;manpath=F</pre>		2 years ago
on #3036 Summar y	However, in Newlit staticinl: const cpu_i ( cpu_set_wo: const cpu_i size_t n = size_t i; for (i = 0, wdest[i] 3 years ago CPU_CMP() implem According to the Fi https://www.freeb The CPU_CMP() implem	<pre>ine void CPU_N set_t *srcsetl rd_t *wdest = set_word_t *ws set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed nentation is not i reeBSD man pag sd.org/cgi/man.c acro returns true</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;_bits[0]; rc1 = &amp;srcset1-&gt;_bi rc2 = &amp;srcset2-&gt;_bi eof(*wdest); &amp; wsrc2[i]);      tool/newlib n line with FreeBSD e we have:</pre>	<pre>srcset2) ; ; its[0]; its[0]; Sebastian Huber ion=9&amp;apropos=0&amp;manpath=F</pre>		2 years ago
on #3036 Summar y Descripti	However, in Newlit staticin1: const cpu_; { cpu_set_wo: const cpu_; size_t n = size_t i; for (i = 0, wdest[i] } 3 years ago CPU_CMP() implem According to the Fin https://www.freeb The CPU_CMP() main However, in Newlit	<pre>ine void CPU_N set_t *srcset1 rd_t *wdest = set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed nentation is not i reeBSD man pag sd.org/cgi/man.c acro returns true o we had:</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;_bits[0]; rcl = &amp;srcsetl-&gt;_bi rc2 = &amp;srcset2-&gt;_bi eof(*wdest); &amp; wsrc2[i]);</pre>	srcset2) its[0]; its[0]; Sebastian Huber ion=9&apropos=0&manpath=F al to cpuset2.		2 years ago
on #3036 Summar y	However, in Newlit staticinl: const cpu_i ( cpu_set_wo: const cpu_i size_t n = size_t i; for (i = 0, wdest[i] 3 years ago CPU_CMP() implen According to the Fi https://www.freeb The CPU_CMP() m: However, in Newlit	<pre>ine void CPU_N set_t *srcsetl rd_t *wdest = set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed nentation is not i reeBSD man pag sd.org/cgi/man.c acro returns true p we had: if the sets se</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rcl = &amp;srcsetl-&gt;_bi rc2 = &amp;srcsetl-&gt;_bi eof(*wdest); &amp; wsrc2[i]);       tool/newlib n line with FreeBSD e we have: cgi?query=cpuset&amp;sekti e if cpuset1 is NOT equa t1 and set2 are equa </pre>	<pre>srcset2) ; its[0]; its[0]; Sebastian Huber ion=9&amp;apropos=0&amp;manpath=F al to cpuset2. al, otherwise return 0 */</pre>	reeBSD+11.0-RELEASE+and+Ports	2 years ago
on #3036 Summar y Descripti	However, in Newlit staticinl: const cpu_; { cpu_set_wo: const cpu_; size_t n = size_t i; for (i = 0, wdest[i] } 3 years ago CPU_CMP() implem According to the Fir https://www.freeb The CPU_CMP() main However, in Newlit /* return 1 : staticinl; {	<pre>ine void CPU_N set_t *srcset1 rd_t *wdest = set_word_t *ws set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed nentation is not i reeBSD man pag sd.org/cgi/man.c acro returns true to we had: if the sets set ine int CPU_CM</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rc1 = &amp;srcset1-&gt;_bi rc2 = &amp;srcset2-&gt;_bi eof(*wdest); &amp; wsrc2[i]);</pre>	srcset2) its[0]; its[0]; Sebastian Huber ion=9&apropos=0&manpath=F al to cpuset2.	reeBSD+11.0-RELEASE+and+Ports	2 years ago
on # <b>3036</b> Summar y Descripti	However, in Newlit staticinl: const cpu_; { cpu_set_wo: const cpu_; size_t n = size_t i; for (i = 0, wdest[i] } 3 years ago CPU_CMP() implem According to the Fir https://www.freeb The CPU_CMP() main However, in Newlit /* return 1 : staticinl; {	<pre>ine void CPU_N set_t *srcsetl rd_t *wdest = set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed nentation is not i reeBSD man pag sd.org/cgi/man.c acro returns true p we had: if the sets se</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rc1 = &amp;srcset1-&gt;_bi rc2 = &amp;srcset2-&gt;_bi eof(*wdest); &amp; wsrc2[i]);</pre>	<pre>srcset2) ; its[0]; its[0]; Sebastian Huber ion=9&amp;apropos=0&amp;manpath=F al to cpuset2. al, otherwise return 0 */</pre>	reeBSD+11.0-RELEASE+and+Ports	2 years ago
on #3036 Summar y Descripti on	However, in Newlit staticinl: const cpu_; { cpu_set_wo: const cpu_; size_t n = size_t i; for (i = 0, wdest[i] } 3 years ago CPU_CMP() implem According to the Fin https://www.freeb The CPU_CMP() m: However, in Newlit { /* return 1 = staticinl: { return CPU} }	<pre>ine void CPU_N set_t *srcset1 rd_t *wdest = set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed nentation is not i reeBSD man pag sd.org/cgi/man.c acro returns true p we had: if the sets se ine int CPU_CM EQUAL(set1, s </pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;_bits[0]; rc1 = &amp;srcset1-&gt;_bi rc2 = &amp;srcset2-&gt;_bi eof(*wdest); &amp; wsrc2[i]);      tool/newlib n line with FreeBSD e we have: cgi?query=cpuset&amp;sekti e if cpuset1 is NOT equa t1 and set2 are equa p( const cpu_set_t * et2); </pre>	<pre>srcset2) state = 1 srcset2 state = 1 stat</pre>	reeBSD+11.0-RELEASE+and+Ports	
on #3036 Summar y Descripti on #3040	However, in Newlit staticinl: const cpu_; { cpu_set_wo: const cpu_; size_t n = size_t i; for (i = 0, wdest[i] } 3 years ago CPU_CMP() implement According to the Fin https://www.freeb The CPU_CMP() m. However, in Newlit { return CPU } 3 years ago	<pre>ine void CPU_N set_t *srcset1 rd_t *wdest = set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed nentation is not i reeBSD man pag sd.org/cgi/man.c acro returns true o we had: if the sets se ine int CPU_CM EQUAL(set1, s invalid</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rcl = &amp;srcsetl-&gt;_bi ccl = &amp;srcsetl-&gt;_bi eof(*wdest); &amp; wsrc2[i]);</pre>	<pre>srcset2) ; its[0]; its[0]; Sebastian Huber ion=9&amp;apropos=0&amp;manpath=F al to cpuset2. al, otherwise return 0 */</pre>	reeBSD+11.0-RELEASE+and+Ports	2 years ago 19 months ago
on #3036 Summar y Descripti on #3040 Summar	However, in Newlit staticinl: const cpu_; { cpu_set_wo: const cpu_; size_t n = size_t i; for (i = 0, wdest[i] } 3 years ago CPU_CMP() implem According to the Fin https://www.freeb The CPU_CMP() m: However, in Newlit { /* return 1 = staticinl: { return CPU} }	<pre>ine void CPU_N set_t *srcset1 rd_t *wdest = set_word_t *ws setsize / siz ; i &lt; n; ++i) = ~(wsrcl[i] fixed nentation is not i reeBSD man pag sd.org/cgi/man.c acro returns true o we had: if the sets se ine int CPU_CM EQUAL(set1, s invalid</pre>	<pre>, const cpu_set_t *s &amp;destset-&gt;bits[0]; rcl = &amp;srcsetl-&gt;_bi ccl = &amp;srcsetl-&gt;_bi eof(*wdest); &amp; wsrc2[i]);</pre>	<pre>srcset2) state = 1 srcset2 state = 1 stat</pre>	reeBSD+11.0-RELEASE+and+Ports	
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# https://devel.rtems.org/wiki/Release//5/5.1

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ımmar y	Warnings in libdebu	gger				
	I started fixing the w more accurately.	warnings in libdeb	ugger with the lates	t tools but apparently some of th	e variables can't be changed to const char *	const. So filing as a ticket so Chris can fix the
escripti on	67 [ [	/./rtems/c/src/// /rtems/c/src/// /rtems/c/src/// /rtems/c/src/// /rtems/c/src/// /rtems/c/src/// /rtems/c/src///	//cpukit/libdebugg (cpukit/libdebugger/ (cpukit/libdebugger/ (cpukit/libdebugger/ (cpukit/libdebugger/ (cpukit/libdebugger/ (cpukit/libdebugger/ (cpukit/libdebugger/ (cpukit/libdebugger/	yer/rtems-debugger-server.c:61: rtems-debugger-server.c:53:14 rtems-debugger-server.c:1490: rtems-debugger-server.c:1490: rtems-debugger-server.c:1220: rtems-debugger-server.c:1260: rtems-debugger-server.c:1064: rtems-debugger-server.c:1025: rtems-debugger-server.c:1022: rtems-debugger-server.c:1021: rtems-debugger-server.c:1022: rtems-debugger-server.c:1022: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1021: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025: rtems-debugger-server.c:1025:	arning: comparison between pointer and zer 19: warning: duplicate 'const' declaration spe warning: duplicate 'const' declaration speci warning: duplicate 'const' declaration spe- 4: warning: duplicate 'const' declaration sp- 14: warning: duplicate 'const' declaration sp- 15: warning: duplicate 'const' declaration sp- 16: warning: duplicate 'const' declarati	becifier [-Wduplicate-decl-specifier] 67 fier [-Wduplicate-decl-specifier] 67 fier [-Wduplicate-decl-specifier] 67 ecifier [-Wduplicate-decl-specifier] 67 ecifier [-Wduplicate-decl-specifier] 67 ecifier [-Wduplicate-decl-specifier] 67 ecifier [-Wduplicate-decl-specifier] 67 ecifier [-Wduplicate-decl-specifier] 60 ecifier [-Wduplicate-decl-specifier] 60 ier [-Wduplicate-decl-specifier] 60
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	//gdb-7.1: DEF_VEC_FUNC_1 DEF_VEC_ALLOC static inline //gdb-7.1: #define VEC_01 <scratch space<br="">VEC_tp_t_alloc ^.//gdb-7.1: //gdb-7.1: DEF_VEC_FUNC_1 DEF_VEC_FUNC_1 //gdb-7.1: DEF_VEC_FUNC_1</scratch>	r_bound 2/gdb/record-btj 2/gdb/common/vec P(T) 2/gdb/common/vec FUNC_P' VEC(T) *VEC_OP 2/gdb/common/vec 2/gdb/common/vec 2/gdb/record-btj 2/gdb/common/vec P(T) 2/gdb/common/vec	<pre>c.h:429:27: note: c.h:744:23: note: (T,alloc) c.h:399:22: note: ##_##OP expanded from he cace.c:2445:1: wa c.h:429:27: note: c.h:751:20: note:</pre>	<pre>rning: unused function 'VEC expanded from macro 'DEF_V' expanded from macro '\ expanded from macro 'VEC_O re rning: unused function 'VEC</pre>	<pre>C_P' ` ` ` ` ' ' ' ' ' ' ' ' ' ' ' ' ' ' '</pre>	
	//gdb-7.1: DEF_VEC_FUNC_1 DEF_VEC_ALLOC static inline //gdb-7.1: #define VEC_01 <scratch space<br="">VEC_tp_t_alloc //gdb-7.1: DEF_VEC_FUNC_1 DEF_VEC_FUNC_1 DEF_VEC_FUNC_1 DEF_VEC_ALLOC static inline //gdb-7.1:</scratch>	r_bound 2/gdb/record-btj 2/gdb/common/vec FUNC P' VEC (T) *VEC_OP 2/gdb/common/vec P(T, OP) VEC_##T+ a>:166:1: note: 2/gdb/record-btj 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/c	<pre>c.h:429:27: note: (T,alloc) c.h:399:22: note: ##_##OP expanded from he: c.ace.c:2445:1: wa: c.h:429:27: note: c.h:751:20: note: free) c.h:399:22: note:</pre>	<pre>rning: unused function 'VEC expanded from macro 'DEF_V' expanded from macro '\ expanded from macro 'VEC_O re rning: unused function 'VEC expanded from macro 'DEF_V'</pre>	<pre>CC_P' ` ` ` ` tp_t_free' [-Wunused-function] CC_P' ` ` ` ` ` ` ` `</pre>	
	/./gdb-7.1 DEF_VEC_FUNC_1 DEF_VEC_ALLOC static inline /./gdb-7.12 #define VEC_01 <scratch space<br="">VEC_tp_t_alloc /./gdb-7.12 /./gdb-7.12 DEF_VEC_FUNC_1 //gdb-7.12 DEF_VEC_ALLOC static inline //gdb-7.12 #define VEC_01</scratch>	r_bound 2/gdb/record-bti 2/gdb/common/vec FUNC P' VEC (T) *VEC_OP 2/gdb/common/vec P(T, OP) VEC_##Ti >>:166:1: note: 2/gdb/record-bti 2/gdb/common/vec P(T) 2/gdb/common/vec P(T, OP) VEC_OP (T, 2/gdb/common/vec P(T, OP) VEC_WEC_WEC P(T, OP) VEC_WEC P(T, OP) VEC P(T,	<pre>c.h:429:27: note: (T,alloc) c.h:399:22: note: ##_##OP expanded from he: c.ace.c:2445:1: wa: c.h:429:27: note: c.h:751:20: note: free) c.h:399:22: note:</pre>	<pre>rning: unused function 'VEC expanded from macro 'DEF_Vi expanded from macro '\ expanded from macro 'VEC_O re rning: unused function 'VEC_O expanded from macro 'DEF_Vi expanded from macro '\ expanded from macro 'VEC_O</pre>	<pre>CC_P' ` ` ` ` tp_t_free' [-Wunused-function] CC_P' ` ` ` ` ` ` ` `</pre>	
		r_bound 2/gdb/record-btj 2/gdb/common/vec P(T) 2/gdb/common/vec PUNC_P' VEC(T) *VEC_OP 2/gdb/common/vec P(T,OP) VEC_##T+ 2/gdb/record-btj 2/gdb/common/vec P(T,OP) VEC_PT void VEC_OP(T, ^ 2/gdb/common/vec P(T,OP) VEC_##T+ a>:170:1: note: 2/gdb/record-btj 2/gdb/record-btj 2/gdb/record-btj 2/gdb/record-btj 2/gdb/record-btj 2/gdb/record-btj	<pre>c.h:429:27: note: (T,alloc) c.h:399:22: note: ##_##OP expanded from he race.c:2445:1: wa c.h:429:27: note: c.h:751:20: note: free) c.h:399:22: note: #_##OP expanded from he race.c:2445:1: wa</pre>	<pre>rning: unused function 'VEC expanded from macro 'DEF_Vi expanded from macro '\ expanded from macro 'VEC_O re rning: unused function 'VEC_ expanded from macro 'DEF_Vi expanded from macro 'VEC_O re</pre>	<pre>tp_t_free' [-Wunused-function] CC_P'  ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` `</pre>	
	<pre>.//gdb-7.1: DEF_VEC_FUNC_1 .//gdb-7.1: DEF_VEC_ALLOC static inline .//gdb-7.1: #define VEC_01 <scratch space<br="">VEC_tp_t_alloc .//gdb-7.1: DEF_VEC_FUNC_1 .//gdb-7.1: DEF_VEC_ALLOC static inline .//gdb-7.1: #define VEC_01 <scratch space<br="">VEC_tp_t_free .//gdb-7.1: DEF_VEC_FUNC_1 .//gdb-7.1: DEF_VEC_FUNC_1 .//gdb-7.1: DEF_VEC_FUNC_1 .//gdb-7.1: DEF_VEC_ALLOC</scratch></scratch></pre>	r_bound 2/gdb/record-bti 2/gdb/common/vec P(T) 2/gdb/common/vec PUNC_P' VEC(T) *VEC_OP 2/gdb/common/vec P(T,OP) VEC_##T4 a>:166:1: note: 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T,OP) VEC_##T4 a>:170:1: note: 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T) 2/gdb/common/vec P(T)	<pre>c.h:429:27: note: (T,alloc) c.h:399:22: note: ##_##OP expanded from he: race.c:2445:1: wa: c.h:429:27: note: c.h:751:20: note: free) c.h:399:22: note: #_##OP expanded from he: race.c:2445:1: wa: c.h:429:27: note: c.h:429:27: note:</pre>	<pre>rning: unused function 'VEC expanded from macro 'DEF_V' expanded from macro '\ expanded from macro 'VEC_O re rning: unused function 'VEC expanded from macro 'DEF_V' expanded from macro 'VEC_O re rning: unused function 'VEC_O</pre>	<pre>tp_t_free' [-Wunused-function] CC_P'  ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` `</pre>	

OP) VEC_##T##_##OP <scratch space>:187:1: note: expanded from here VEC_tp_t_merge ../../gdb-7.12/gdb/record-btrace.c:2445:1: warning: unused function 'VEC_tp_t_safe_grow' [-Wunused-function] ../../gdb-7.12/gdb/common/vec.h:429:27: note: expanded from macro 'DEF_VEC_P' DEF_VEC_FUNC_P(T) \vec{P}(T) ../../gdb-7.12/gdb/common/vec.h:817:20: note: expanded from macro '\ DEF_VEC_ALLOC_FUNC_P' static inline void VEC OP (T, safe grow) /../gdb-7.12/gdb/common/vec.h:399:22: note: expanded from macro 'VEC_OP' #define VEC_OP(T,OP) VEC_##T##_##OP <scratch space>:205:1: note: expanded from here VEC_tp_t_safe_grow ../../gdb-7.12/gdb/record-btrace.c:2445:1: warning: unused function 'VEC_tp_t_safe_insert' [-Wunused-function] ../../gdb-7.12/gdb/common/vec.h:429:27: note: expanded from macro 'DEF_VEC_P' DEF_VEC_FUNC_P(T) \ ../../gdb-7.12/gdb/common/vec.h:835:18: note: expanded from macro '\ DEF_VEC_ALLOC_FUNC_P' static inline T *VEC OP (T, safe insert) ../gdb-7.12/gdb/common/vec.h:399:22: note: expanded from macro 'VEC_OP' #define VEC_OP(T,OP) VEC_##T##_##OP <scratch space>:225:1: note: expanded from here VEC_tp_t_safe_insert 2 warnings generated. 2 warhings generated. /usr/bin/c++ -02 -pipe -fbracket-depth=1024 -I/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/rtems-source-builder/rtems/~/rtems/4.12/include -g -02 -I. -I../../gdb-7.12/gdb -I../../gdb-7.12/gdb/common -I../../gdb-7.12/gdb/config -DLOCALEDIR="\"/rtems-source-builder/rtems/~/rtems/4.12/share/locale\"" -DHAVE_CONFIG H -I../../gdb-7.12/gdb/../include/opcode -I../../gdb-7.12/gdb/../include -I../libdecnumber -I../../gdb-7.12/gdb/../libdecnumber -I../../gdb-7.12/gdb/gnulib/import -Tbuild-gnulib/import -DT /.12/gdb/../include -I../libdecnumber -I../../gdb-7.12/gdb/../libdecnumber -I../../gdb-7.12/gdb/gnulib/import -Tbuild-gnulib/import -DT //rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/rtems-source-builder/rtems/4.12/include -//gate//image/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtems/alle/rtem -DTUI=1 //icems doited birted//framework/Python.framework/Versions/2.7/include/python2.7 -I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 - Wall -Wpointer-arith -Wno-unused -Wunused-value -Wunused-function I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 - Wall -Mpointer-arith -Mno-unused -Wunused-value -Wunused-function -Wno-switch -Wno-char-subscripts -Wempty-body -Wunused-but-set-parameter -Wunused-but-set-variable -Wno-sign-compare -Wno-write-strings -Wno-narrowing -Wformat-nonliteral -c -o debug.o -MT debug.o -MMD -MP -MF .deps/debug.Tpo ./../gdb-7.12/gdb/debug.c /usr/bin/c++ -02 -pipe -fbracket-depth=1024 -I/ttems-source-builder/ttems/build/tmp/sb-peer/4.12/ttems-powerpc/rtems-source-builder/rtems/-/rtems/4.12/include -g -02 -I. -I.././gdb-7.12/gdb -I../../gdb-7.12/gdb/common -I.././gdb-7.12/gdb/../include/opcode -I../../gdb-DLOCALEDIR="\"/rtems-source-builder/ttems/-/ttems/4.12/share/locale\"" -DHAVE_CONFIG H -I../../gdb-7.12/gdb/../include/opcode -I../../gdb-7.12/gdb/../ipcodes/.. -I.././gdb-7.12/gdb/../readline/.. -I../../gdb-7.12/gdb/../zdb -I.././gdb-7.12/gdb/../bfd -I../../gdb-7.12/gdb/../ipcodes/.. -I.././gdb-7.12/gdb/../readline/.. -I../../gdb-7.12/gdb/../zdb -I.././gdb-7.12/gdb/../bfd -I../../gdb-7.12/gdb/../ipcodes/.. -I.././gdb-7.12/gdb/../readline/.. -I../../gdb-7.12/gdb/../zdb -I.././gdb-7.12/gdb/../bfd -I../../gdb-7.12/gdb/../ipcde-I../ibde-I.././gdb-7.12/gdb/../readline/.. -I../../gdb-7.12/gdb/gnulib/import -Ibuild-gnulib/import -DTUI=1 -I/system/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 -I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 -Wno-switch -Wno-char-subscripts -Wempty-body -Wunused-but-set-parameter -Wunused-but-set-variable -Wno-sign-compare -Wno-write-strings -Wno-narrowing -Wformat-nonliteral -c -o common-exceptions.o -MT common-exceptions.o -MMD -MP -MF .deps/common-exceptions.Tpo ../../gdb-7.12/db/common/common-exceptions.c 7.12/gdb/common/common-exceptions.c /usr/bin/c++ -02 -pipe -fbracket-depth=1024 -I/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/rtems-source /usr/bin/c++ -02 -pipe -fbracket-depth=1024 -I/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerp/rtems-source-builder/rtems/~/rtems/4.12/include -g -02 -I. -I../../gdb-7.12/gdb -I../../gdb-7.12/gdb/common -I../../gdb-7.12/gdb/../include/opcode -I../../gdb-DLCALEDIR="\"/rtems-source-builder/rtems/-/rtems/4.12/share/locale\"" -DHAVE_CONFIG H -I../../gdb-7.12/gdb/../include/opcode -I../../gdb-7.12/gdb/../opcodes/.. -I../../gdb-7.12/gdb/../readline/.. -I../../gdb-7.12/gdb/../zlb -I..//.gdb-7.12/gdb/../bfd -I../../gdb-7.12/gdb/../include -I../libdecnumber -I../../gdb-7.12/gdb/../libdecnumber -I../../gdb-7.12/gdb/goulib/import -Ibuild-gnulib/import -DTUI=1 -I/system/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 -I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 -Wno-switch -Wno-char-subscripts -Wempty-body -Wunused-but-set-parameter -Wunused-but-set-variable -Wno-sign-compare -Wno-write-strings -Wno-narrowing -WFormat-nonliteral -c -o btrace-common.o -MT btrace-common.o -MMD -MP -MF .deps/btrace-common.Tpo ../../gdb-7.12/gdb/common/btrace-common.c -DTUI=1 -/usr/bin/c++ -O2 -pipe -fbracket-depth=1024 -I/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/rtems-source -DTUT=1 -2 warnings generated. warning: unknown warning option '-Wunused-but-set-parameter'; did you mean '-Wunused-parameter'? [-Wunknown-warning-option] warning: unknown warning option '-Wunused-but-set-parameter'; did you mean '-Wunused-parameter'? [-Wunknown-warning-option] warning: unknown warning option '-Wunused-but-set-variable'; did you mean '-Wunused-const-variable'? [-Wunknown-warning-option] warning: unknown warning option '-Wunused-but-set-variable'; did you mean '-Wunused-const-variable'? [-Wunknown-warning-option] warning: unknown warning option '-Wunused-but-set-variable'; did you mean '-Wunused-const-variable'? [-Wunknown-warning-option] warning: unknown warning option '-Wunused-but-set-variable'; did you mean '-Wunused-const-variable'? [-Wunknown-warning-option] /usr/bin/ct+ -02 -pipe -fbracket-depth=1024 -1/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-source-builder/rtems/4.12/include -g -02 -1. -I.././gdb-7.12/gdb -I.././gdb-7.12/gdb/common -I.././gdb-7.12/gdb/config -DLOCALEDIR="\"/rtems-source-builder/rtems/./ttems/4.12/share/locale\"" -DHAVE_CONFIG H -I.././gdb-7.12/gdb/../include/opcode -I.././gdb-7.12/gdb/../include -I../libdecnumber -I../../gdb-7.12/gdb/../libdecnumber -I.././gdb-7.12/gdb/../include/opcode -I.././gdb-7.12/gdb/../include -I../libdecnumber -I../../gdb-7.12/gdb/../libdecnumber -I../../gdb-7.12/gdb/gdullb/import -DTUI=1 -I/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/rtems-source-builder/rtems/4.12/include -I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 - Wall -Wpointer-arith -Wno-unused -Wunused-value -Wunused-function -Wno-switch -Wno-char-subscripts -Wempty-body -Wunused-but-set-parameter -Wunused-but-set-variable -Wno-sign-compare -Wno-write-strings -Wno-narrowing -Wformat-nonliteral -c -o common-regcache.o -MT common-regcache.o -MMD -MP -MF .deps/common-regcache.Tpo ../../gdb-7.12/gdb/common/regcache.c 2 warnings generated. /.12/gdb/common/common-regcache.c
clang: warning: treating 'c' input as 'c++' when in C++ mode, this behavior is deprecated
warning: unknown warning option '-Wunused-but-set-parameter'; did you mean '-Wunused-parameter'? [-Wunknown-warning-option]
warning: unknown warning option '-Wunused-but-set-variable'; did you mean '-Wunused-const-variable'? [-Wunknown-warning-option]
2 warnings generated.
2 warnings generated. 7.12/gdb/common/common-regcache.c -DTIIT=1 -

Elease Noies 7.12/gdb/../opcodes/..-I.././gdb-7.12/gdb/../readline/.. -I../../gdb-7.12/gdb/../lib -I../bfd -I.././gdb-7.12/gdb/../bfd -I.././gdb-7.12/gdb/../include -I../libdecnumber -I../../gdb-7.12/gdb/../libdecnumber -I../../gdb-7.12/gdb/gnulib/import -Ibuild-gnulib/import -DTUI=1 I/ttems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/ttems-source-builder/rtems/4.12/include -I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 -Visystem/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 -Wno-switch -Wno-char-subscripts -Wempty-body -Wunused-but-set-parameter -Wunused-but-set-variable -Wno-sign-compare -Wno-write-strings -Wno-narrowing -WFormat-nonliteral -c -o compile-c-symbols.o -MT compile-c-symbols.o -MMD -MP -MF .deps/compile-c-symbols.Tpo ../../gdb-7.12/gdb/compile/compile-c-symbols.Tpo ../../gdb-Descripti 7.12/gdb/compile/compile-c-symbols.c //.12/gdb/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile/compile -DTUI=1 //i/judy/../inforded first/build/tmp/bb-peer/4.12/rtems-powerpc/rtems-source-builder/rtems/4.12/include -I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 -I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 -Wall -Wpointer-arith -Wno-unused -Wunused-value -Wunused-function -Wno-switch -Wno-char-subscripts -Wempty-body -Wunused-but-set-parameter -Wunused-but-set-variable -Wno-sign-compare -Wno-write-strings -Wno-narrowing -Wformat-nonliteral -c -o compile-c-types.o -MT compile-c-types.o -MMD -MP -MF .deps/compile-c-types.Tpo ../../gdb-7.12/gdb/compile/compile-c-types.c 7.12/gdb/compile/compile/compile/compile/c-types.c
2 warnings generated.
2 warnings treating 'c' input as 'c++' when in C++ mode, this behavior is deprecated
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Clangs warning; treating 'c' input as 'c++' when in C++ mode, this behavior is deprecated
Clangs warning; treating 'c' input as 'c++' when in C++ mode, this behavior is deprecated
Clangs warning; throws warning option '-Munused-but-set-parameter', I-Munkown-warning-option]
warning; unknown warning option '-Munused-but-set-variable'; did you mean '-Munused-const-variable'? I-Munknown-warning-option]
warning; unknown warning option '-Munused-but-set-variable'; did you mean '-Munused-const-variable'? I-Munknown-warning-option]
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warning: unknown warning option'-/-Munused-but-set-variable'? I-Munknown-warning-opti 2 warnings generated. 2 warnings generated. warning: unknown warning option '-Wunused-but-set-variable'; did you mean '-Wunused-const-variable'; [-wunknown-warning-option]
2 warnings generated.
/usr/bin/c++ -02 -pipe -fbracket-depth=1024 -I/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/rtems-sourcebuilder/rtems/~/rtems/4.12/include -g -02 -I. -I.././gdb-7.12/gdb/config DLOCALEDIR="\"rtems-source-builder/rtems//include -g -02 -I. -I../../gdb-7.12/gdb/config DLOCALEDIR="\"rtems-source-builder/rtems//include -g -02 -I. -I../../gdb-7.12/gdb/config DLOCALEDIR="\"rtems-source-builder/rtems//include -g -02 -I. -I../../gdb-7.12/gdb/config DLOCALEDIR="\"rtems-source-builder/rtems//include -I../../gdb-7.12/gdb/../includeopcode -I../../gdb7.12/gdb/../include -I../.i/gdb-7.12/gdb/../include/pdb/config I/trems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-source-builder/rtems/-/include I/tygdb/../include -I../include -I../include -I../ingdb-7.12/gdb/../include -I../.i/gdb-7.12/gdb/../include -I../ingdb-7.12/gdb/../include -I../ingdb-7.12/gdb/../include-II./ingdb-7.12/gdb/../include-II./ingdb-7.12/gdb/../include-II./ingdb-7.12/gdb/../include/ptho12.7 I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 - Wall -Wpointer-arith -Wno-unused -Wunused-Wunused-function Wno-switch -Wno-Korks -Wputhon.framework/Versions/2.7/include/ptho12.7 - Wall -Wpointer-arith -Wno-sign-compare -Wno-write-strings -Wno-Wno-switch -Wno-char-subscripts -Wempty-body -Wunused-but-set-parameter -Wunused-but-set-variable -Wno-sign-compare -Wno-write-strings -Wno-narrowing -Wformat-nonliteral -c -o compile-loc2c.o -MT compile-loc2c.o -MMD -MP -MF .deps/compile-loc2c.Tpo ../../gdb-7.12/gdb/compile/compileclang: warning: treating 'c' input as 'c++' when in C++ mode, this behavior is deprecated warning: unknown warning option '-Wunused-but-set-parameter'; did you mean '-Wunused-parameter'? [-Wunknown-warning-option] warning: unknown warning option '-Wunused-but-set-variable'; did you mean '-Wunused-const-variable'? [-Wunknown-warning-option] warning: unknown warning option '-Wunused-but-set-variable'; did you mean '-Wunused-const-variable'? [-Wunknown-warning-option]
2 warnings generated.
//uar/bin/crt+=02=pipe=fbracket-depth=1024 =I/rtems-source-builder/rtems/build/tmp/sb=peer/4.12/rtems-powerpc/rtems-sourcebuilder/rtems/-/rtems/4.12/include =g =02 =I. =I../../gdb=7.12/gdb =I../../gdb=7.12/gdb/common =I../../gdb=7.12/gdb/config =
DLOCALEDIR="\"/rtems-source-builder/rtems/-/rtems//ttems//ttems//ttems//ttems/ourcele\"" =DHAVE_CONFIG H =I../../gdb=7.12/gdb/../include/opcode =I../../gdb=7.12/gdb/../include/opcode =I../../gdb=7.12/gdb/../include/opcode =I../../gdb=7.12/gdb/../include/opcode =I../../gdb=7.12/gdb/../include =I../ibde=T../../gdb=7.12/gdb/../include/opcode =I../../gdb=7.12/gdb/../include/opcode =I../../gdb=7.12/gdb/../include/opcode =I../../gdb=7.12/gdb/../include/opcode =I../../gdb=7.12/gdb/../include =I../ibde=T../../gdb=7.12/gdb/../include/opcode =I../../gdb=7.12/gdb/../include =I../ibde=T../../gdb=7.12/gdb/../include=T../ibde=T../../gdb=7.12/gdb/../include/opcode =I../../gdb=7.12/gdb/../include/opcode =I../../g 7.12/gdb/compile/compile-c-support.c clang: warning: treating 'c' input as 'c++' when in C++ mode, this behavior is deprecated 2 warnings generated. warning: unknown warning option '-Wunused-but-set-parameter'; did you mean '-Wunused-parameter'? [-Wunknown-warning-option] warning: unknown warning option '-Wunused-but-set-variable'; did you mean '-Wunused-const-variable'? [-Wunknown-warning-option] /usr/bin/c++ -O2 -pipe -fbracket-depth=1024 -I/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/rtems-source-builder/rtems/~/rtems/4.12/include -g -O2 -I. -I.././gdb-7.12/gdb/-1.12/gdb/onon -I.././gdb-7.12/gdb/config -DLOCALDETR*"\"/rtems-source-builder/rtems//trems/12/gdb/../idberounder_0DHTG_H -I.././gdb-7.12/gdb/../include/opcode -I.././gdb-7.12/gdb/../include/opcode -I.././gdb-7.12/gdb/../include -I.././gdb-7.12/gdb/../bfd -I.././bfd -I.././gdb-7 2 warnings generated. -DTIIT=1 -2 warnings generated. ../../gdb-7.12/gdb/compile/compile-loc2c.c:671:23: note: initialize the variable 'uoffset' to silence this warning uint64_t uoffset, reg; warning: unknown warning option '-Wunused-but-set-parameter'; did you mean '-Wunused-parameter'? [-Wunknown-warning-option] warning: unknown warning option '-Wunused-but-set-variable'; did you mean '-Wunused-const-variable'? [-Wunknown-warning-option] 2 warnings generated. 2 warnings generated. 17 warnings generated. 2 warnings generated. warnings generated.

/usr/bin/c++ -02 -pipe -fbracket-depth=1024 -I/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/rtems-source-

Making init.c https://devel.rtems.org/wiki/Release//5/5.1

2 warnings generated. 99 warnings generated.

builde/rtems//rtems/4.12/include -g -O2 -I. -I../../gdb-7.12/gdb -I../../gdb-7.12/gdb/common -I../../gdb-7.12/gdb/config -DLOCALEDIR="\"/rtems-source-builder/rtems/~/rtems/4.12/share/localk\"" -DRAVE_CONFIG H -I../../gdb-7.12/gdb/../include/opcode -I../../gdb-7.12/gdb/../opcodes/.. -I../../gdb-7.12/gdb/../leadline/.. -I../../gdb-7.12/gdb/../libdecnumber -I../../gdb-7.12/gdb/../include -I../libdecnumber -I../libdecnum -DTUI=1 ///intersource-builder/rtems/build/thm/sb-peer/4.12/rtems-powerpc/rtems-source-builder/rtems/4.12/include -I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 -I/System/Library/Frameworks/Python.framework/Versions/2.7/include/python2.7 -Wall -Wpointer-arith -Wno-unused -Wunused-value -Wunused-function -Insystem/initialy/rameworks/sython.frameworks/versions2.//initide/python2./~wail =wpointer-arith =who-unised =wundsed=funct Who-switch =Who-ther-subscripts =Wempt-body =Wunused-but-set-parameter = Wunused=but-set-variable =Who-sign-compare =Who-write-strings =Who narrowing =Wformat-nonliteral =c -o init.o =MT init.o =MMD =MP =-MF.deps/init.Tpo init.c clang: warning: treating 'c' input as 'c++' when in C++ mode, this behavior is deprecated warning: unknown warning option '=Wunused-but-set-parameter'; did you mean '=Wunused-const-variable'? [=Wunknown-warning-option] warning: unknown warning option '=Wunused-but-set-variable'; did you mean '=Wunused-const-variable'? [=Wunknown-warning-option] 2 warnings generated. rm -f adb /usr/bin/c++ -O2 -pipe -fbracket-depth=1024 -I/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/rtems-source builder/rtems/~/rtems/4.12/include -g -02 -Wl,-no_pie -L/rtems-source-builder/rtems/build/tmp/sb-peer/4.12/rtems-powerpc/rtems-sourcebuilder/rtems/~/rtems/4.12/lib -o gdb gdb.o rs6000-tdep.o ppc-sysv-tdep.o solib-svr4.o ravenscar-thread.o ppc-ravenscar-thread.o ser-base.o ser-unix.o ser-pipe.o ser-tcp.o remote o dcache o tracepoint o ax-general o ax-gdb o remote-fileio o remote-notif o ctf o tracefile o tracefile-tfile o remote-sim.o cli-dump.o cli-decode o cli-script.o cli-cmds.o cli-setshow.o cli-logging.o cli-interp.o cli-utils.o mi-out.o mi-console.o mi-cmds.o mi-cmd-catch.o mi-cmd-env.o mi-cmd-var.o mi-cmd-break.o mi-cmd-stack.o mi-cmd-file.o mi-cmd-disas.o mi-symbol-cmds.o mi-cmd-target.o mi-cmd-info.o mi-interp.o mienv.o mi-cmd-var.o mi-cmd-break.o mi-cmd-stack.o mi-cmd-file.o mi-cmd-disas.o mi-symbol-cmds.o mi-cmd-target.o mi-cmd-info.o mi-interp.o mi-main.o mi-parse.o mi-getopt.o tui-command.o tui-data.o tui-disas.o tui-file.o tui-hocks.o tui-interp.o tui-io.o tui-layout.o tui-out.o tui-regs.o tui-source.o tui-stack.o tui-windata.o tui-windata.o tui-wingeneral.o tui winsource.o tui opy-arch.o py-auto-lado.o py-block.o py-bevent.o py-breakpoint.o py-cmd.o py-continueevent.o py-xmethods.o py-evtregistry.o py-evts.o py-exts.o py-auto-lado.o py-block.o py-bevent.o py-framefilter.o py-function.o py-gbb-readline.o py-inferior.o py-infevents.o py-infthread.o py-lazy-string.o py-linetable.o py-newobjfileevent.o py-objfile.o py-param.o py-putyprint.o py-progspace.o py-signalevent.o py-stopevent.o py-symbol.o py-symtab.o py-threadevent.o py-type o py-unwind.o py-utils.o py-value.o py-varobj.o guile.o elfread.o stap-probe.o dtrace-probe.o posix-hdep.o posix-strerror.o c-exp.o c-pname-parser.o ada-exp.o jv-exp.o d-exp.o go-exp.o m2-exp.o p-exp.o rust-exp.o version.o annotate.o addrmap.o auto-load.o auxv.o agent.o bfd-target.o blockframe.o breakpoint.o break-catch-sig.o break-catch-throw.o break-catch-syscall.o findvar.o regcache.o cleanups.o charset.o continuations.o corelow.o disasm.o dummy-frame.o dfp.o source.o value.o eval.o valoso valarith.o valprint.o printcmd.o block.o symtab.o symfile.o symfile-debug.o symmisc.o linespec.o dictionary.o namespace.o location.o infcall.o infcmd.o infrun.o expprint.o environ.o stack.o tid-parse.o thread.o thread-fsm.o exceptions.o extension.o filesystem.o filesystem.o filesvif.o arch-utils.o gdbtpes.o gdb bfd.o gdb obstack.o osabi.o macroscope.o mi-common.o event-loop.o event-top.o inf-loop.o completer.o gdbarch.o arch-utils.o gdbtypes.o gdb_bfd.o gdb_obstack.o osabi.o copying.o memattr.o mem-break.o target.o target-dcache.o parse.o language.o build-id.o buildsym.o findcmd.o std-regs.o signals-state-save-restore.o signals.o exec.o reverse.o bcache.o objfiles.o observer.o minsyms.o maint.o demangle.o dbxread.o coffread.o coff-pe-read.o dwarf2read.o mipsread.o stabsread.o corefile.o dwarf2expr.o dwarf2loc.o dwarf2-frame.o dwarf2-frame-tailcall.o ada-lang.o c-lang.o d-lang.o f-lang.o objc-lang.o ada-tasks.o ada-varobj.o c-varobj.o ui-out.o cli-out.o varobj.o vec.o go-lang.o go-valprint.o go-typeprint.o jv-lang.o jv-valprint.o jv-typeprint.o jv-varobj.o m2-lang.o opencl-lang.o p-typeprint.o p-valprint.o selftest.o sentinel-frame.o complaints.o typeprint.o adatypeprint.o c-typeprint.o f-typeprint.o m2-typeprint.o ad-valprint.o c-valprint.o cp-valprint.o d-valprint.o f-valprint.o m2-vyalprint.o ser-event.o serial.o mdebugread.o top.o utils.o ui-file.o user-regs.o frame.o frame-unwind.o doublest.o frame-base.o inline-frame.o gnu-v2-abi.o gnu-v3-abi.o cp-abi.o cp-support.o cp-namespace.o d-namespace.o reggroups.o rust-lang.o trad-frame.o tramp-frame.o solib.o solib-target.o prologuev3-abi.o cp-abi.o cp-support.o cp-namespace.o d-namespace.o reggroups.o rust-lang.o trad-frame.o tramp-frame.o solib.o solib-target.o prologue-value.o memory-map.o memrange.o xml-support.o xml-syscall.o xml-utils.o target-descriptions.o target-memory.o xml-tdesc.o xml-builtin.o inferior.o sodata.o gdb_usleep.o record.o record-full.o gcore.o gdb_vecs.o jit.o progspace.o skip.o probe.o common-utils.o buffer.o ptid.o gdb-dlfen.o common-agent.o format.o registry.o btrace.o record-btrace.o waitstatus.o print-utils.o rsp-low.o errors.o common-debug.o debug.o common-exceptions.o btrace-common.o fileio.o common-regcache.o compile-o-symbols.o compile-c-types.o compile-object-load.o compile-object-run.o compile-loc2c.o compile-c-support.o inflow.o init.o.\ ../sim/ppc/libsim.a ../readline/libreadline.a ../opcodes/libopcodes.a ../bfd/libbfd.a -L./../zlib -lz ../libiperty/libiperty.a ../libdecnumber.libdecnumber.a -lncurses -lm -L/System/Library/Frameworks/Python.framework/Versions/2.7/lib/python2.7/config -ldl -framework CoreFoundation -lpython2.7 -u _PyMac Error /System/Library/Frameworks/Python.framework/Versions/2.7/Python -lexpat .../libiberty/libiberty.a buld-gnulib/import/libgnu.a -liconv Undefined symbols for architecture x86_64: "_error", referenced from: _sim_io_printf_filtered in libsim.a(sim_calls.o) _sim_create_inferior in libsim.a(sim_calls.o) _sim_io_read_stdin in libsim.a(sim_calls.o) _sim_io_read_stdin in libsim.a(sim_calls.o) _sim_io_write_stdout in libsim.a(sim_calls.o) _____in_io_write_stdout in libsim.a(sim_calls.o) __sim_io_write_stdour in libsim.a(sim_calls.o) sim io flush stdoutput in libsim.a(sim calls.o) (maybe you meant: _device_error, _Z20host_to_fileio_errori , _bfd_get_error_handler , _bfd_default_error_handler , _bfd_set_error_program_name , _Z28dwarf_reg_to_regnum_or_errorP7gdbarchm , _Z29observer_detach_command_errorP8observer , _sim_io_error , _deprecated_error_pedin_hook , _Z35throw_max_completions_reached_error v, _Z25type_name_no_tag_or_errorP4type , _Z12memory_error18target_xfer_statusm , _bfd_error_handler , _Z20annotate_error_beginv , _Z19compile_rx_or_errorP17re_pattern_bufferPKcS2_ , _Z12catch_errorsPFiPVES_PCllreturn_mask , _Z20memory_error message18target_xfer_statusP7gdbarchm , _Z11range_errorPKcz , _Z13gdb_xml_errorP14gdb_xml_parserPKcz , _Z29observer_notify_command_errorv , _gdbpy_gdb_memory_error , _bfd_set_error , _Z23invalid_thread_id_errorPKc , _Z11throw_error6errorsPKcz , _gdbpy_gdb_error , _Z14gdb_bfd_errorPPC , _Z29observer_attach_command_errorPFvvE , _Z27gdbpy_print_python_errorspv , _bfd_get_error , _Z14internal_errorPKcis0_z , _Z17get_regcomp_erroriP17re_pattern_buffer , _Z14annotate_error ) ld: symbol(s) not found for architecture x86_64 clang: error: linker command failed with exit code 1 (use -v to see invocation) lit:symbol(s) hot found for architecture xos_o4
clang: error: linker command failed with exit code 1 (use -v to see invocation)
make[2]: *** [gdb] Error 1
make[1]: *** [all-gdb] Error 2
make: *** [all] Error 2
hold of foiled. (bic/ch error (three course builder(three/build/course streng) shell cmd failed: /bin/sh -ex /rtems-source-builder/rtems/build/powerpc-rtems4.12-gdb-7.12-x86_64-apple-darwin14.5.0-1/doit error: building powerpc-rtems4.12-gdb-7.12-x86 64-apple-darwin14.5.0-1 • • #3054 3 years ago fixed unspecified AndiK Andreas Kölbl <andreas.koelbl@...> 2 years ago gdb 7.12.1 on RSB 4.12 branch fail to build on Archlinux Summar 7.12.1 does not compile with latest guile As already stated here: https://sourceware.org/bugzilla/show_bug.cgi?id=21104 GDB in version 7.12.1 fails when trying to compile on Descripti Archlinux. GDB uses deprecated functions of libguile which were gone in version 2.2 of libguile. As GDB states in its configure script to support version 2.2 of libguile it fails compiling. Tested with the latest rtems source builder on master. <del>#3056</del> 3 years ago fixed Sebastian Huber Sebastian Huber 2 years ago score Add EDF SMP scheduler Summar The current SMP schedulers are all fixed-priority schedulers. Add a job-level fixed priority scheduler (EDF). Descripti #3057 3 years ago fixed tool/qcc Sebastian Huber Daniel Hellstrom 2 years ago Add a workaround for the LEON3FT store-store errata Summar GCC needs support to provide a workaround for the LEON3FT store-store errata, e.g. Descripti https://gcc.gnu.org/ml/gcc-patches/2017-06/msg01577.html on and follow up versions. #3059 3 years ago fixed score Sebastian Huber Sebastian Huber 2 vears ago Add a simple processor affinity support to the EDF SMP scheduler Summar Add support to the EDF SMP scheduler to honour one-to-one and one-to-all thread processor affinities. Use one ready queue for threads with a one-to-all affinity. Use one ready Descripti queue for each of the one-to-one threads for each processor. Since a red-black tree is used for the ready queues, the space overhead of one pointer per ready queue is small. tool #3061 3 years ago invalid Chris Johns chrisi@.. 2 years ago including 'unistd.h' in C++ does not build. Summar

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on

	Including unistd.h	in a C++ program	n does not compile with	the RSB for today:			
	rtems4.12/xili In file includ /opt/work/rtem /opt/work/rtem BIT_AND2(_c ^~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	<pre>nx_zynq_zc706/li led from /opt/woo from /opt/woo from /opt/woo from u.cpp:6. is/4.12/arm-rtems pu_set_bits(sets)</pre>	<pre>b -specs bsp_specs rk/rtems/4.12/arm-rt rk/rtems/4.12/arm-rt rk/rtems/4.12/arm-rt s4.12/arm-rt s4.12/include/sys/cp s4.12/include/sys/cp size), destset, srcs</pre>	-qrtems -march=armv7-a - ems4.12/include/sys/type iems4.12/include/sys/type iems4.12/include/sys/unis iems4.12/include/unistd.h buset.h: In function 'voi puset.h:147:3: error: 'BI set1, srcset2);	<pre>mthumb -mfpu=neon -mfloat-abi=h readtypes.h:24:0, s.h:239, td.h:12, :4,</pre>	-B/opt/work/si/rtems/4.12/arm- ard -mtune=cortex-a9 -g -02 u.cpp const cpu_set_t*, const cpu_set_t* .s scope	)':
	BIT_AND2(_c ^~~~~~ BIT_AND /opt/work/rtem /opt/work/rtem	pu_set_bits(sets us/4.12/arm-rtems us/4.12/arm-rtems	size), destset, srcs	setl, srcset2); puset.h: In function 'voi puset.h:159:3: error: 'BI	_	<pre>const cpu_set_t*, const cpu_set_t*) s scope</pre>	·:
			s4.12/include/sys/cp ize), destset, srcse		ested alternative: 'BIT_OR'		
	/opt/work/rtem	s/4.12/arm-rtems		puset.h:171:3: error: 'BI	d CPU_XOR_S(size_t, cpu_set_t*, T_XOR2' was not declared in thi	<pre>const cpu_set_t*, const cpu_set_t* .s scope</pre>	)':
	/opt/work/rtem		s4.12/include/sys/cp size), destset, srcs		ested alternative: 'BIT_OR'		
	/opt/work/rtem /opt/work/rtem	us/4.12/arm-rtems		puset.h:183:3: error: 'BI	d CPU_NAND_S(size_t, cpu_set_t; T_NAND2' was not declared in th	<pre>c, const cpu_set_t*, const cpu_set_t is scope</pre>	*)':
	/opt/work/rtem BIT_NAND2 (		s4.12/include/sys/cp tsize), destset, sro		ested alternative: 'BIT_NAND'		
	BIT_NAND In file includ	from /opt/woo from /opt/woo from /opt/woo from /opt/woo	rk/rtems/4.12/arm-rt rk/rtems/4.12/arm-rt rk/rtems/4.12/arm-rt rk/rtems/4.12/arm-rt	cems4.12/include/sys/cpus cems4.12/include/sys/_pth cems4.12/include/sys/type cems4.12/include/sys/unis cems4.12/include/unistd.h	readtypes.h:24, s.h:239, td.h:12,		
Descripti on	/opt/work/rtem	s/4.12/arm-rtems	s4.12/include/sys/cp	ouset.h:194:10: error: '_	CPU_COUNT_S(size_t, const cpu_ _bitcountl' was not declared in		
	^				gested alternative: ' count'		
	4						•
<del>#3063</del>	3 years ago	fixed	config	Sebastian Huber	Sebastian Huber	2 years	s ago
Summar y Descripti		iler supports simple	e thread processor affi			efault SMP scheduler lacks support for thre e libraries like libgomp, MTAPI, work steal	
on <b>#3069</b>					ad processor affinity for example. Sebastian Huber	2 years	
Summar y	Add rtems_scheduler					2 years	

	* @brief Identifies a sche	eduler by a processo	TINGER.		
	* * @param[in] cpu_index The * @param[out] id The sched		ociated with the processo	index	
Descripti on	* @retval RTEMS_SUCCESSFUI	L Successful operati	.on.	. Index.	
	<pre>* @retval RTEMS_INVALID_AI * @retval RTEMS_INVALID_NA * @retval RTEMS_INVORRECT_ * processor is not owned */</pre>	AME Invalid processo _STATE The processor	or index.	this	
<del>¥3070</del>	3 years ago fixed	rtems	Sebastian Huber	Sebastian Huber	2 years ago
ummar y	Add rtems_scheduler_ident_by_pro	cessor_set()			
у	/**				
	<pre>/**  * @brief Identifies a sche  *</pre>	eduler by a processo	or set.		
	* The scheduler is selecte * in the specified process		highest numbered online p	rocessor	
	* * @param[in] cpusetsize Si		nrocessor set buffer in		
	* bytes. This value mus * @param[out] cpuset The p * @param[out] id The sched	st be positive. processor set to ide	entify the scheduler.	set.	
	* @retval RTEMS_SUCCESSFUL				
Descripti	<pre>* @retval RTEMS_INVALID_AI * @retval RTEMS_INVALID_SI * @retval RTEMS_INVALID_NX * @retval RTEMS_INCORRECT * highest numbered onlin</pre>	IZE Invalid processo AME The processor se _STATE The processor	or set size. et contains no online proce s set is valid, however, th	le	
on	<pre>* owned by a scheduler. */ rtems status code rtems sch</pre>				
	<pre>size_t cpusetsize, cpu_set_t *cpuset, rtems_id *id</pre>				
	);				
<del>3071</del>	3 years ago fixed	bsps	Sebastian Huber	Sebastian Huber	2 years ago
ummar	3 years agofixedCreate an interrupt server for every	· · ·		Sebastian Huber	2 years ago
ummar y	Create an interrupt server for every	y processor in the system y processor in the system	m	Sebastian Huber	
ummar y escripti on <b>#3072</b>	Create an interrupt server for every Create an interrupt server for every customize the setup after initializati 3 years ago fixed	y processor in the system y processor in the system ion. arch/sparc	m		
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Summar y	parallel make not	working							
Descripti on	As reported on the	e mailing list, par	rallel make on the master	r is broken.					
<del>#3084</del>	3 years ago	fixed	build	Joel Sherrill	Chris Johns	2 years ago			
Summar y	Makefile recipe ov	erride warning h	as returned						
				th Chris' rework to improve pa I at the top of the build tree	rallelism. Unfortunately, one Makefile has had th	e warning return. To reproduce, complete			
Descripti on	[joel@rtbf64c rtems-work]\$ ./build_bsp sparc erc32 Using rtems for RTEMS source								
				f64c rtems-work]\$ cd b-erc32/ recipe for target `spprofiling01	[joel@rtbf64c b-erc32]\$ make >/dev/null Make	file:653: warning: overriding recipe for			
<del>#3085</del>	target `spprofiling 3 years ago	01' Makefile:653 fixed	: warning: ignoring old r arch/powerpc			file:653: warning: overriding recipe for 2 years ago			
Summar	target `spprofiling	01' Makefile:653 fixed	: warning: ignoring old r arch/powerpc	ecipe for target `spprofiling01					
Summar y	target `spprofiling 3 years ago Add hypervisor su	01' Makefile:653 fixed pport for QorIQ I	: warning: ignoring old r arch/powerpc	ecipe for target `spprofiling01					
Summar y	target `spprofiling 3 years ago Add hypervisor su NXP provides a hy	01' Makefile:653 fixed pport for QorIQ I pervisor (Topaz)	8: warning: ignoring old r arch/powerpc BSPs for the QorIQ platform.	recipe for target [°] spprofiling01 Sebastian Huber	Sebastian Huber				
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	Background There	are two ways t	o address a block in R	RFS:					
	1. Via a single 32bit block number (bno) 2. Via a group number(gno) and a bit offset (bit)								
				words the equation to convert	t to 2 should be unique within the DEC imple	montation			
	They should be fully convertible (1-1 mapping). In other words, the equation to convert 1 to 2 should be unique within the RFS implementation. The bug The RFS implementation contains two different conversions between 1 and 2.								
	Details								
	1. In rtems_rfs_group_bitmap_alloc (rtems-rfs-group.c, line 172)								
	<pre>bno = gno * group_blocks + bit</pre>								
	<ol> <li>In rtems_rfs_group_bitmap_alloc (rtems-rfs-group.c, line 228)</li> </ol>								
	2. In rtems_rfs_group_bitmap_alloc (rtems-rfs-group.c, line 228) bno = gno * group_blocks + bit + 1 (via rtems_rfs_group_block() function)								
	<pre>bno = gno * group_blocks + bit + 1 (via rtems_rfs_group_block() function) 3. In rtems_rfs_group_bitmap_free (rtems-rfs-group.c, line 283)</pre>								
	L	_		EMS_RFS_SUPERBLOCK_SIZE)					
			_test (rtems-rfs-group	o.c, line 332)					
	bno = g	no * group_bl	ocks + bit						
	To summarize, the	implementation	contains two ways of	converting a bno to a (gno, bit)	pair:				
Descripti	Either:								
on	bno = gno * g	roup_blocks +	bit						
	Or:								
	bno = gno * g	roup_blocks +	bit + 1						
	The Fix The RFS in	nplementation s	hould consistently cor	nvert a bno to a (gno, bit) pair w	ith:				
	bno = gno * g	roup_blocks +	bit + RTEMS_RFS_SU	UPERBLOCK_SIZE					
	This is because the	superblock is n	ot accounted for in the	e block bitmaps. So places to cha	ange:				
	1. rtems-rfs-gr	oup.c: all refere	ences to the conversion	n must be updated to use RTEMS	S_RFS_SUPERBLOCK_SIZE explicitly.				
	2. rtems_rfs_g	roup_block conv	verts the pair to bno v	ia:					
	#define	rtems_rfs_gr	oup_block(_g, _b)	(((_g)->base) + (_b))					
	(_g)->base is calcu	lated via rtems-	-rfs-format.c from:						
	#define rtems	_rfs_fs_block	(_fs, _grp, _blk)	\					
	((((_fs)->g	roup_blocks)	* (_grp)) + (_blk)	+ 1)					
				know it is a superblock and eds to change it is handled					
	* superblock */ #define RTEMS	s but if for _RFS_SUPERBLO	any reason this nee CK_SIZE (1)	eds to change it is handled					
<b>#3090</b> Summar y	* superblock */ #define RTEMS	s but if for	any reason this nee			2 years ago			
Summar y	<pre>* superblock */ #define RTEMS 3 years ago Add BSP for i.MX 7 3 years ago</pre>	s but if for RFS_SUPERBLO fixed fixed	any reason this nee CK_SIZE (1) bsps tool	eds to change it is handled		2 years ago 2 years ago			
Summar y <b>#3091</b> Summar	<pre>* superblock */ #define RTEMS 3 years ago Add BSP for i.MX 7</pre>	s but if for RFS_SUPERBLO fixed fixed	any reason this nee CK_SIZE (1) bsps tool	eds to change it is handled Sebastian Huber	Sebastian Huber				
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<u>у</u>	[joel@localhost rter 4.12.78bbe4c1a31c 'g++' (C++ compile yes Checking for he getrusage : yes Che for 'g++' (C++ com 'configure' finished verbose Waf: Enter Wall', '-Wextra', '-p I/rtemstoolkit/lib DRTEMS_RELEASE= process.cpp.7.0'] Ir from/rtem /rtemstoolkit/libib # define AT /rtemstoolkit/libib extern char In file included from from/rtem /rtemstoolkit/libib # define AT /rtemstoolkit/libib # define AT /rtemstoolkit/libib # define AT /rtemstoolkit/libib # define AT /rtemstoolkit/libib # define AT /rtemstoolkit/libib # define AT /rtemstoolkit/libib # define AT	ns-tools]\$ ,/waf co l (4.12) Checking fr ry) : /usr/bin/g++ + wader string.h : yes adder sys/types.h : acking for program inpiler) : /usr/bin/g- successfully (0.786 ing directory `/hon edantic', '-Irtemstoo olchain/common', "4.12.78bbe4c1a3 file included from stoolkit/rld-process erty/ansidecl.h:169 rRIBUTE_NONNULL erty/libiberty.h:112 *basename (const - n/rtemstoolkit/rld stoolkit/rld-process erty/ansidecl.h:169 rr.instoolkit/rld-process erty/ansidecl.h:169 rr.instoolkit/rld-process erty/ansidecl.h:169 rr.instoolkit/rld-process erty/ansidecl.h:169 rr.instoolkit/rld-process erty/ansidecl.h:169 rr.instoolkit/rld-process erty/ansidecl.h:169 rr.instoolkit/rld-process erty/ansidecl.h:169 rr.instoolkit/rld-process erty/ansidecl.h:169 rr.instoolkit/rld-process erty/ansidecl.h:169 rr.instoolkit/libiberty. wasprintf (char , co om/rtemstoolki	nfigure Setting top or program 'pythor Checking for head yes Checking for yes of the set of the set of the set of the set of the set of the set of the set of the set of the set of the set of the set of the set of the set	<pre>i' : /usr/bin/python Checking for r alloca.h : yes Checking for header sys/wait.h : yes Checking for header sys/wait.h : yes Checking for header sys/wait.h : yes Checking for header sys/wait.h : yes Checking t Checking for header sys/wait. ++' (C++ compiler) : /usr/bin/ istrophy '-i./rtemstoolkit/libiber EVEL=1', './rtemstoolkit/libiber EVEL=1', './rtemstoolkit/libiber EVEL=1', './rtemstoolkit/libiber EVEL=1', './rtemstoolkit/libiber EVEL=1', './rtemstoolkit/libiber EVEL=1', './rtemstoolkit/rld-pr berty/libiberty.h:42:0, aclaration 'char* basename(cor anull (m))) ansion of macro 'ATTRIBUTE_NV &amp; RETURNS_NONNULL ATTRIBUTE : /usr/include/string.h:599:26: *filename) 2:0, ation of 'int vasprintf(char, con ((nonnull (m))) n expansion of macro 'ATTRE ((format (printf, m, n))) AT in expansion of macro 'ATTRE ist) ATTRIBUTE_PRINTF(2,/ p:23:0: /usr/include/stdio.</pre>	ems-tools Setting out to : /home/joel/rtems-woo or python version >= 2.6.6 : 2.7.5 Checking for eader fcntl.h : yes Checking for header process. header sys/file.h : yes Checking for header sys, g for header unistd.h : yes Checking for header h : yes Checking for function kill : yes Checking g++ Checking for function open64 : not found (0 ====================================	'gcc' (C compiler) : /usr/bin/gcc Checking f h : not found Checking for header stdlib.h : s/stat.h : yes Checking for header sys/time.l vfork.h : not found Checking for function for 'gcc' (C compiler) : /usr/bin/gcc Checki Checking for function stat64 : not found joel@localhost rtems-tools]\$ ./waf -j 1 ; runner ['/usr/bin/g++', '-pipe', '-g', '-O2', Irtemstoolkit/elftoolchain/common', '- N=*4.12'', '- s-tools/build/rtemstoolkit/rld- basename(const char*)'
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	Add RISC-V support				
y escripti on	Add RISC-V 32-bit tool chain to RSB co	onsisting of Binutils, G	CC, Newlib and GDB. Add CPU	port and a basic simulator BSP.	
• <b>3111</b> ummar	3 years ago fixed Newlib: Change time_t and clock_t inte	tool/newlib eger types to 64-bit	Sebastian Huber	Sebastian Huber	2 years ago
y 3112 ummar	3 years ago fixed POSIX: Make pthread_mutex_t self-co	<b>posix</b> ntained	Sebastian Huber	Sebastian Huber	2 years ago
У	Change the POCIV mutay into a solf as	entriped object using	cove/lock by o e		
	Change the POSIX mutex into a self-co	ontained object using	<sys lock.n="">, e.g.</sys>		
escripti on	<pre>typedef struct {     struct _Mutex_recursive_Con     unsigned int     struct _Scheduler_Control    uint64_t     pthread_mutex_t;</pre>	trol _mutex; _flags; *_scheduler; _priority_ce:	ling;		
<del>3113</del>	3 years ago fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago
ummar	POSIX: Make pthread_cond_t self-cont	ained			
У	Change the DOCIV and then we work to it				
	Change the POSIX condition variable in	ito a seir-contained of	oject using <sys lock.n="">, e.g.</sys>		
escripti	typedef struct { struct _Condition_Control	condition:			
on	pthread_mutex_t *	mutex;			
	<pre>clockid_t } pthread_cond_t;</pre>	_clock;			
<del>3114</del>	3 years ago fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago
ummar	POSIX: Make pthread_barrier_t self-co				2 years ago
у					
	Change the POSIX barrier into a self-co	ontained object using	<sys lock.h="">, e.g.</sys>		
	typedef struct {				
escripti on	<pre>struct _Thread_queue_Queue unsigned int</pre>	_queue; flags;			
	unsigned int	_count;			
	<pre>} pthread_barrier_t;</pre>				
<del>3115</del>	3 years ago fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago
ummar	POSIX: Make pthread_rwlock_t self-co	ntained			
У	Change the POSIX read-write lock into	a self-contained obje	tusing <svs h="" lock=""> e.g.</svs>		
		a sen containea obje	ce using (sys)locking, e.g.		
escripti	typedef struct { struct Thread queue Queue	queue;			
on	unsigned int	flags;			
	<pre>unsigned int } pthread_rwlock_t;</pre>	_readers;			
<del>3116</del>	3 years ago fixed	nosiy	Sebastian Huber	Sebastian Huber	2 1/22/20 2020
	3 years ago fixed POSIX: Make sem t self-contained	posix	Sebastian Huber	Sebasilali Huber	2 years ago
ummar y					
	Change the POSIX semaphore into a se	elf-contained object us	sing <sys lock.h="">, e.g.</sys>		
escripti on	<pre>typedef struct {    struct _Semaphore_Control _;</pre>	sem;			
	l com ha				
2117	} sem_t;	ccoro	Sobaction Huber	Sobaction Huber	
	3 years ago fixed	score	Sebastian Huber	Sebastian Huber	2 years ago
				Sebastian Huber	2 years ago
ummar y escripti	3 years ago fixed score: Optimize _Thread_queue_Enque Use the Thread_queue_Context::enque	eue() timeout handling	3	Sebastian Huber	
ummar y escripti on	3 years ago fixed score: Optimize _Thread_queue_Enque Use the Thread_queue_Context::enque dependency to _Thread_Timeout().	eue() timeout handling	imeout handling. This avoids t	ne switch statement in _Thread_queue_Timeout	(). It removes the thread queue
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y escripti on <b>3121</b> y escripti on <b>3122</b> ummar y	3 years ago       fixed         score: Optimize _Thread_queue_Enque         Use the Thread_queue_Context::enque         dependency to _Thread_Timeout().         3 years ago       fixed         clock() implementation in Newlib is broc         Newlib uses _times_r() in clock(). The         CLOCKS_PER_SEC.         FreeBSD uses getrusage() for clock().         3 years ago       fixed         Simplify and unify BSP_output_char	eue() timeout handling eue_callout to do the tool/newlib iken problem is that the _t bsps	Sebastian Huber	ne switch statement in _Thread_queue_Timeout Sebastian Huber ined by sysconf(_SC_CLK_TCK). The clock freque	(). It removes the thread queue 2 years ago ency of clock() is the constant 13 months ago
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on # <b>3126</b>	3 years ago	fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago
ummar y	Accept PTHREAD	PROCESS_SHAP	RED for POSIX barrie	rs		
escripti	Since we have on	ly one process, s	sharing between proc	cesses is trivial.		
on 3127	3 years ago	fixed	tool/gcc	Chris Johns	Chris Johns	19 months ago
ummar	MIPS tool build or					
У	This is the same	bug that effects	FreeBSD. For details	S66.		
escripti on		-		https://gcc.gnu.org/bugzilla/show_	_bug.cgi?id=62097	
<del>3128</del>	3 years ago	fixed	tool	Chris Johns	chrisj@	2 years ago
ummar y	RTEMS Tools corv	ar does not buil	d on Windows.			
	The following erro	or has appeared	on Windows:			
escripti on	/rtemstoo #include <	from/ from/ from/ from/ from/ from/	rtemstoolkit/rld- rtemstoolkit/rld- rtemstoolkit/rld- tester/covoar/Obj tester/covoar/app tester/covoar/app	<pre>h:72, process.h:31, dumpProcessor.h:16, iredSymbols.h:18, _common.h:6, _common.cc:40:</pre>	eue.h: No such file or directory	
	Waf: Leavin	g directory `D	:/opt/rtems/rsb.g	it/rtems/build/rtH/rtems-tool:	s.git/build'	
	3 years ago	fixed	tool	Chris Johns	joel	2 years ago
ummar y	RTEMS Tools cove		WINdows			
	The following war	nings and errors	are present so the F	SB tools do not finish and install:		
escripti on	/rtemstoo th /rtemstoo rld::e /rtemstoo (100/150] C (100/150] C (103/150] C (103/150] C (106/150] C (106/150] C (106/150] C (109/150] C (110/150] C (111/150] C (114/150] C (115/150] C (116/150] C (116/150] C (116/150] C (116/150] C (116/150] C (116/150] C (119/150] C	<pre>lkit/rld-files row rld_error_ ^^</pre>	.cpp:256:15: note at ("references w) "" "55: note: in C++ d::string (FIL) .cpp:256:15: note at ("references w) "" "/covoar/coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Coveragel r/covoar/Co	Factory.cc Map.cc MapBase.cc Ranges.cc ReaderBase.cc ReaderRTEMS.cc ReaderTSIM.cc WriterRTEMS.cc WriterSkyeye.cc WriterSkyeye.cc WriterSkyeye.cc WriterSkyeye.cc Usins.cc LeInfo.cc ions.cc .cc tionData.cc cccessor.cc per function 'void Coverage::1 error: 'realpath' was not dec	<pre>^ ^  pror_at' xcept S)) ^  pror_at' DesiredSymbols::determineSourceLines(Coverse)</pre>	erage::CoverageRanges*,
	Build faile	d	-	tus 1 (run with -v to display	more information)	
+ <del>3130</del>	3 years ago	fixed	doc	Chris Johns	chrisj@	2 years ago
ummar	RTEMS Doxygen.	in latex output d	oes not build			
У	Doxygen latex ou	tput on sync.rte	ms.org does not build	d.		
escripti on			-	and what tool combination.		
on	If it does not buil	d we should cons	sider defaulting the s	etting for latex output to "no".		
	3 years ago	fixed	fs	Sebastian Huber	Sebastian Huber	2 years ago
ummar y	Add reference co	unting to file des	criptors			

The use of a file descriptor after or during a close() operation may result in a use after free. Finding such errors in applications is difficult. Especially in SMP systems using the highly dynamic libbsd network stack.

The file descriptor objects reside in a table with a application configuration defined size. So, the storage of a file descriptor object is always present, only the referenced file system node may change over time. The file system nodes may use an internal reference counting, which is independent of the file descriptors.

To implement reference counting for the file descriptors add a bit field for the reference count to the rtems_libio_t::flags and use atomic operations to maintain the flags.

escripti on	<pre>int op( int f {     rtems_libic     unsigned in</pre>							
Descripti on								
			ns_libio_number_iops nrn_minus_one( EBADF					
	<pre>iop = rtems_libio_iop( fd ); flags = rtems_libio_iop_hold( iop );</pre>							
	<pre>rtems_lib rtems_set } do_op( iop,</pre>	bio_iop_drop( _i _errno_and_retu	rr_minus_one( EBADF	);				
					e, no close operation will be performed.			
	3 years ago Remove rtems_libio	fixed p_t::driver	fs	Sebastian Huber	Sebastian Huber	2 years ago		
	Remove unused rte	ms_libio_t::drive	r member.					
	3 years ago	fixed	fs	Sebastian Huber	Sebastian Huber	2 years ago		
Summar F y	Remove LIBIO_FLA	.GS_CREATE						
Descripti F	Remove unused LIE	3IO_FLAGS_CREA	TE flag.					
on #3135 3	3 years ago	fixed	admin	Sebastian Huber	amar@	2 years ago		
	Devel mailing list d	oesn't work and G	it push impossible due	to disk full				
У								
	Delta compress Compressing of Writing object Total 18 (del remote: error remote: fatal	ta 16), reused : file write er : unable to wri	7/17), done. 8), 1.68 KiB   0 byte 0 (delta 0) eror (No space left o tte shal file	on device)				
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	results in an error: gmake[5]: Entering directory '/opt/work/chris/rtems/kernel/bsps/beagleboneblack/arm-rtems4.12/c/beagleboneblack/cpukit/score' arm-rtems4.12-gccpipe -DHAVE CONFIG H -II//cpukit///.beagleboneblack/lib/include -mcpu=cortex-a8 -O2 -g -ffunction-sections -								
escripti on	<pre>fdata-secti. -MP -MF src: '/opt/work/ In file inc. /opt/work/cl did you meau Asser //cpuki ( ( /opt/work/cl each functi Asser //cpuki ( ( gmake[5]: *' We need the rte Tools are:</pre>	<pre>ns -Wall -Wmie /.deps/libScore chris/rtems/ker pris/rtems/ker rris/rtems/ker rris/rtems/ker cl sem-&gt;count &lt; c///beag </pre>	<pre>sing_prototypes -W2 a_a-semaphore.Tpo -c cnel/rtems.git/c/src /work/chris/rtems/ iel/rtems.git/c/src, } gleboneblack/lib/ind hel/rtems.git/c/src, in &lt; UINT_MAX ); ^ gleboneblack/lib/ind 571: src/libscore_a- to be run on a regula</pre>	<pre>mplicit-function-declaratic c -o src/libscore_a-semaphon //././cpukit/score/`src/sema semapl/rtems.git/c/src/././ /././cpukit/score/src/sema clude/rtems/score/assert.h:{ /.//cpukit/score/src/sema clude/rtems/score/assert.h:{ /.//cpukit/score/src/sema clude/rtems/score/assert.h:{ /.//cpukit/score/src/sema clude/rtems/score/assert.h:{ /.//cpukit/score/src/sema clude/rtems/score/assert.h:{ /.//cpukit/score/src/sema clude/rtems/score/assert.h:{ ///cpukit/score/src/sema clude/rtems/score/assert.h:{</pre>	on -Wstrict-prototypes -Wnested-externs re.o `test -f 'src/semaphore.c'    echo	-MT src/libscore_a-semaphore.o -MD			
	arm-rtems4.	12-gcc (GCC) 7.		5 4.12, RSB e6d0a8bae6d16eba	605370calla5928b797820bb-modified, Newl				
	3 years ago	fixed	doc something smaller.	Chris Johns	Chris Johns	2 years ago			
ummar v	Change the BSP r	towto's name to	something smaller.						
	The BSP Howto's	current name is:							
	RTEMS BS	SP and Device D	Priver Development (	Guide					
escripti on	This is long and c	auses problems i	n the PDF output. Chai	nge the name to:					
		SP and Driver G		-					
<del>3142</del>	3 years ago	fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago			
ummar v	POSIX: Reduce si	ze of pthread_on	ce_t and make it zero-	initialized					
escripti on	A zero-initialized	pthread_once_t r	reduces the ROM usage	e of RTEMS applications, since th	e global pthread_once_t objects may reside in	the BSS section.			
<del>3148</del>	3 years ago	fixed	posix	Chris Johns	joel.sherrill@	2 years ago			
ummar	PSXRDWRV Test	ailure on Beaglel	bone Black						
У	Running rtema-t	est with a recer	at u-boot and a current	master this failure is reported:					
escripti on	] ] *** BEGIN ] writev bad ] writev er:	<pre>gleboard: am335 OF TEST PSXRDW d file descript cor 1: 22=Inval ing error test</pre>	NRV *** cor EBADF Lid argument						

	The crash with a Linux type image	and no FDT is:			6
	<pre>  ## Booting kernel from : ] Image Name: RTEMS ] Image Type: ARM Li; ] Data Size: 60886 ; ] Load Address: 800000 ] Entry Point: 800000 ] Verifying Checksum . ] Uncompressing Kernel ] Starting kernel ] ] data abort ] ] MAYBE you should re. ] ] pc : [&lt;8000010c&gt;] ] sp : 80101000 ip : 000</pre>	Legacy Image at 8200 hux Kernel Image (gz 3ytes = 59.5 KiB 00 OK Image OK Ad doc/README.arm-un lr : [<800000ac>] 000c fp : 9f35ac2	ip compressed) aligned-accesses 8		
escripti on	] r10: 9f3ad0f4 r9 : 000 ] r7 : 0000000 r6 : 800 ] r3 : 9f238fe0 r2 : 800 ] Flags: nzcv IRQs off ] Resetting CPU	00100 r5 : 00000e0 00100 r1 : 00000e0	5 r4 : 60000193 5 r0 : 60000193		
	and the code is:				
	BSP_START_TEXT_SECTION vo	id bsp_start_hook_0(	void)		
	} 80000104: e12fffle	bx lr			
	80000108 <bsp_start_hook_ BSP_START_TEXT_SECTION st. {</bsp_start_hook_ 		re_start_set_vector_base(	void)	
	/*  * Do not use bsp_vecto.  */  if (bsp vector table end		since this will get optim e size) {	ized away.	
	80000108: e3002040 8000010c: e3003040	movw r2, #			
	80000110: e3482000 80000114: e3403000	movt r2, #	32768 ; 0x8000		
	80000114: e3403000 80000118: e1520003	movt r3, # cmp r2, r			
<del>153</del>	3 years ago fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago
nmar	Accept PTHREAD_PROCESS_SHAR	ED for POSIX rwlocks			
y cripti	Since we have only one pro	ocess, sharing between	processes is trivial.		
y cripti on	Since we have only one pro 3 years ago fixed	ocess, sharing between unspecified	processes is trivial. Jeff Mayes	Joel Sherrill	2 years ago
y cripti in <b>157</b> imar		unspecified		Joel Sherrill	2 years ago
y scripti on <b>157</b> nmar y	3 years ago fixed PowerPC tools don't build on 32-b	unspecified t hosts	Jeff Mayes	Joel Sherrill	2 years ago
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y cripti on <b>157</b> nmar	3 years ago       fixed         PowerPC tools don't build on 32-b         Using RSB and trying to build Power         using RSB and trying to build Power         tools/rsb/tems/build/powerpc-rte         tools/rsb/tems/build/powerpc-rte         B/desk/rtems/powerpc-rtems4.12         conftest.c >&S Assembler         PACKAGE_BUGREPORT ""   #define         Years ago       fixed         B years ago       fixed         Updating waf breaks the rootfs. Au         S years ago       fixed         Examples v2 trace linker ini files r	unspecified t hosts erPC. Updated RSB just x of object files configu- bols/rsb/rtems/build/po ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.0-newl ms4.12-gcc-7.2.	Jeff Mayes a few days ago. i386 and arr re:3684: /opt/rtems-tools/rsb werpc-rtems4.12-gcc-7.2.0-n ib-2.5.0.20170818-i686-pc-ci ib-2.5.0.20170818-i686-pc-ci ib-2.5.0.20170818-i686-pc-ci ib-2.5.0.20170818-i686-pc-ci ws/powerpc-rtems4.12/inclu supported configure:3688: \$     #define PACKAGE_VERSION //www.gnu.org/software/libg/ cc-7.2.0-newlib-2.5.0.201708 Chris Johns ems-waf.git Chris Johns	n build successfully, but PowerPC fails. /rtems/build/powerpc-rtems4.12-gcc-7.2.0-n ewlib-2.5.0.20170818-1686-pc-cygwin-1/build /gwin-1/build/powerpc-rtems4.12/me6500/m gwin-1/build/powerpc-rtems4.12/me6500/m gwin-1/gcc-7.2.0/newlib/libc/include =b/desk e -isystem /desk/rtems/powerpc-rtems4.12/ * = 1 configure: failed program was:  /* con "1.0"   #define PACKAGE_STRING "GNU C R c/"   /* end confdefs.h. */     int   main ()   l8-i686-pc-cygwin-1/build/powerpc-rtems4.1	ewlib-2.5.0.20170818-i686-pc-cygwin- J/./gcc/ -nostdinc -B/opt/rtems- 64/newlib/-isystem /opt/rtems- 64/newlib/targ-include -isystem /opt/rtems- /rtems/powerpc-rtems4.12/bin/ - sys-include -mcpu=e6500 -m64 -c-g -O2 idefs.h */   #define PACKAGE_NAME "GNU C untime Library 1.0"   #define {     ;   return 0;   } configure:3702: error: in 2/me6500/m64/libgcc': configure:3705: error: 2 years ago

y scripti on		fixed	score					
mmar y scripti				Sebastiar	n Huber	Sebastian Huber	2 years ago	
nmar y			clear that this ticket has				-, Leidela, losign the tickets to a virti	
ımar	We have to many t	ickets with an i	unclear state if someone	is working on t	hem. One problem i	that the tickets are assigned to a real person	by default. Assign the tickets to a virt	
100	New default ticket	assignee: Need	· ·					
, 166	3 years ago	fixed	unspecified	Sebastiar	n Huber	Sebastian Huber	2 years ago	
ımar v	Add I2C device driv	ver for tempera	ture sensor LM75A					
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This is a snip of the errors.								
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	<pre>/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: 'CORE_mutex_Control'? /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: const CORE_mutex_Attributes* a3, bool a4); /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:</pre>				: ^~~~~~			
	/Users/chris		rtems/4.12/bin/arm-rt rtems/4.12/bin/arm-rt		^~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~			
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	'Thread_queue	e_Flush_callo	ut'		hello-deep.c:744:55: error: expected declaration specifiers or '' before			
			rtems/4.12/bin/arm-rt		Accession of 744,55, array, availed dealarction appairies or ! . ! before			
	<pre>/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: uint32_t a3);</pre>				<pre>voidrealCORE_mutex_Flush(CORE_mutex_Control* a1, Thread_queue_Flush_callout a2,</pre>			
	'Thread_queue	e_Flush_callo	ut'		hello-deep.c:743:55: error: expected declaration specifiers or '' before			
			rtems/4.12/bin/arm-rt rtems/4.12/bin/arm-rt					
	a3);	-		-				
				ems4.12-gcc:	void _CORE mute:	Flush(CORE_mutex_Control* al, Thread_q	ueue_Flush_callout a2, uint32 t	
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			rtems/4.12/bin/arm-rt rtems/4.12/bin/arm-rt		printf hello-deep.c: At top level:			
	/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:				^~~~~~			
	'printf'? [-W	Vimplicit-fun	ction-declaration]					
	a function); did you mean 'Thread gueue Operations'? /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:				^			
						ad_blocking_operation_States), "Thread_	<pre>blocking_operation_States" },</pre>	
	/Users/chris,	/development/:	rtems/4.12/bin/arm-rt	ems4.12-gcc:		:13: error: 'Thread_blocking_operation_	States' undeclared here (not in	
	/Users/chris,	/development/:	rtems/4.12/bin/arm-rt rtems/4.12/bin/arm-rt	ems4.12-gcc:	_Th	ead_Start		
	/Users/chris,	/development/:	rtems/4.12/bin/arm-rt		<pre>{ sizeof (Thread_Start_types), "Thread_Start_types" }, ^~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~</pre>			
scripti on		/development/: '_Thread_Sta:		ems4.12-gcc:	hello-deep.c:54	:13: error: 'Thread_Start_types' undecl	ared here (not in a function);	
scripti	/Users/chris	/development/:	rtems/4.12/bin/arm-rt	ems4.12-gcc:				
	/Users/chris,	/development/:		ems4.12-gcc:	<pre>hello-deep.c:443:31: error: expected expression before ')' token { sizeof (Objects Locations*), "Objects Locations*" },</pre>			
	/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:				- <u> </u>			
	/Users/chris,	/development/:	rtems/4.12/bin/arm-rt	ems4.12-gcc:		:31: error: expected expression before		
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		/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:			hello-deep.c:359:31: error: expected expression before ')' token			
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	<pre>did you mean 'Objects_Information'? /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:</pre>							
	/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:			hello-deep.c:321:13: error: 'Objects_Locations' undeclared here (not in a function);				
	/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:			{ sizeof (CORE_mutex_API_mp_support_callout), "CORE_mutex_API_mp_support_callout" }				
	/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: in a function)			hello-deep.c:16	:13: error: 'CORE_mutex_API_mp_support_	callout' undeclared here (not		
	/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /ttems/this/development/ctems/4.12/bin/arm-rtems4.12-gcc				^~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	_		
	/Users/chris.	/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:			hello-deep.c:14			
		/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:						
	<pre>did you mean 'CORE_mutex_Control'? /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:</pre>					_mutex_Status), "CORE_mutex_Status" },		
	/Users/chris	/development/:	rtems/4.12/bin/arm-rt			:13: error: 'CORE_mutex_Status' undecla	red here (not in a function);	
	/Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc: /Users/chris/development/rtems/4.12/bin/arm-rtems4.12-gcc:			Thr	^~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~			
	/Users/chris	/development/:	rtems/4.12/bin/arm-rt	ems4.12-gcc:		ad_queue_Flush_callout), "Thread_queue_	Flush_callout" },	
			rtems/4.12/bin/arm-rt Thread queue Flush fi		hello-deep.c:13	:13: error: 'Thread_queue_Flush_callout	' undeclared here (not in a	

## RT

	Test runs with a interrupt driven console driver result in unreliable test outcomes.									
	Problem was noticed with test runs on Microzed, for example libtest/block08:									
Descripti on	The test prints:									
	** END OF TEST BLOCK 8 ***									
	The rtems-test command marks the result as a failure. There is a single * missing from the start of the line. I attach the full test trace.									
#3171		fixed	tool/gcc	Chris Johns	Chris Johns	19 months ago				
Summar y	RSB GCC does not build on High Sierra and APFS									
Descripti on	The issue has been reported upstream as https://gcc.gnu.org/bugzilla/show_bug.cgi?id=81797									
<del>#3172</del>	3 years ago	fixed	arch/i386	Chris Johns	Chris Johns	2 years ago				
Summar y	i386 PC BSP does not reset when bsp_reset is called.									
Descripti on	Removal of the Ec	lison support ren	noved the standard PC	eset using the keyboard control	ler rather then the specific Edison support.					
<del>#3173</del>	3 years ago	fixed	arch/arm	Chris Johns	Chris Johns	2 years ago				
Summar y	XIlinx AXI I2C dri	ver IP race condi	tion causes clock glitch.							
Descripti on	The Xilinx AXI I20	IP has a race co	ondition when the PIRQ	read FIFO level is reached and t	he clock is throttling.					
<del>#3174</del>	3 years ago	fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago				
Summar y	Remove rtems_pt	hread_attribute_	_compare()							
Descripti on	The rtems_pthrea	d_attribute_com	pare() function is undo	cumented and used only in one t	est. Move it to the test.					
<del>#3175</del>	3 years ago	fixed	score	Sebastian Huber	Sebastian Huber	19 months ago				
Summar y	Merge FreeBSD ti	mecounter chang	jes from 2015-01-20 to	now						
<del>#3176</del>	3 years ago	fixed	tool/newlib	Joel Sherrill	Joel Sherrill	20 months ago				
Summar y	getreent in libc.a	and generated by	y confdefs.h							
Descripti on	from confdefs.h a	nd another from	newlib.	for <u>getreent(). One of the exan</u> addresses the issues and needs t	nples-v2 programs is doing this. It is because there to be incorporated by the RSB.	are two bodies for this method on				
	https://sourcewar	e.org/ml/newlib/	'2017/msg01019.html i	s a cleanup that was spotted at	the same time. It can be picked up by a newlib sna	pshot.				
<del>#3177</del>	3 years ago	fixed	doc	Joel Sherrill	Chris Johns	2 years ago				
Summar y	Replace/update POSIX Compliance Guide									
	The POSIX Compl anymore. This ticl		never converted from t	exinfo. Beyond that, it is out of (	date and follows the outline of the printed version o	f the POSIX standard which no one so				
	anymore. This tick 1. new Sphin: 2. contents g 3. outline per	ket proposes: x document	DSIX API tracking sprea ional area		date and follows the outline of the printed version o	f the POSIX standard which no one so				
y Descripti on	anymore. This ticl 1. new Sphin 2. contents g 3. outline per 4. use bullets 3 years ago	xet proposes: x document enerated from PC .h file, not funct , not tables so ea fixed	DSIX API tracking sprea ional area asier to format tool/gcc	dsheet (CSV) Joel Sherrill	date and follows the outline of the printed version o Joel Sherrill <joel@></joel@>	f the POSIX standard which no one so				
y Descripti on	anymore. This ticl 1. new Sphin 2. contents g 3. outline per 4. use bullets 3 years ago	xet proposes: x document enerated from PC .h file, not funct , not tables so ea fixed	DSIX API tracking sprea ional area asier to format	dsheet (CSV) Joel Sherrill						
y Descripti on	anymore. This ticl 1. new Sphin. 2. contents g 3. outline per 4. use bullets 3 years ago Update sh-rtems4	<pre>xet proposes: x document enerated from PC .h file, not funct , not tables so ea fixed .12 bset to use r</pre>	DSIX API tracking sprea ional area asier to format tool/gcc tems-default (using old	dsheet (CSV) Joel Sherrill gcc)		2 years ago				

Summar y Descripti on Summar y Descripti on <del>#3182</del> Summar y Descripti on <del>#3185</del> Summar y Descripti on	Remove this warnin sparc-rtems4. 3 years ago Various cc1plus warn cc1plus: wa	ng 12-ar: 'u' modif fixed rnings for "valid for aning: command lin ing: command lin ing: command lin ing: command lin fixed timeout implement DCK_REALTIME mu fixed onds to int32_t integer for the upt fixed am Issues two issues in smpl ity0[12] are listed	build         r C/ObjC but not for C+         ne option 'Wmissing         ne option '-Wimplicine         ne option '-Whested-         posix         ation is not POSIX com         st trigger absolute time         score         ime seconds is overkill         build         tests/Makefile.am:         twice. This causes a way	D' is the default (see ` Joel Sherrill ++" -prototypes' is valid for -prototypes' is valid for C/ -prototypes' is valid for C/ Sebastian Huber -pliant 	Joel Sherrill <joel@> r C/ObjC but not for C++ is valid for C/ObjC but not for C++ C/ObjC but not for C++</joel@>	2 years ago 2 years ago 2 years ago 2 years ago s_clock_get_uptime_second 2 years ago	
Summar y Descripti on Summar y Descripti on <b>#3182</b> Summar y Descripti on <b>#3185</b> Summar y Summar y Summar	ar warning: u' moo Remove this warnin sparc-rtems4. 3 years ago Various cc1plus warn cc1plus: warn c	difier ignored s ng 12-ar: 'u' modii fixed rnings for "valid fo hing: command lin hi	ince D' is the default fier ignored since ' build r r C/ObjC but not for C+ ne option '-Wmissing ne option '-Wmissing ne option '-Wmissing ne option '-Wmissing ne option '-Wmissing ne option '-Wmissing station is not POSIX com st trigger absolute time score ime seconds is overkill build tests/Makefile.am:	(see `U') D' is the default (see ` Joel Sherrill ++"  p-prototypes' is valid for t-function-declaration' prototypes' is valid for C/ Sebastian Huber pliant eouts. This is not the case. Th Sebastian Huber . Use int32_t instead for _Tir Joel Sherrill	Joel Sherrill <joel@>         r C/ObjC but not for C++         is valid for C/ObjC but not for C++         C/ObjC but not for C++         ObjC but not for C++         Sebastian Huber         ere is no test for this in RTEMS.         Sebastian Huber         hecounter_Time_uptime. Change derived APIs, e.g. rtem         Sebastian Huber</joel@>	2 years ago 2 years ago 2 years ago 2 years ago 15_clock_get_uptime_second	
Summar y Descripti on Summar y Descripti on <b>#3182</b> Summar y Descripti on <b>#3185</b> Summar y Summar y Summar	ar warning: u' moo Remove this warnin sparc-rtems4. 3 years ago Various cclplus warn cclplus: warn cclplus: warn cclplus: warn cclplus: warn cclplus: warn clplus: warn clock_REALTIME the clock_REALTIME the cl	difier ignored s ng 12-ar: 'u' modii fixed rnings for "valid for ning: command lin ning: command lin ning: command lin fixed fixed timeout implement DCK_REALTIME mu fixed conds to int32_t integer for the upt fixed am Issues	ince D' is the default fier ignored since ' build r r C/ObjC but not for C4 ne option '-Wmissing ne option '-Wimplici ne option '-Wimsted- posix ation is not POSIX com st trigger absolute time score dime seconds is overkill build	(see `U') D' is the default (see ` Joel Sherrill '+" Joel Sherrill '+" Sebastian Huber pliant Sebastian Huber Use int32_t instead for _Tir	Joel Sherrill <joel@>         Joel Sherrill <joel@>         r C/ObjC but not for C++         is valid for C/ObjC but not for C++         C/ObjC but not for C++         Sebastian Huber         ere is no test for this in RTEMS.         Sebastian Huber         hecounter_Time_uptime. Change derived APIs, e.g. rtem</joel@></joel@>	18 months a         2 years ago         2 years ago         2 years ago         s_clock_get_uptime_seconds         2 years ago	
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Summar y Descripti on <b>#3181</b> Summar y Descripti	ar warning: u' mov Remove this warnin sparc-rtems4. 3 years ago Various cc1plus warn cc1plus: warn cc1plus: warn cc1plus: warn	difier ignored s ng .12-ar: `u' modif fixed rnings for "valid for hing: command lin ning: command lin ing: command lin	fier ignored since ` build r C/ObjC but not for C+ ne option '-Wmissing ne option '-Wimplici ne option '-Wimplici	<pre>(see `U') D' is the default (see `    Joel Sherrill +*" g-prototypes' is valid for it-function-declaration' prototypes' is valid for</pre>	Joel Sherrill <joel@> r C/ObjC but not for C++ is valid for C/ObjC but not for C++ C/ObjC but not for C++</joel@>		
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Summar y Descripti	ar warning: u' mo	difier ignored s	ince D' is the default	(see `U')		18 months a	
Summar	ar warning: u' mo	difier ignored s			Sebastian Huber	18 months a	
					Sebastian Huber	18 months a	
	L						
Descripti on	//cpukit/ of type 'stru void _Thread		lib/include/rtems/sc sed(	^ /src///cpukit/libcsup core/threadimpl.h:906:6:	port/src/times.c:37:0: note: expected 'Timestamp_Control * {aka long lc	ong int *}' but argument	
	<pre>static inline void _TOD_Get_zero_based_uptime(</pre>						
	//cpukit/ of type 'stru	<pre>///erc32/1 act bintime *' ae void _TOD_Get_</pre>	lib/include/rtems/sc _zero_based_uptime(	/src///cpukit/libcsup core/todimpl.h:215:20: nc	port/src/times.c:35:0: te: expected 'Timestamp_Control * {aka long long	g int *}' but argument is	
	<pre>sparc-rtems4. sparc-rtems4. sparc-rtems4////. incompatible</pre>	12-ar: `u' modif 12-ar: `u' modif 12-ar: `u' modif .//rtems/c/src .//rtems/c/src	fier ignored since `` fier ignored since `` c//.cpukit/libcs c//.cpukit/libcs Wincompatible-pointe	D' is the default (see ` D' is the default (see ` D' is the default (see ` support/src/times.c: In support/src/times.c:60:	טי) טי)	ro_based_uptime' from	
	//cpukit/ 'Timestamp_Co void _TOD_Ad	(//erc32/1 ontrol * {aka lor djust(	<pre>lib/include/rtems/sc ng long int *}'</pre>	<pre>/src//.cpukit/posix/s core/todimpl.h:287:6: not</pre>	e: expected 'const struct timespec *' but argume	ent is of type	
			c///cpukit/posix s]	<pre>«/src/adjtime.c: In funct «/src/adjtime.c:85:16: wa</pre>	ion 'adjtime': rning: passing argument l of '_TOD_Adjust' from	incompatible pointer typ	
	///.	.//rtems/c/sro		setpreallocated,			

11 Th

	3 years ago fixed	tool/newlib	Joel Sherrill	Sebastian Huber	2 years ago
ummar y	MUTEX_INITIALIZER missing brace	es warning			
Y	Hi				
	Multiple tests have this warning. A	Appears to be something	not quite right in the newlib	.h files.	
	21///// initializer [-Wmissing-bra		/testsuites/psxtests/psxh	drs/pthread/pthread_mutex_unlock.c:27:27: wa	arning: missing braces around
	21////// initializer [-Wmissing-bra		./testsuites/psxtests/psx	hdrs/pthread/pthread_mutex_trylock.c:27:27:	warning: missing braces around
	21/////	//rtems/c/src//.	./testsuites/psxtests/psx	hdrs/pthread/pthread_mutex_timedlock.c:30:27	7: warning: missing braces around
	initializer [-Wmissing-bra 21/////		./testsuites/psxtests/psx	hdrs/pthread/pthread mutex lock.c:27:27: was	rning: missing braces around
	initializer [-Wmissing-bra	aces]		hdrs/pthread/pthread mutex init.c:27:31: way	
	initializer [-Wmissing-bra	aces]			
Descripti on	initializer [-Wmissing-bra	aces]		hdrs/pthread/pthread_mutex_destroy.c:27:28:	
	initializer [-Wmissing-bra		./testsuites/psxtests/psx	hdrs/pthread/pthread_cond_wait.c:28:27: war	ning: missing braces around
	21///// initializer [-Wmissing-bra		./testsuites/psxtests/psx	hdrs/pthread/pthread_cond_timedwait.c:28:27	: warning: missing braces around
	21////// Wmissing-braces]	//rtems/c/src//.	./testsuites/psxtests/psx	autoinit02/init.c:33:25: warning: missing bu	races around initializer [-
		//rtems/c/src//.	./testsuites/psxtests/psx	autoinit01/init.c:29:28: warning: missing br	races around initializer [-
	21/////	//rtems/c/src//.	./testsuites/psxtests/psx	autoinit01/init.c:28:28: warning: missing bu	races around initializer [-
	Wmissing-braces] 21/////	//rtems/c/src//.	./testsuites/psxtests/psx	D	
	t.				
<del>#3190</del>	3 years ago fixed	tool	Chris Johns	Chris Johns	2 years ago
Summar	RTEMS Tester covoar does not link	1			2 /00/0 030
У	The eventables do not link on Ma				
Descripti on	The executables do not link on Ma				
<del>#3191</del>	3 years ago fixed	tool	Chris Johns	Joel Sherrill	2 years ago
Summar y	RTEMS Tester covoar dies with no	arguments.			
1	Running covoar terminate with a	n unhandled exception	with no arguments on the cor	nmand line:	
	\$ ./build/tester/covoar/co	ovoar			
	error missing option: tard				
			ARGET - F FORMAT [-E EXPLA	NATIONS1 -1 EXECUTABLE coverage1 coverage	aeN
	Usage: ./build/tester/covo	oar/covoar [-v] -T TA		NATIONS] -1 EXECUTABLE coverage1 coverage	
	Usage: ./build/tester/covo	oar/covoar [-v] -T TA		NATIONS] -1 EXECUTABLE coverage1 coverage NATIONS] -e EXE_EXTENSION -c COVERAGEFILE_EX	
	Usage: ./build/tester/covo OR Usage: ./build/tester/covo	oar/covoar [-v] -T Ti oar/covoar [-v] -T Ti	ARGET -f FORMAT [-E EXPLA		
Descrinti	Usage: ./build/tester/covo OR Usage: ./build/tester/covo EXECUTABLE2 -v -T TARGET	oar/covoar [-v] -T Ti oar/covoar [-v] -T Ti - verbose at init: - target name	ARGET -f FORMAT [-E EXPLA ialization	NATIONS] -e EXE_EXTENSION -c COVERAGEFILE_EX	
Descripti on	Usage: ./build/tester/covo OR Usage: ./build/tester/covo EXECUTABLE2 -v -T TARGET -f FORMAT -E EXPLANATIONS	<pre>aar/covoar [-v] -T Ti par/covoar [-v] -T Ti - verbose at init: - target name - coverage file fo - name of file wi</pre>	ARGET -f FORMAT [-E EXPLA ialization ormat (RTEMS, QEMU, TSIM th explanations	NATIONS] -e EXE_EXTENSION -c COVERAGEFILE_EX	
	Usage: ./build/tester/covo OR Usage: ./build/tester/covo EXECUTABLE2 -v -T TARGET -f FORMAT -E EXPLANATIONS -s SYMBOLS_FILE -1 EXECUTABLE	<pre>aar/covoar [-v] -T Ti paar/covoar [-v] -T Ti - verbose at init: - target name - coverage file fi - name of file wii - name of file wii - name of file wii</pre>	ARGET -f FORMAT [-E EXPLA ialization ormat (RTEMS, QEMU, TSIM th explanations th symbols of interest ble to get symbols from	NATIONS] -e EXE_EXTENSION -c COVERAGEFILE_EX	
	Usage: ./build/tester/covd OR Usage: ./build/tester/covd EXECUTABLE2 -v -T TARGET -f FORMAT -E EXPLANATIONS -s SYMBOLS FILE -1 EXECUTABLE -e EXE_EXTENSION	<pre>aar/covoar [-v] -T Ti paar/covoar [-v] -T Ti - verbose at init: - target name - coverage file fo - name of file wi - name of file wi - name of executal - extension of th</pre>	ARGET -f FORMAT [-E EXPLA ialization ormat (RTEMS, QEMU, TSIM th explanations th symbols of interest	NATIONS] -e EXE_EXTENSION -c COVERAGEFILE_EX	
	Usage: ./build/tester/covo OR Usage: ./build/tester/covo EXECUTABLE2 V T TARGET f FORMAT E EXPLANATIONS S SYMPOLS_FILE 1 EXECUTABLE e EXE_EXTENSION c COVERAGEFILE_EXTENSIO g GCNOS_LIST	- verbose at init: - verbose at init: - target name - coverage file fi - name of file wii - name of file wii - name of executal - extension of the - name of file wii	ARGET -f FORMAT [-E EXPLA ialization ormat (RTEMS, QEMU, TSIM th explanations th symbols of interest ble to get symbols from e executables to analyze e coverage files to analy th list of *.gcno files	NATIONS] -e EXE_EXTENSION -c COVERAGEFILE_EX	
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72/130

#### R

Descripti on	tools/plain/tools/4.12/gdb/gdb-7.11-si 0b8b2a23c7d1592315fe0130188f457c	s-leon2-leon3.diff -> p 80f8b1e26645535bed0	atches/gdb-7.11-sis-leon2-leon3.d 91a5e0671682dc44a1987d00e693	.diff -> patches/gdb-7.11-sis-leon2-leon3.diff download: https iff checksums: gdb-7.11-sis-leon2-leon3.diff: 99a1b1c562c7579404db43183e666c29c2b479446aa61ca4f6 = error: gdb-7.11-sis-leon2-leon3.diff error: checksum failure fi	>
	sis-leon2-leon3.diff				
<del>#3203</del>	3 years ago fixed	admin	Amar Takhar	Amar Takhar	19 months ago
ummar y	Upgrade trac to fix numerous problem	5.			
	There are a ton of issues going on with	n trac that need to be re	esolved. The two major ones are:		
escripti on	<ul> <li>The ticket commenter emails pr</li> <li>The always_email setting is tak</li> <li>The Git plug-in consistently spin</li> <li>Frequent, strange and random         <ul> <li>This one is not a huge d</li> </ul> </li> </ul>	ing things too literally and ns, floods the jail with p crashes.	and always sending emails even if i	t shouldn't.	
	Upgrading trac is a weeklong project u	sually I will start prepa	ring for it and update here.		
	If anyone has any feature requests no	w is the time to do it!			
<del>#3204</del>	3 years ago worksforme	tool	Joel Sherrill	Chris Johns	5 months ago
ummar y	Exception in rtems-test				
У	Note: No category for rtems-tools.				
	My first attempt to send run logs didn'	t qo so well. This was a	weird failure mode. It didn't exit b	out had to be killed by hand. I dropped off the options related t	o mailing the log and
	still failed. This is on an up to date Cer				5
Descripti on	<pre>mail-from=joel@rtems.orgma rtems4.12//erc32/testsuites/ rtems4.12//erc32/testsuites/ rtems4.12//erc32/testsuites/ rtems4.12//erc32/testsuites/ rtems4.12//erc32/testsuites/ RTEMS Testing - Tester, 4.12 [ 5/11] p:0 f:0 u:0 e:0 I [ 6/11] p:0 f:0 u:0 e:0 I [ 7/11] p:0 f:0 u:0 e:0 I [ 1/11] p:0 f:0 u:0 e:0 I [ 2/11] p:0 f:0 u:0 e:0 I [ 8/11] p:0 f:0 u:0 e:0 I [ File "/data/home/joel/rtems     job_trace) File "/data/home/joel/rtems     exe = path.basename(self. File "/home/joel/rtems-work     return shell(os.path.base File "/usr/lib64/python2.7/</pre>	<pre>il-to=build@lists.rt samples/iniimum/mini samples/ileio/filei samples/inecs/nsecs. samples/cdtest/cdtes samples/cdtest/cdtes samples/cdtest/cdtes samples/cdtest/cdtes samples/unlimited/un (52513610668b) 10 B:0 t:0 i:0 10 B:0 t:0 10 B:0 10 B:</pre>	<pre>sems.org ./sparc-rtems4.12/c/e mum.exe ./sparc-rtems4.12/c/e io.exe ./sparc-rtems4.12/c/e io.exe ./sparc-rtems4.12/c/erc3 st.exe ./sparc-rtems4.12/c/erc3 st.exe ./sparc-rtems4.12/c/erc3 st.exe ./sparc-rtems4.12/c/erc3 limited.exe ./sparc-rtems4.12/ sparc/erc32: catest.exe sparc/erc32: catest.exe sparc/erc32: minimum.exe sparc/erc32: minimum.exe sparc/erc32: minimum.exe sparc/erc32: minimum.exe sparc/erc32: inlinited.exe sparc/erc32: inlinited.exe sparc/erc32: catest.exe sparc/erc32: inlinited.exe sparc/erc32: inlinited.exe sparc/erc32: inlete.exe sparc/erc32: nsecs.exe tr/trems-test", line 40, in <mc ester/rt/test.py", line 136, i ester/rt/test.py", line 189, i ester/rt/report.py", line 193, )) oolkit/path.py", line 77, in t 121, in basename</mc </pre>	odule> in run in report_finished , in log	-
	<pre>i = p.ffind('/') + 1 AttributeError: 'NoneType' ob</pre>	ject has no attribut	e find		
		ject has no attribut			
#3205		ject has no attribut	Sebastian Huber	Sebastian Huber	2 years ago
Summar	AttributeError: 'NoneType' ob	posix		Sebastian Huber	2 years ago
Summar y	AttributeError: 'NoneType' ob 3 years ago fixed Relative timespec timeouts are subject	posix to integer overflows	Sebastian Huber		2 years ago
Summar y	AttributeError: 'NoneType' ob 3 years ago fixed Relative timespec timeouts are subject	posix to integer overflows	Sebastian Huber	Sebastian Huber	2 years ago
Summar y Descripti on	AttributeError: 'NoneType' ob 3 years ago fixed Relative timespec timeouts are subject	posix to integer overflows	Sebastian Huber		2 years ago
Summar y Descripti	AttributeError: 'NoneType' ob 3 years ago fixed Relative timespec timeouts are subject As a best-effort approach, a very large	posix to integer overflows relative timeout should doc	Sebastian Huber d result in the maximum monotoni		
Summar y Descripti on #3207 Summar y	AttributeError: 'NoneType' ob         3 years ago       fixed         Relative timespec timeouts are subject         As a best-effort approach, a very large         3 years ago       fixed         Supported Architectures Page is out of	posix to integer overflows relative timeout should doc date	Sebastian Huber d result in the maximum monotonic Joel Sherrill	c watchdog value and not in an undefined integer overflow.	
Summar y Descripti on <b>#3207</b> Summar y Descripti	AttributeError: 'NoneType' ob         3 years ago       fixed         Relative timespec timeouts are subject         As a best-effort approach, a very large         3 years ago       fixed         Supported Architectures Page is out of         https://devel.rtems.org/wiki/TBR/User	posix to integer overflows relative timeout should doc date Manual/SupportedCPUs	Sebastian Huber d result in the maximum monotoni Joel Sherrill	c watchdog value and not in an undefined integer overflow.	
Summar y Descripti on #3207 Summar y Descripti on	AttributeError: 'NoneType' ob         3 years ago         fixed         Relative timespec timeouts are subject         As a best-effort approach, a very large         3 years ago         fixed         Supported Architectures Page is out of         https://devel.rtems.org/wiki/TBR/User         I don't know the best way to provide to	posix to integer overflows relative timeout should doc date Manual/SupportedCPUs his broad view from 4.6	Sebastian Huber d result in the maximum monotonic Joel Sherrill s is out of date. I have the informal s up on what architectures are supp	c watchdog value and not in an undefined integer overflow. tion to update it if that's what we want to do. ported. The wiki seems OK.	2 years ago
Summar y Descripti on <b>#3207</b> Summar y Descripti	AttributeError: 'NoneType' ob         3 years ago       fixed         Relative timespec timeouts are subject         As a best-effort approach, a very large         3 years ago       fixed         Supported Architectures Page is out of         https://devel.rtems.org/wiki/TBR/User	posix to integer overflows relative timeout should doc date Manual/SupportedCPUs	Sebastian Huber d result in the maximum monotoni Joel Sherrill	c watchdog value and not in an undefined integer overflow.	

#### RTEMS 5.1 Release Notes I was updating the md5's to sha512's on gemu and made a typo which resulted in this message:

Descripti	loading: vd get: requir	es ()	he build aborting . D	erhans this should be a fatal	7	
on		fragment was in qer		erhaps this should be a fatal err	Jr.	
	-%hash md5	0001-openrisc-ter	minate-qemu-proce	ess-upon-receiving-a-hal.pa	enrisc-terminate-qemu-process-upon-rece tch 6aa9dfc4522466ab4a463129b3b9cb1d 849522c59dcd05724270756763438aecdb70211	
<b>#3210</b> Summar	3 years ago Improve the RSB	fixed build email messag	tool/rsb	Chris Johns	Chris Johns	2 years ago
y Descripti on	The message nee	eds more detail to p	rovide a suitable arc	hive.		
<del>#3211</del>	, 3	fixed	posix	Sebastian Huber	Sebastian Huber	2 years ago
Summar y	Fix pthread_crea	te() with user provid	led stack			
Descripti on	In case the user	provides a stack wit	h address and size,	then do not alter the stack size.		
#3212 Summar	3 years ago Qemu Fails to Bu	worksforme ild, RSB Gives Odd	tool/rsb Traceback	Joel Sherrill	Chris Johns	5 months ago
У	After applying the	e attached patch to	update the md5's to	sha512's, something goes wro	ng in the RSB build of Qemu. There is nothing	obvious from the gemu build directory. Bu
	appears that the	cd into the qemu gi	t directory didn't wo	rk and it is acting on my RSB gi		· · ·
Descripti on	<pre>script: 82: source setu making dir: _url: git:/ work/rtems- cmd: (/data exe: ['/usr # On branch # Untracked # (use "g # #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./ #//./</pre>	<pre>p: qemu=42d58e7cf /data/home/joel/ /dit.qemu-project source-builder/bz /home/joel/rtems- /bin/git', 'statu am files: it add <file>' /am/ j_qemu nohup.out /gcc7/ /rtems/d.10-tau /rtems/do_a1 /rtems/do_a1 /rtems/do_a1 /rtems/do_a1 /rtems/do_a1 /rtems/b_rm /rtems/sh-gdb.cd dt o commit but /home/joel/rtems- /bin/git', 'clear</file></pre>	""gemu-42d58e7c67 5760cb9c55627c28a Trems-work/rtems- .org/gemu.git?pui re/sources/git/ge work/rtems-source is'] ' to include in wh rgets it liff untracked files p work/rtems-source i, '-f', '-d'] work/rtems-source	<pre>=source=builder/bare/source ll?checkout=42d58e7c6760cb9 emu.git e-builder/bare/sources/git/ hat will be committed) present (use "git add" to t e-builder/bare/sources/git/</pre>	x-gnu-1: source qemu -q -n qemu-42d58e7 s/git c55627c28ae538e27dcf2f144?submodule=dtc qemu.git) /usr/bin/git status	
	<pre>cmd: (/data exe: ['/usr HEAD is now cmd: (/data exe: ['/usr Switched to git: pull: cmd: (/data exe: ['/usr Build Set:</pre>	/bin/git', 'reset at 96485e3 Add 3 /home/joel/rtems- /bin/git', 'check branch 'master' git://git.qemu-pa	HA512 checksums i work/rtems-source cout', 'master'] coject.org/qemu.g: work/rtems-source ]	e-builder/bare/sources/git/ it	qemu.git) /usr/bin/git checkout master qemu.git) /usr/bin/git pull	
<b>#3213</b> Summar	<pre>cmd: (/dta exe: ['/usr HEAD is now cmd: (/data exe: ['/usr Switched to git: pull: cmd: (/data exe: ['/usr Build Set: abort: user 3 years ago</pre>	/bin/git', 'reset at 96485e3 Add 5 /home/joel/rtems- /bin/git', 'cheo' branch 'master' git://git.qemu-pp /home/joel/rtems- /bin/git', 'pull' Time 0:05:24.8108 terminated fixed	SHA512 checksums i work/rtems-source cout', 'master'] ooject.org/qemu.g: work/rtems-source ] 71 tool	e-builder/bare/sources/git/ it		5 months ago
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# RTEMS 5.1 Release Notes There must be a recent change to msys2 which is breaking things. I installed the 64-bit version from https://msys2.github.io/ per the instructions at https://docs.rtems.org/branches/master/user/hosts/index.html#microsoft-windows

	https://docs.rtems.org					
	error: no hosts	defaults found	d; please add			
	After adding some prin	nts, I learned this	5:			
	<pre>\$ ./source-build posix made it MSYS_NT-10.0 error: no hosts</pre>		d; please add			
	I filled in options.py an missing some package			MSYS2. I was then able to run	sb-check. But it wasn't happy. Apparently the	e pacman command in the User's Guide is
	\$ ./source-build					
cripti	posix MSYS_NT-10.0 RTEMS Source Bu error: exe: not error: exe: not Environment is s I installed binutils exp	found: (_ar) found: (_as) found: (_cc) found: (_cxx) found: (_ld) found: (_objc found: (_objc found: (_cran) not correctly s	ar as x86_64-w64-ming x86_64-w64-ming ld nm copy) objcopy dump) objdump lib) ranlib set up	gw32-gcc ngw32-g++	lid a find to locate the gcc's installed:	
	\$ find / -name	"*gcc.*"				
	/mingw64/lib/gcv /mingw64/share/ /mingw64/share/r /usr/share/vim/	c.exe 6_64-w64-mingw1 c/x86_64-w64-mi c/x86_64-w64-mi info/gcc.info.g man/man1/gcc.1 vim80/compiler/	32-gcc.exe ingw32/6.2.0/ind ingw32/6.2.0/lib gz .gz /gcc.vim	clude/stdint-gcc.h ogcc.a stem at or below \/': No su	ch file or directory	
	<pre>\$ /mingw64/bin/: x86_64-w64-mingr Copyright (C) 2 This is free so</pre>	x86_64-w64-ming w32-gcc.exe (Re 016 Free Softwa ftware; see the	gw32-gcc.exev ev2, Built by MS are Foundation, e source for cop	SYS2 project) 6.2.0		
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Sebastian Huber

2 years ago

https://devel.rtems.org/wiki/Release//5/5.1

fixed

score

Sebastian Huber

#3237 2 years ago

#### **RTEMS 5.1 Release Notes** Summar Fix priority ceiling updates

У

У						
escripti on				Extract_locked() since this fun ly important in SMP configurat	ction is used by the priority ceiling surrender operations a ions.	after the ceiling priority handover
				) invocation to the callers.		
3238	2 years ago	fixed	admin	Chris Johns	Amar Takhar	19 months ago
ımmar y	Git push to Trac with	n more than one o	commit does not upda	të tickets.		
scripti	The git push to trac	hook does not qu	eue or handle a numb	er of commits in a push. As a	result updates to tickets can be missed.	
on		1	:			
<del>3239</del>	2 years ago Add getentropy() im	fixed	dev	Sebastian Huber	Sebastian Huber	2 years ago
ımmar y	Add getentropy() in	plementation pro	VIDED Dy Each DSP			
scripti			oduced by OpenBSD a	and is now also available on gl	ibc 2.25 and later. It is used for example by arc4random	_buf(). Which in turn is used by
on	various cryptograph					
<del>3240</del>	2 years ago	fixed	lib addresses formatted	Andrei Chichak	Chris Johns	2 years ago
mmar y	CPURITY IDITISC/Stack		addresses formatted	incorrectly.		
	The function Stack_	check_Dump_thre	eads_usage displays th	ne stack high, low, and curren	t pointers incorrectly.	
scripti on	Instead of displaying	g these pointers in	n conventional hex for	mat, the values have a proper	prefix of 0x, but the pointer value is displayed in decima	al.
	The incorrect inttype	es.h formatting de	efine was used.			
<del>3242</del>	2 years ago	fixed	tool/gcc	Sebastian Huber	Sebastian Huber	2 years ago
nmar y	Workarounds for UT	699, 01700, and	GR/12RC errata			
	https://gcc.gnu.org/	ml/gcc-patches/2	2017-11/msg01751.ht	ml		
	This patch series ad	ds workarounds f	or the newly discovere	ed errata for UT699, UT700, ar	nd GR712RC. The errata and possible workarounds are de	escribed in the following documents
	available at http://w	ww.gaisler.com/i	ndex.php/information/	/app-tech-notes:		
scripti					ns GRLIB-TN-0011 - LEON3/FT AHB Lock Release during for GRLIB-TN-0013 - GRFPU Floating-point controller: Mis	
on			ting-point Operation t		or GREID-TN-0013 - GREPO Floating-point controller. Mis	Sing PDIV/PSQKT Result
	Daniel Cederman (4					
		or GRLIB-TN-001		ARCJ Errata workaround for G	RLIB-TN-0011 [SPARC] Errata workaround for GRLIB-TN-	0010 [SPARC] Errata
<del>3243</del>	2 years ago	fixed	score	Sebastian Huber	Sebastian Huber	19 months ago
mmar	Simplify global cons		50010	Sebustian Huber		15 months ugo
У						
		-	-		ad restart after global construction. With this implementa n thread). This is not in line with what users familiar with	-
scripti on	expect. See for exar					
	https://lists.rtems.o	rg/pipermail/user	s/2017-July/031525.h	itml		
<del>3244</del>	2 years ago	fixed	rtems	Sebastian Huber	Sebastian Huber	2 years ago
mmar y	Change rtems_panio	:() implementatio	n and document this f	unction		
Y	The current rtems_p	anic() implement	ation is quite heavy w	eight. It depends on _exit() w	hich calls the global destructors. It uses fprintf(stderr,	.) for output which depends on an
	initialized console de	evice and the com	plex fprintf().			
scripti on	Introduce a new fata	al source RTEMS_	FATAL_SOURCE_PANI	C for rtems_panic() and output	ıt via printk().	
011	Document this funct	ion in Fatal Mana	ger chapter.			
	Replace all BSP_pan	ic() with rtems_p	anic().			
<del>3245</del>	, 3	fixed	arch/powerpc	Sebastian Huber	Sebastian Huber	2 years ago
mmar y	Replace BSP_panic(	) with rtems_pani	c()			
scripti	Due to a new rtems	_panic() impleme	ntation, it is possible t	o replace the PowerPC-specifi	c BSP_panic() with rtems_panic(). Remove BSP_panic()	mplementations.
on			:			
<del>3246</del>	2 years ago	fixed	arch/powerpc	Sebastian Huber	Sebastian Huber	2 years ago
mmar y	Remove _BSP_Fatal	_error()				
scripti	BSPs can use the bs	p_fatal_extensior	n() to provide BSP-spe	cific fatal error handling. Ther	e is no need for a _BSP_Fatal_error().	
on						
<del>3247</del>	2 years ago	fixed	build	Sebastian Huber	Sebastian Huber	2 years ago
mmar y	Remove BSP-specifi	c defaults for RTE	MS_BSP_CLEANUP_OI	PIIONS()		
scripti	Remove BSP-specifi	c defaults for RTE	MS_BSP_CLEANUP_OI	PTIONS() to simplify the BSP (	configuration and documentation. Change default to:	
on	BSP_PRESS KEY FO	R_RESET=0 BSP	_RESET_BOARD AT F	EXIT=1 BSP_PRINT_EXCEPTIC	- N_CONTEXT=1	
<del>3248</del>	2 years ago	fixed	build	Sebastian Huber	Sebastian Huber	2 years ago
nmar	Add BSP_VERBOSE_	FATAL_EXTENSIO	ON to RTEMS_BSP_CLE	EANUP_OPTIONS		
У		FATAL EVTENCY	NI to PTEMS BOD OU	FAMILE OPTIONS to optionally	print the RTEMS version, the fatal source and the fatal c	ode in the chared
scripti on	bsp_fatal_extension		UNITEND_BOP_CT	בהויטר_טר ונטועס נס optionally	print the KILING VERSION, the latal SOURCe and the fatal C	Sue ill ule sidieu
1 <del>249</del>	2 years ago	fixed	arch/arm	Joel Sherrill	Sebastian Huber	2 years ago
nmar	imx7 does not link g	i				
У						00
					numb -mfpu=neon -mfloat-abi=hard -mtune=cortex-a7 - -Wnested-externs -Wl,gc-sections -march=armv7-a -m	
escripti	abi=hard -mtune=c	ortex-a7 -WI,wr	ap=printf -WI,wrap=	puts -WI,wrap=putchar -o g	etentropy01.exe init.o init.o: In function `test_getentrop	oy': /home/joel/rtems-work/rtems-
on				estsuites/libtests/getentropy0: sting/rtems/build-arm-imx7-rt	L//////rtems/c/src///testsuites/libtests/get ems/arm-	entropy01/init.c:57: undefined
	rtems5/c/imx7/tests	uites/libtests/get	entropy01/////	///rtems/c/src///testsuit	es/libtests/getentropy01/init.c:59: undefined reference t	
<del>3254</del>	2 years ago	fixed	build	Sebastian Huber		17 months ago
nmar y	Reorganize header f	nes to avoid "mal	e premstall			
	2 years ago	duplicate	arch/powerpc	Joel Sherrill	Sebastian Huber	2 years ago

<del>#325</del> Warnings on 64-bit targets Summar У

<ul> <li>Are un-many metes associed for self-contenting POSC synchronization objects</li> <li>Vester Step and Post Area and Post Area</li></ul>	escripti on	pointer-cast] log/po [-Wint-to-pointer-ca of type 'unsigned in	owerpc-qoriq_e ast] log/powerp it', but argumer	5500_64.log:/// c-qoriq_e6500_64.log nt 5 has type 'long int	///rtems/c/src///cpukit/libr g://///rtems/c/src/// ' [-Wformat=] log/powerpc-qoriq	<pre>/rtems-fdt-shell.c:57:27: warning: cast to pointer f nisc/rtems-fdt/rtems-fdt-shell.c:64:27: warning: ca cpukit/libmisc/rtems-fdt/rtems-fdt-shell.c:488:11: v _e6500_64.log:./././././.tems/c/src/././cpuk upt 2 has tyne 'long int' [=\//crmat_]</pre>	st to pointer from integer of different : warning: format '%u' expects argumer
Image: A solution notes appet for effe carbona PDER spacements and poet         Image: A poet first one hand notes in particular solution effects.           Image: A poet first one hand notes in particular solution effects.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one hand notes in particular solution.         Image: A poet first one particular solution.         Image: A poet	<del>3256</del>						2 years ago
Biget hypes are hear could in good and in good an	ummar		s support for se		nchronization objects		
Second ago         Noned         and stylepart         Sebastion Huber	escripti	Object types are ha	rd coded in gcc	/ada/s-osinte-rtems.a	ads.		
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<pre>In file included from /opt/work/chris/tems.git/cpukt/include/pci.hi23;, from /opt/work/chris/tems/kernel/tems.git/cpukt/include/pci.hi23;, opt/work/chris/tems/kernel/tems.git/cpukt/include/pci/access.hi6:10; fatal error; libcpu/byteorder.h: No such file or directory #include filebpu/byteorder.h&gt; //bsps/parcor/include/libcpu/byteorder.h //bsps/parcor/include/libcpu/byteorder.h //bsps/parcor/include/libcpu/byteorder.h //bsps/parcor/include/libcpu/byteorder.h //bsps/parcor/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //bsps/jasc/include/libcpu/byteorder.h //pt/work/chris/tems/kernel/rtems.git/cpukt/include/rtems/status-checks.h:16:3; note: in expansion of macro 'RTEMS_SYSLOG_ERROR' RTEMS_SYSLOG_ERROR("Sc = 1%; % Nn", (int) sc, msg); //opt/work/chris/tems/kernel/rtems.git/cpukt/include/rtems/status-checks.h:113:3; note: in expansion of macro 'RTEMS_SYSLOG_ERROR' RTEMS_SYSLOG_ERROR("Sc = 1%; % Nn", (int) sc, msg); //opt/work/chris/tems/kernel/rtems.git/cjub/jibjjibjjijjjijjjijjjijjjijjjijjjijjji</pre>		<pre>sparc-rtems5- I/opt/work/ch -g -ffunction /opt/work/chr</pre>	gccpipe -E ris/rtems/ker -sections -fd is/rtems/kern	HAVE_CONFIG_H -I nel/rtems.git/cpuk lata-sections -MT p el/rtems.git/c/src	<pre>I/opt/work/chris/rtems/ it/include -I/opt/work/chris oci_access.o -MD -MP -MF \$dep</pre>	kernel/bsps/beagleboneblack/sparc-rtems5/c/e s/rtems/kernel/rtems.git/cpukit/score/cpu/sp obase.Tpo -c -o pci_access.o	
\$ findname byteorder.h ./bsps/space/include/libcpu/byteorder.h ./bsps/space/include/libcpu/byteorder.h ./bsps/space/include/libcpu/byteorder.h       ./bsps/space/include/libcpu/byteorder.h         ./bsps/space/include/libcpu/byteorder.h       ./bsps/space/include/libcpu/byteorder.h       2 year         ummar       rtems/status-checks.h calls printk without including the needed header.       2 year         y       /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:74:7: warning: implicit declaration of function 'printk'; did :		In file inclu /opt/work/chr #include <li< td=""><td>ded from /opt from /opt is/rtems/kern bcpu/byteorde</td><td>/work/chris/rtems/ /work/chris/rtems/ el/rtems.git/cpuki er.h&gt;</td><td>kernel/rtems.git/c/src//.</td><td>./cpukit/libpci/pci_access.c:10:</td><td>file or directory</td></li<>	ded from /opt from /opt is/rtems/kern bcpu/byteorde	/work/chris/rtems/ /work/chris/rtems/ el/rtems.git/cpuki er.h>	kernel/rtems.git/c/src//.	./cpukit/libpci/pci_access.c:10:	file or directory
<pre>./bsps/powerpc/include/libcpu/byteorder.h ./bsps/jack/include/libcpu/byteorder.h ./bsps/1366/include/libcpu/byteorder.h ./bsps/1366/include/libcpu/byteorder.h ./bsps/1366/include/libcpu/byteorder.h ./bsps/1366/include/libcpu/byteorder.h ./bsps/1366/include/libcpu/byteorder.h ./bsps/ise/include/libcpu/byteorder.h ./bsps/ise/include/libcpu/byteorder.h ./bsps/ise/include/libcpu/byteorder.h ./bsps/ise/include/libcpu/byteorder.h ./bsps/ise/ise/include/libcpu/byteorder.h ./bsps/ise/ise/ise/ise/ise/ise/ise/ise/ise/is</pre>		This header is found	d under:				
<pre>rtems/status-checks.h calls printk without including the needed header. y  /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:74:7: warning: implicit declaration of function 'printk'; did 'printf'? [-Wimplicit-function-declaration]     printk(fmt, ##_VA_ARGS_)     /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:86:3: note: in expansion of macro 'RTEMS_SYSLOG_PRINT'     RTEMS_SYSLOG_PRINT( "%s: " fmt, _func_, ##_VA_ARGS_)     /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:107:3: note: in expansion of macro 'RTEMS_SYSLOG'     RTEMS_SYSLOG ("Srror: " fmt, ##_VA_ARGS_)     /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:107:3: note: in expansion of macro 'RTEMS_SYSLOG'     RTEMS_SYSLOG [RROR( "SC = %i: %s\n", (int) sc, msg);     /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:113:3: note: in expansion of macro 'RTEMS_SYSLOG_ERROR'     RTEMS_SYSLOG_ERROR( "SC = %i: %s\n", (int) sc, msg);     /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:152:5: note: in expansion of macro 'RTEMS_SYSLOG_ERROR_WITH_SC     RTEMS_SYSLOG_ERROR_WITH_SC( sc, msg); \     /ort/work/chris/rtems/kernel/rtems.git/c/src/lib/libbsp/lm32/milkymist///lm32/shared/milkymist_gpio/gpio.c:57:5: note: in expansion of     RTEMS_CHECK_SC'     RTEMS_CHECK_SC'(sc, "create GPIO device"); </pre>		./bsps/powerp ./bsps/sparc/	c/include/lib include/libcp	cpu/byteorder.h wu/byteorder.h			
<pre>y /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:74:7: warning: implicit declaration of function 'printk'; did 'printf'? [-Wimplicit-function-declaration]     printk(fmt, ##_VA_ARGS_) /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:86:3: note: in expansion of macro 'RTEMS_SYSLOG_PRINT'     RTEMS_SYSLOG_PRINT( "%s: " fmt, _func_, ##_VA_ARGS_) /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:107:3: note: in expansion of macro 'RTEMS_SYSLOG'     RTEMS_SYSLOG( "Error: " fmt, ##_VA_ARGS_) /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:107:3: note: in expansion of macro 'RTEMS_SYSLOG'     RTEMS_SYSLOG( "Error: " fmt, ##_VA_ARGS_) /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:107:3: note: in expansion of macro 'RTEMS_SYSLOG'     RTEMS_SYSLOG_ERROR( "SC = %i: %s\n", (int) sc, msg); /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:152:5: note: in expansion of macro 'RTEMS_SYSLOG_ERROR_WITH_SC     RTEMS_SYSLOG_ERROR_WITH_SC( sc, msg); \     ^</pre>	<del>3267</del>	2 years ago	fixed	lib	Chris Johns		2 years ago
<pre>/opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:74:7: warning: implicit declaration of function 'printk'; did y 'printf'? [-Wimplicit-function-declaration] printk(fmt, ##_VA_ARGS_) ^ /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:86:3: note: in expansion of macro 'RTEMS_SYSLOG_PRINT' RTEMS_SYSLOG_PRINT( "%s: " fmt, _func_, ##_VA_ARGS_) ^ /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:107:3: note: in expansion of macro 'RTEMS_SYSLOG' RTEMS_SYSLOG ("Error: " fmt, ##_VA_ARGS_) ^ /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:113:3: note: in expansion of macro 'RTEMS_SYSLOG' RTEMS_SYSLOG_ERROR( "SC = %i: %s\n", (int) sc, msg); ^ /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:152:5: note: in expansion of macro 'RTEMS_SYSLOG_ERROR_WITH_SC RTEMS_SYSLOG_ERROR_WITH_SC( sc, msg); \ ^ / rtems_SYSLOG_ERROR_WITH_SC( sc, msg); \ ^ / rtems_check_SC' RTEMS_CHECK_SC(sc, "create GPI0 device");</pre>		rtems/status-check	s.h calls printk	without including the	needed header.		
<pre>^</pre>		'printf'? [-W	implicit-func	tion-declaration]	t/include/rtems/status-chec	<pre>ks.h:74:7: warning: implicit declaration of</pre>	function 'printk'; did you mean
<pre>^^</pre>		^ /opt/work/chr	is/rtems/kern	 el/rtems.git/cpuki		ks.h:86:3: note: in expansion of macro 'RTEM	S_SYSLOG_PRINT'
<pre>escripti on /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:113:3: note: in expansion of macro 'RTEMS_SYSLOG_ERROR' RTEMS_SYSLOG_ERROR("SC = %i: %s\n", (int) sc, msg); /opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:152:5: note: in expansion of macro 'RTEMS_SYSLOG_ERROR_WITH_SC RTEMS_SYSLOG_ERROR_WITH_SC( sc, msg); \ /opt/work/chris/rtems/kernel/rtems.git/c/src/lib/libbsp/lm32/milkymist///lm32/shared/milkymist_gpio/gpio.c:57:5: note: in expansion of RTEMS_CHECK_SC(sc, "create GPIO device");</pre>		^~~~~~ /opt/work/chr	is/rtems/kern	el/rtems.git/cpuki	t/include/rtems/status-chec	<pre><s.h:107:3: 'rte<="" expansion="" in="" macro="" note:="" of="" pre=""></s.h:107:3:></pre>	MS_SYSLOG'
<pre>/opt/work/chris/rtems/kernel/rtems.git/cpukit/include/rtems/status-checks.h:152:5: note: in expansion of macro 'RTEMS_SYSLOG_ERROR_WITH_SC RTEMS_SYSLOG_ERROR_WITH_SC( sc, msg); \</pre>		/opt/work/chr	~~ is/rtems/kern	 el/rtems.git/cpuki		<pre>xs.h:113:3: note: in expansion of macro 'RTE</pre>	MS_SYSLOG_ERROR'
<pre>/opt/work/chris/rtems/kernel/rtems.git/c/src/lib/libbsp/lm32/milkymist///lm32/shared/milkymist_gpio/gpio.c:57:5: note: in expansion of 'RTEMS_CHECK_SC' RTEMS_CHECK_SC(sc, "create GPIO device");</pre>		/opt/work/chr RTEMS_SY	is/rtems/kern SLOG_ERROR_WI	el/rtems.git/cpuki TH_SC( sc, msg); \	t/include/rtems/status-chec	ks.h:152:5: note: in expansion of macro 'RTE	MS_SYSLOG_ERROR_WITH_SC'
^~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~		/opt/work/chr 'RTEMS_CHECK_ RTEMS_CH	is/rtems/kern SC' ECK_SC(sc, "c	el/rtems.git/c/src		//lm32/shared/milkymist_gpio/gpio.c:57:5	: note: in expansion of macro
3268     2 years ago     fixed     arch/powerpc     Chris Johns     2 year							

	The PowerPC BSP f	amily neaders need	d some refactoring for	r the RTEMS 5 release. The BS	P family relies on the preinstall process to get suitable head	ers installed to work and
Descripti on					ng the no-preinstall branch. There is a PowerPC BSP fame $p/irq.h>$ and the header used depends on the include order	
	is fragile for any us	er. These headers	needs to be moved to	BSP specific paths, for examp	<pre>ble #include <mvme3100 irq.h="">.</mvme3100></pre>	
<del>#3270</del>	2 years ago	fixed	arch/powerpc	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Remove unused su	pport for MPC505				
y Descripti	There is some supp	ort for MPC505 in	libcpu, however, I car	nnot find a BSP for this code. R	Remove this apparently dead code.	
on	2	<b>C</b> 1				2
<del>#3277</del>	2 years ago QorIQ: Add MAC-le	fixed	network/libbsd	Sebastian Huber	Sebastian Huber	2 years ago
Summar y	QUITQ. AUGUACE	33 DI AA di wei to i	10030			
Descripti	The SDK Linux DPA	A driver supports a	a so called MAC-less i	nterface driver. This driver allo	ows Ethernet communication between guest systems of a hy	pervisor.
on	-					-
<del>#3278</del>	2 years ago bsp-builder has inc	fixed	tool	Joel Sherrill	Chris Johns	2 years ago
Summar y	bsp-builder has hie	orrect print ( 905 m	output)			
	I don't think the to	ols have branches	so only impacts mast	er.		
Docorinti	Notice the "run: %	s:"				
Descripti on	[1114/1565] powe	rpc/mpc5674fevb (	profiling) Configuring	run: %s: powerpc/mpc5674fe	evb.profiling\	
	/home/joel/	rtems-work/rtems/	configuretarget=p	owerpc-rtems5\enable-rtem	sbsp=mpc5674fevbprefix=/home/joel/rtems-work/bsps\	enable-profiling
<del>#3281</del>	2 years ago	wontfix	tool/gdb	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Add epiphany supp		cool, gub	Sebastian nabel		2 years ago
у						
<del>#3283</del>	2 years ago	fixed	doc	Joel Sherrill	Chris Johns	2 years ago
Summar	Bad URL in OpenO	CD/Xilinx_Zynq Wik	ki Page			
У	https://devel.rtem	s.org/wiki/Debuani	ng/OpenOCD/Xilinx 7	Zyng has a link to the Zedboard	d Processor Debug Adapter. I think the URL has changed to	this but would like someone
Descripti			before it is changed.	, ,		
on	http://zedboard.or	g/accessories/zedb	oard-processor-debug	g-adapter		
<del>#3284</del>	2 years ago	fixed	tool/rsb	Sebastian Huber	Sebastian Huber <sebastian.huber@></sebastian.huber@>	2 years ago
Summar v	RSB uses hard code	ea GCC binary path	IS			
у	In order to build a g++ programs:	tool chain with Ada	support you need a	native GCC with Ada support o	f the same version as the cross compiler. The RSB uses hard	d coded paths for the gcc an
Descripti	source-builde	er/defaults.mc:	_cc:	exe, required, '/usr		
on	source-builde	er/defaults.mc:	_cxx:	exe, required, '/usr	/bin/g++'	
	So, the RSB user m	nust change the ma	ain GCC installation of	the machine to build a particu	des DTEMC tool shain. This is underived (infersible in most si	tuationa
#220-				i ule machine to build a particu	Jar RTEMS tool chain. This is undesired/infeasible in most si	tudtions.
<del>#3285</del>	2 years ago	fixed	build	Sebastian Huber	Sebastian Huber	
Summar	2 years ago Reorganize BSP so					
	Reorganize BSP so	urce directory				21 months ag
Summar	Reorganize BSP so Now, that all BSP h	eader files are in				
Summar	Reorganize BSP so Now, that all BSP h • bsps/include	eader files are in				
Summar	Reorganize BSP so Now, that all BSP h • bsps/include • bsps/@RTEI	eader files are in 45_CPU@/include		Sebastian Huber		
Summar	Reorganize BSP so Now, that all BSP h bsps/include bsps/@RTEI bsps/@RTEI	urce directory eader files are in 4 4S_CPU@/include 4S_CPU@/@RTEMS	build	Sebastian Huber		21 months ag
Summar	Reorganize BSP so Now, that all BSP h bsps/include bsps/@RTEI bsps/@RTEI we should also mov include (this	urce directory eader files are in 4 4S_CPU@/include 4S_CPU@/@RTEMS	build 5_BSP_FAMILY@/incluto this new directory	Sebastian Huber	Sebastian Huber	21 months ag
Summar	Reorganize BSP so Now, that all BSP h bsps/include bsps/@RTEI bsps/@RTEI we should also mo	urce directory eader files are in 45_CPU@/include 45_CPU@/@RTEMS ve the BSP sources is already there, s	build 5_BSP_FAMILY@/incluto this new directory	Sebastian Huber	Sebastian Huber	21 months ag
Summar	Reorganize BSP so Now, that all BSP h bsps/include bsps/@RTEI bsps/@RTEI we should also mo include (this config s start (every	urce directory eader files are in 45_CPU@/include 45_CPU@/@RTEMS /e the BSP sources : is already there, s ebsp.cfg thing required to ru	build 5_BSP_FAMILY@/incluto this new directory	Sebastian Huber	Sebastian Huber	21 months ag
Summar	Reorganize BSP so Now, that all BSP h bsps/include bsps/@RTEI bsps/@RTEI we should also mov include (this config o some	urce directory eader files are in 45_CPU@/include 45_CPU@/@RTEMS ve the BSP sources is already there, s ebsp.cfg thing required to ru .S	build 5–BSP_FAMILY@/inclu to this new directory see <del>#3254</del> )	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti	Reorganize BSP so Now, that all BSP h bsps/include bsps/@RTEI bsps/@RTEI we should also mo include (this config some start (every start bsps obsps	urce directory eader files are in MS_CPU@/include MS_CPU@/@RTEMS we the BSP sources is already there, s ebsp.cfg thing required to ru .S cart.c mp.c	build 5–BSP_FAMILY@/inclu to this new directory see <del>#3254</del> )	Sebastian Huber	Sebastian Huber	21 months ag
Summar y	Reorganize BSP so Now, that all BSP h bsps/@RTEI bsps/@RTEI we should also mov include (this config o some start (every o start o bsps bsps o linkci	urce directory eader files are in 45_CPU@/include 45_CPU@/@RTEMS we the BSP sources is already there, s ebsp.cfg thing required to ru .S tart.c mp.c mds	build 5–BSP_FAMILY@/inclu to this new directory see <del>#3254</del> )	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti	Reorganize BSP so Now, that all BSP h bsps/@RTEI bsps/@RTEI we should also mov include (this config start (every start bsps) bsps bsps cache (ever irq (everyth	eader files are in eader files are in 4S_CPU@/include 4S_CPU@/@RTEMS ve the BSP sources is already there, s ebsp.cfg thing required to ru .S cart.c mp.c mds ything for the cachd ing for the interrup	build 5_BSP_FAMILY@/inclu to this new directory see #3254) un a minimal application e controller support) t controller support)	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti	Reorganize BSP so Now, that all BSP h bsps/include bsps/@RTEI bsps/@RTEI we should also mov include (this config som start (every start (every start bsps blinkc cache (ever irq (everyth console (evv	eader files are in 4S_CPU@/include 4S_CPU@/@RTEMS ve the BSP sources is already there, s ebsp.cfg thing required to ru S cart.c mp.c mds vthing for the cache	build 5_BSP_FAMILY@/inclu to this new directory iee #3254) an a minimal application t controller support) t controller support) usole driver)	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti	Reorganize BSP so Now, that all BSP h bsps/@RTEI bsps/@RTEI we should also mov include (this config start (every start bsps bsps bsps bsps bsps cache (ever irq (everyth console (ever izq (everyth console (ever izc (everyth console (ever	eader files are in 45_CPU@/include 45_CPU@/@RTEMS ve the BSP sources is already there, s ebsp.cfg thing required to ru .S cart.c mp.c mds ything for the cach- ing for the interrup arything for the clock ing for the clock ing for the 12C driv	build 5_BSP_FAMILY@/inclut to this new directory wee #3254) an a minimal application t controller support) t controller support) usole driver) driver ter)	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti	Reorganize BSP so Now, that all BSP h bsps/@RTEI bsps/@RTEI bsps/@RTEI we should also mov include (this config o some start (every o start bsps o linkc cache (ever irq (everyth console (evv i2c (everyth o ster (everyth console (everyth o spi (everyth o spi (everyth o net (legacy	arce directory eader files are in 45_CPU@/include 45_CPU@/@RTEMS we the BSP sources is already there, s besp.cfg thing required to ru. S tart.c mp.c mds ything for the cacht ing for the interrup arything for the cacht ing for the clock ing for the 212 driv ing for the Stack drive network stack drive	build 5_BSP_FAMILY@/inclut to this new directory see #3254) an a minimal application t controller support) t controller support) t controller support) sole driver) driver ver) sers)	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti	Reorganize BSP so Now, that all BSP h bsps/@RTEI bsps/@RTEI we should also mov include (this config start (every start (every start (every start console (ever irq (everyth console (ever iz2 (everyth spi (everyth spi (everyth net (legacy mpci (RTEM	eader files are in 45_CPU@/include 45_CPU@/@RTEMS ve the BSP sources is already there, s ebsp.cfg thing required to ru .5 cart.c mp.c mds ything for the cach ing for the interrup arything for the cock ing for the lock ing for the SPL driv ing for the SPL driv network stack drive 5_MULTIPROCESSI	build 5_BSP_FAMILY@/inclut to this new directory to this new directory ee #3254) un a minimal application t controller support) t controller support) un a minimal application t controller support) t controller support) (NG support)	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti	Reorganize BSP so Now, that all BSP h bsps/include bsps/@RTEI bsps/@RTEI we should also mo include (this config som start (every start bsps bsps bsps linkcu cache (ever irq (everyth console (ever i2c (everyth spi (everyth net (legacy mpci (RTEM rtc (everyth ata (everyth ata (everyth	arce directory eader files are in MS_CPU@/include AS_CPU@/@RTEMS we the BSP sources is already there, s ebsp.cfg thing required to ru. S cart.c mp.c mds typing for the cacht ing for the interrup erything for the cacht ing for the 12C driv ing for the 12C driv ing for the SPI driv network stack drive S_MULTIPROCESI ing for the RTC driv ing for the RTC driv ing for the RTC driv ing for the ATA driv	build 5_BSP_FAMILY@/inclu to this new directory ice #3254) in a minimal application t controller support) t controller support) driver ter) ers) Support) /er) ver)	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti	Reorganize BSP so Now, that all BSP h bsps/@RTEI bsps/@RTEI we should also mov include (this config start (every start (every start (every clock (every i2c (everyth spi (everyth spi (everyth net (legacy mpci (RTEM rtc (everyth ata (everyth contrib (impc	eader files are in 4S_CPU@/include 4S_CPU@/@RTEMS ve the BSP sources is already there, s ebsp.cfg thing required to ru. S comp.c mds ything for the cach- ing for the interrup prything for the cach- ing for the clock- ing for the SIL driv S_MULTIPROCESSI ing for the RTC driv- ing for the ATA driv ort of external sou	build build S_BSP_FAMILY@/inclut to this new directory wee #3254) un a minimal application e controller support) t controller support) t controller support) driver er) driver ers) NG support) ver) ver) ver) reces)	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti	Reorganize BSP so Now, that all BSP h bsps/@RTEI bsps/@RTEI we should also mov include (this config start (every start (every start (every clock (every i2c (everyth spi (everyth spi (everyth net (legacy mpci (RTEM rtc (everyth ata (everyth contrib (impc	eader files are in 4S_CPU@/include 4S_CPU@/@RTEMS ve the BSP sources is already there, s ebsp.cfg thing required to ru. S comp.c mds ything for the cach- ing for the interrup prything for the cach- ing for the clock- ing for the SIL driv S_MULTIPROCESSI ing for the RTC driv- ing for the ATA driv ort of external sou	build 5_BSP_FAMILY@/inclu to this new directory ice #3254) in a minimal application t controller support) t controller support) driver ter) ers) Support) /er) ver)	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti on	Reorganize BSP so Now, that all BSP h bsps/@RTEI bsps/@RTEI bsps/@RTEI we should also mo include (this config start (every start bsps bsps blinkc cache (ever iq (everyth console (ever clock (every i2c (everyth net (legacy mpci (RTEM tata (everyth ata (everyth ata (everyth tata (everyth)) tata (everyth tata (everyth))	eader files are in eader files are in S_CPU@/include dS_CPU@/@RTEMS ve the BSP sources is already there, s ebsp.cfg thing required to ru s ebsp.cfg thing required to ru s estr.c mp.c mp.c mds ything for the cacht ing for the interrup erything for the cacht ing for the interrup erything for the cacht ing for the te cacht ing for the sPI driv ing for the acht ing for the cacht ing for the cacht ing for the acht ing for the acht	build build S_BSP_FAMILY@/inclu to this new directory ice #3254) un a minimal application e controller support) t controller support) t controller support) driver ter) ers) sole driver) driver ter) ers) sole support) ver) ver) ver) ver) ver) ces) ources should be use arch/arm	Sebastian Huber	Sebastian Huber	21 months ag
Summar y Descripti on #3290 Summar	Reorganize BSP so Now, that all BSP h bsps/include bsps/@RTEI bsps/@RTEI we should also mov include (this config some start (every start bsps bsps blinkc cache (every izc (everyth spi (everyth spi (everyth spi (everyth ata (everyth ata (everyth ata (everyth ata (everyth to contrib (imp o The l	eader files are in eader files are in S_CPU@/include dS_CPU@/@RTEMS ve the BSP sources is already there, s ebsp.cfg thing required to ru s ebsp.cfg thing required to ru s estr.c mp.c mp.c mds ything for the cacht ing for the interrup erything for the cacht ing for the interrup erything for the cacht ing for the te cacht ing for the sPI driv ing for the acht ing for the cacht ing for the cacht ing for the acht ing for the acht	build build S_BSP_FAMILY@/inclu to this new directory ice #3254) un a minimal application e controller support) t controller support) t controller support) driver ter) ers) sole driver) driver ter) ers) sole support) ver) ver) ver) ver) ver) ces) ources should be use arch/arm	d as is if possible.	Sebastian Huber	21 months ag
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## 79/130

escripti) on	····· • • • • • • • • •		ly have paravirtualization					
<del>#3307</del>	2 years ago	fixed	arch/powerpc	Joel Sherrill	Joel Sherrill	20 months ago		
Summar y	PowerPC linkcmd	s.base missing wi	ldcards on some sections	5				
Descripti on	Some sections we	ere missing sectio	ns. Wildcards needed to	be added.				
<del>#3309</del>	2 years ago	fixed	doc	Chris Johns		19 months ago		
Summar Y								
Descripti on	The initial_mod	de cannot have t	he non-preempt flag or a	an interrupt level set or an RT	EMS_UNSATISFIED error is returned. This is not documented in	the directive.		
<del>#3312</del>	2 years ago	fixed	tool/rsb	Chris Johns	Chris Johns	2 years ago		
Summar y	RSB macro calls s	such as define fail	on unicode keys.					
	The define call	in macros.py ch	ecks for a str while the	e call can conve	rt a unicode string to str. Remove the check.			
	Remove the other	r places in macro	s.py a key str check	is made and see if they can	be improved.			
	The following has	been reported to	me:					
		der.run()		e 29, in <module></module>				
Descripti on	<pre>File "/ opts = @ File "/ version File "/ sources File "/ macros. File "/ raise T</pre>	source-builder/ options.load(sy source-builder/ .load_release_s source-builder/ .hash((hs[0], h source-builder/ define(_file, ' source-builder/ ypeError('bad k	<pre>sb/setbuilder.py", li s.argv, optargs) sb/options.py", line ettings(o.defaults) sb/version.py", line ash[0], hs[1]), macro sb/sources.py", line %s %s' % (args[0], ar sb/macros.py", line 4 ey type: %s' % (type 'type 'unicode'&gt;</pre>	ine 526, in run 668, in load 123, in load_release_set ps, setting_error) 105, in hash pgs[2])) 39, in define	ings			
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	timing sensitive tes	sts. Add a simple	task console drivel	r.		
	index:: CO	ONFIGURE_APPLIC	CATION_NEEDS_SIME	PLE_TASK_CONSOLE_DRIVER		
	CONFIGURE	E_APPLICATION_1	NEEDS_SIMPLE_TASP	CONSOLE_DRIVER:		
			S_SIMPLE_TASK_CON			
	CONSTANT: ``CONFIGU	JRE_APPLICATIO	N_NEEDS_SIMPLE_T#	ASK_CONSOLE_DRIVER``		
	DATA TYPE: Boolean :	feature macro.				
	RANGE:	or undefined.				
	DEFAULT VALUE	5:	do Fourl t			
	DESCRIPTION: ``CONFIGU		N_NEEDS_SIMPLE_T#	ASK_CONSOLE_DRIVER`` is define		
escripti	NOTES:			Simple Task Console Device Dra		
on		ile. This dev: file descript		nitialize the standard input,	output,	
	This dev:	ice driver read	ds via ``getchar}	c()``.		
	written : requested non-block	into the write d count, in cas king and may be	buffer is return se the write buff e called from int	buffer. The count of characted. It might be less than the fer is full. The write is corrupt context. A dedicated	ne	
	``rtems_p	putc()``. This	s task runs with	s the characters via the least important priority nd it is not configurable.	. The	
	Use ``fsy write bui		ENO)`` or ``fdata	async(STDOUT_FILENO)`` to dra	in the	
			is not used. The stop bits, parity	ere is no support to change de 7, etc.	evice	
	The * ``CONFI	IGURE_APPLICAT:	ION_NEEDS_CONSOLE	E_DRIVER``,		
	* ``CONF:	IGURE_APPLICAT:	ION_NEEDS_SIMPLE_	CONSOLE_DRIVER``, and		
	* ``CONF	IGURE APPLICAT:	ION NEEDS SIMPLE	TASK CONSOLE DRIVER``		
		_	are mutually excl			
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<b>+3323</b> ummar y	,	fixed can result in inv	alid caching in a bro	Chris Johns	Chris Johns	2 years ago
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	or changes happen	that do not cha	nge the length.		a target has no RTC or it is incorrect and files ar	e being copied without preserving the mt
	or changes happen	that do not chai r code do not u	nge the length. pdate a file's time.	nd this can cause subtle issues if a		e being copied without preserving the mt
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Behavior is as expected with a bad user name.

Try to login to Trac with a bad password:

Oops... Trac detected an internal error: ProgrammingError?: (1064, "You have an error in your SQL syntax; check the manual that corresponds to your MySQL server version for the right syntax to use near 'sid='joel.sherrill' AND authenticated=1 AND name='failed_logins_count at line 1") There was an internal error in Trac. It is recommended that you notify your local Trac administrator with the information needed to reproduce the issue. To that end, you could anonymous ProgrammingError?: (1064, "You have an error in your SQL server version for the right syntax to use near 'sid='joel.sherrill' AND authenticated=1 AND name='failed_logins_count at line 1") There was an internal error in Trac. It is recommended that you notify your local Trac administrator with the information needed to reproduce the issue. To that end, you could anonymous ProgrammingError?: (1064, "You have an error in your SQL server version for the right syntax to use near 'sid='joel.sherrill' AND authenticated=1 AND name='failed_logins_count at the torresponds to your MySQL server version for the right syntax to use near 'sid='joel.sherrill' AND authenticated=1 AND name='failed_logins_count at torresponds to your MySQL server version for the right syntax to use near 'sid='joel.sherrill' AND authenticated=1 AND name='failed_logins_count at torresponds to your MySQL server version for the right syntax to use near 'sid='joel.sherrill' AND authenticated=1 AND name='failed_logins_count at torresponds to your MySQL server version for the right syntax to use near 'sid='joel.sherrill' AND authenticated=1 AND name='failed_logins_count at torresponds to your MySQL server version for the right syntax to use near 'sid='joel.sherrill' AND authenticated=1 AND name='failed_logins_count at torresponds to your MySQL server version for the right syntax torresponds to your MySQL server version for the right syntax torresponds torresponds to your MySQL server version for the right syntax to use near 'sid='joel.sherrill' AND authenticated=1 AND name='failed_logins_cou line 1") ==== How to Reproduce ====

While doing a POST operation on /login, Trac issued an internal error.

(please provide additional details here)

#### Request parameters:

{u'__FORM_TOKEN': u'0dc25ae350c181046ceae015', u'password': u'XX', u'referer': u'https://devel.rtems.org/ticket/3328', 'user_locked': False, u'user_name': u'joel.sherrill'}

User agent: Mozilla/5.0 (X11; Linux x86_64) KHTML/4.14.8 (like Gecko) Konqueror/4.14 Fedora/4.14.8-6.e17_3

#### System Information

System information not available

#### Enabled Plugins

Plugin information not available

Interface Customization

Traceback (most	t recent cal	l last):			
			ine 620, in _dispatch_reques	t	
	.dispatch(re src/trac/tra		ine 220, in dispatch		
			est(req, chosen_handler)		
			ine 429, in _pre_process_req	uest	
			quest(req, chosen_handler) er=0.5_dev0=pv2_7_egg/acct_m	gr/api.py", line 478, in pre process reques	+
		henticated or \	er o.o.devo pyz.,.egg/deet_m	gr/api.py , line 4/0, in pie_piocess_leques	
			ne 491, ingetattr		
		[name](self)			
	src/trac/tra sion(self.en		ine 354, in _get_session		
			, line 243, in init		
	hname == 'an				
			ne 491, ingetattr		
		[name](self)	ine 172, in authenticate		
		or.authenticate(r			
			er-0.5.dev0-py2.7.egg/acct_m	gr/util.py", line 81, in wrap	
	c(self, *arg		er=0 5 dev0=pv2 7 egg/acct m	gr/web ui.py", line 395, in authenticate	
		ser, req.remote_a		gr/web_ur.py , time 555, in addiencedate	
				gr/guard.py", line 107, in failed_count	
		f.env, user, key,			
	trac/plugins ername, attr		er-U.5.devU-py2.7.egg/acct_m	gr/model.py", line 509, in set_user_attribu	te
			ne 128, in execute		
cursor.exe	cute(query,	params if params	is not None else [])		
		c/db/util.py", li			
			ercent(sql), args) ges/MySQLdb/cursors.py", lin	e 205. in execute	
		, exc, value)	ges/Mysglub/edisors.py , iin	e 200, in execute	
			ges/MySQLdb/connections.py",	line 36, in defaulterrorhandler	
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syntax to use i			TH Joar ogn ojnean, encon e		rver version for the right
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<pre>}} Create a The action that POST: /login TracGuide - The 2 years ago deadlock in _once() RTEMS threads gettir Investigating deeper, _once() uses a comm completed. After this Attached you can find 2 years ago</pre>	ticket. t triggered e Trac User fixed fixed ng locked up v , seems like th non semaphor , std::future a d a test applic fixed	the error was: and Administration posix when using certain c+ his happens if std::as e for all calls, thus th ilso uses pthread_ond ation that reproduces arch/powerpo	authenticated=1 AND name='f n Guide  Stavros Passas  ++ functionality. Issue happens for sync executes before std::future g the first function (async.get usually ce to execute, but because the lo the deadlock. C Joel Sherrill	Sebastian Huber or example when std::future is combined with std:: gets scheduled to run. Both of these create a pthrea () gets the lock, calls its "init" function (which block ck is already taken, it also blocks, casing a deadlock	async. s until the second function has k.
<pre>}} Create a The action that POST: /login TracGuide - The 2 years ago deadlock in _once() RTEMS threads gettir Investigating deeper, _once() uses a comm completed. After this Attached you can find 2 years ago</pre>	ticket. t triggered e Trac User fixed fixed ng locked up v , seems like th non semaphor , std::future a d a test applic fixed	the error was: and Administration posix when using certain c+ his happens if std::as e for all calls, thus th ilso uses pthread_ond ation that reproduces arch/powerpo	authenticated=1 AND name='f n Guide  Stavros Passas  ++ functionality. Issue happens for sync executes before std::future g the first function (async.get usually ce to execute, but because the lo the deadlock. C Joel Sherrill	Sebastian Huber or example when std::future is combined with std:: gets scheduled to run. Both of these create a pthrea () gets the lock, calls its "init" function (which block ck is already taken, it also blocks, casing a deadlock	async. s until the second function has k.

Descripti on	powerpc-rtems5-g rtems/powerpc-rte qrtems -L////	cc -mcpu=603e ems5/c/qemuppc //qemuppc/lib -	-Dppc603e -O2 -g -fno-k /lib/libbsp/powerpc/qem -L/home/joel/rtems-work	<pre>keep-inline-functions -mcpu: uppc -B/home/joel/rtems-w k/rtems-testing/rtems/rtems</pre>	603e -Dppc603e -B/home/joel/rtems-work/rtem prk/rtems-testing/rtems/rtems/c/src/lib/libbsp/pc /c/src/lib/libbsp/powerpc/shared/startup -WI,wi	werpc/qemuppc/startup/ -specs bsp_specs ap=printf -WI,wrap=puts -WI,
	[00000000ffc1978 [spglobalcon02.0	0,00000000ffc19 exe] Error 1 gm	9783] overlaps section .s nake[6]: Leaving direc	data LMA [00000000ffc1978	b/gcc/powerpc-rtems5/7.3.0////powerpc-rt 0,0000000ffc19807] collect2: error: Id returned tems-work/rtems-testing/rtems/build-pow pn02] Error 2	1 exit status gmake[6]: *
<del>#3340</del>	2 years ago	fixed	arch/powerpc	Joel Sherrill	Sebastian Huber	2 years ago
ummar	gen83xx warning f	for macros redefi	ned			
У					werpc/gen83xx/include/bsp/hwreg_vals.h:244:0 werpc/gen83xx/include/bsp/hwreg_vals.h:246:0	
	Looking at the cod	le, it is pretty cle	ar that the macros are re	edefined. Unfortunately one	of the three has a different value the second time	
				==== /* fpga BCSR register	*/ #define FPGA_START 0xF8000000 #define FP	GA_SIZE 0x8000 #define FPGA_END
	(FPGA_START+FP	GA_SIZE-1)				
escripti on	/*					
	<ul> <li>working val</li> </ul>	lues for various r	egisters, used in start/st	art.S */		
					_CONFIG_SIZE 0x01000000 /* fpga register 8 MB ine FPGA_FIFO_START 0xF9800000 #define FPG	
					A_SIZE (0x02000000) #define FPGA_END (FPGA_	
<del>3341</del>	2 years ago	fixed	arch/sparc64	Joel Sherrill	Gedare Bloom	2 years ago
ımmar	sparc64: Macro Re	edefined				
У		/h /i  /++		(		
escripti on					include/arch/stack.h:56:0: warning: "STACK_BIA	S" redefined
<del>3342</del>	This is defined in t 2 years ago	wo header files v fixed	posix	sure what the proper fix is. Joel Sherrill	Joel Sherrill	21 months ago
ummar	pthread_setschedp			Joer Brieffin		21 months ago
У						
	5		ird parameter. This requ	ires a change to newlib and	RTEMS. The correct prototype is:	
escripti	int pthread_setsch	edparam(				
on	pthread_t t	hread, int policy,	, const struct sched_para	am *param		
	)					
3343	2 years ago	fixed	posix	Joel Sherrill	Joel Sherrill	21 months ago
ımmar y	pthread_mutex_ge	etpriocelling() ha	s incorrect prototype			
escripti on	int pthread_mutex const pthre		strict mutex, int *prioceil	r. This requires a change to		
on			strict mutex, int *priocei		Joel Sherrill	2 years ago
on <b>3344</b> Immar	const pthre	ad_mutex_t *res	arch/m68k	ling	Joel Sherrill	2 years ago
on 3344	const pthre ) 2 years ago mcf5272/mcf5272	ad_mutex_t *res fixed .h Timer3 Duplic	arch/m68k ate Definition	ling Joel Sherrill	:	
on <b>3344</b> Immar	const pthre ) 2 years ago mcf5272/mcf5272 This .h file uses th	ad_mutex_t *res fixed .h Timer3 Duplic e same macro na	arch/m68k ate Definition ames for two blocks of IN	Joel Sherrill NT macros. My assumption g	iven that the second looks to be a different INT, i	
on <b>3344</b> Jimmar y	const pthre ) 2 years ago mcf5272/mcf5272 This .h file uses th a/bsps/m68k/iii	ad_mutex_t *res <b>fixed</b> .h Timer3 Duplic e same macro na nclude/mcf5272/	arch/m68k ate Definition ames for two blocks of IN mcf5272.h +++ b/bsps/	Joel Sherrill T macros. My assumption g (m68k/include/mcf5272/mcf	iven that the second looks to be a different INT, i 5272.h @@ -88,9 +88,9 @@	s that it should not be INT3 again but INT.
on <b>3344</b> Immar y	const pthree 2 years ago mcf5272/mcf5272 This .h file uses th a/bsps/m68k/iii #define MC	ad_mutex_t *res fixed .h Timer3 Duplic e same macro na nclude/mcf5272/ 2F5272_ICR1_INT	arch/m68k ate Definition ames for two blocks of IN mcf5272.h +++ b/bsps/ T3_PI (bit(23)) #define N	Ing Joel Sherrill NT macros. My assumption g (m68k/include/mcf5272/mcf MCF5272_ICR1_INT3_IPL(x)	iven that the second looks to be a different INT, i 5272.h @@ -88,9 +88,9 @@ ((x) << 20) #define MCF5272_ICR1_INT3_MASI	s that it should not be INT3 again but INT.
on <b>3344</b> Jimmar y	const pthre ) 2 years ago mcf5272/mcf5272 This .h file uses th a/bsps/m68k/ii #define MC -#define MCF5272	ad_mutex_t *res <b>fixed</b> .h Timer3 Duplic e same macro na nclude/mcf5272/ :F5272_ICR1_INT ICR1_INT3_PI	arch/m68k ate Definition ames for two blocks of IN 'mcf5272.h +++ b/bsps/ T3_PI (bit(23)) #define NCF52	Ing Joel Sherrill NT macros. My assumption g (m68k/include/mcf5272/mcf MCF5272_ICR1_INT3_IPL(x)	iven that the second looks to be a different INT, i 5272.h @@ -88,9 +88,9 @@ ((x) << 20) #define MCF5272_ICR1_INT3_MASI << 16) -#define MCF5272_ICR1_INT3_MASK ((7	s that it should not be INT3 again but INT.
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	In file included from /h	iome/joel/rtem	ns-work/rtems-testing,	/rtems/rtems/cpukit/include/rt	ems/confdefs.h:323:0,	
Descripti	from////	.///rtems/c/	/src///testsuites/sp	tests/sptimecounter02/init.c:2	68:	
on					ion 'IMFS_update_atime': /home/joel/rtems-work function 'gettimeofday' [-Wimplicit-function-declar	
	gettimeofday( 8		inits, inits.inits inits.inits	armig. Implicit acclaration of	unction gettimeorody [ wimplice function decidi	
<b>#3352</b> Summar	2 years ago f Warning in all lpc176x	fixed variants	arch/arm	Joel Sherrill	Sebastian Huber	2 years ago
У	bsps/arm/lpc176x/inclu	ude/bsp.h defi	nes OPERATION COUI	NT in an attempt to override th	e autoconf generated constant. This conflicts and	results in this warning:
	/home/joel/rtems-work	k/rtems-testing	g/rtems/rtems/bsps/a	rm/lpc176x/include/bsp.h:42:(	- ): warning: "OPERATION_COUNT" redefined	-
					the include file order is different between tests, yo	ou could get the BSP value or the autoco
	generated value based				· · · · · · · · · · · · · · · · · · ·	
escripti) on	This warning needs to	be fixed and a	safer mechanism for	a BSP to override OPERATION_	_COUNT defined.	
		o use BSP_OP	ERATION_COUNT and	add logic to one of the commo	on test .h files to undef OPERATION_COUNT and re	edefine it to BSP_OPERATION_COUNT if i
	is defined.					
				nf generated variable to OPERA SP_OPERATION_DEFAULT.	TION_COUNT_DEFAULT and rely on logic in a con	nmon test support .h to define
<del>#3354</del>	2 years ago f	fixed	arch/powerpc	Joel Sherrill	Joel Sherrill	2 years ago
Summar	PowerPC BSPs duplicat	e PAGE_MASK	, etc redefinition			
У	The following BSPs:					
	powerpc-beatnik power	rpc-mcp750 pc	owerpc-mtx603e powe	erpc-mvme2100 powerpc-mvm	e2307 powerpc-mvme5500 powerpc-gemuprep-a	ltivec powerpc-gemuprep
					SIZE. These are defined by <machine param.h="">.</machine>	
	versions in libcpu/page					
					bsp/powerpc/mvme5500///powerpc/shared/sta	artup/pgtbl_setup.c:3:0:
				owerpc/include/incpu/page.ii.	22:0: warning: "PAGE_MASK" redefined	
	#define PAGE_N					
escripti on				powerpc-rtems5/include/sys/_c		
011					15, from /data/home/joel/rtems-work/tools/5/pow 5/powerpc-rtems5/include/sys/types.h:239, from	
	work/tools/5/po	owerpc-rtems5	/include/sys/time.h:43	3, from /home/joel/rtems-work	<pre></pre>	
					read.h:36, from /home/joel/rtems-work/rtems- s-work/rtems-testing/rtems/rtems/cpukit/include	/rtems/rtems/types.h:26, from
	/home/joel/rten	ns-work/rtems	-testing/rtems/rtems/	cpukit/include/rtems.h:31, fro	m	
				nvme5500///powerpc/share		
				Ide/machine/param.n:/U:U: no	ote: this is the location of the previous definition	
		MASK (PAGE_S				
		fixed	lib/block	Sebastian Huber	Sebastian Huber	17 months ago
ummar y	2 years ago f Deprecate rtems_disk_	fixed _create_phys()	lib/block , etc.			
ummar y	2 years ago f Deprecate rtems_disk_	fixed _create_phys() o implementat	lib/block , etc. ions of a block device	(disk). Deprecate the legacy rt	Sebastian Huber	
ummar y escripti on	2 years ago f Deprecate rtems_disk_ There are currently two the block01 test. Add R	fixed _create_phys() o implementat	lib/block , etc. ions of a block device	(disk). Deprecate the legacy rt		
Summar y Pescripti on <b>#3374</b> Summar	2 years ago f Deprecate rtems_disk_ There are currently two the block01 test. Add F	fixed _create_phys() o implementat RTEMS_DEPRE fixed	lib/block , etc. ions of a block device CATED attribute to AP tool	(disk). Deprecate the legacy rt I.	rems_disk_create_phys(), etc. implementation. Re	emove all RTEMS internal uses except in
ummar y escripti on <b>#3374</b>	2 years ago     f       Deprecate rtems_disk_       There are currently two the block01 test. Add F       2 years ago     f	fixed _create_phys() o implementat RTEMS_DEPRE fixed pnormail-from	lib/block i, etc. ions of a block device CATED attribute to AP tool m argument	(disk). Deprecate the legacy rt I.	rems_disk_create_phys(), etc. implementation. Re	emove all RTEMS internal uses except in
Summar y Pescripti on <b>#3374</b> Summar	2 years ago       f         Deprecate rtems_disk_         There are currently two the block01 test. Add R         2 years ago       f         rtems-test does not ho         This is on the master b	fixed _create_phys() o implementat RTEMS_DEPRE fixed onormail-from	lib/block i, etc. ions of a block device CATED attribute to AP tool m argument to other branches.	(disk). Deprecate the legacy rt I. Joel Sherrill	ems_disk_create_phys(), etc. implementation. Re	emove all RTEMS internal uses except in 19 months ago
summar y vescripti on <b>*3374</b> Summar y vescripti	2 years ago f Deprecate rtems_disk_ There are currently two the block01 test. Add R 2 years ago f rtems-test does not ho This is on the master b \$ /home/joel/rtems-wood	fixed _create_phys() o implementat RTEMS_DEPRE fixed 	lib/block i, etc. ions of a block device CATED attribute to AP tool m argument to other branches. stester/rtems-testrt	(disk). Deprecate the legacy rt I. Joel Sherrill	ems_disk_create_phys(), etc. implementation. Re Chris Johns work/tools/5rtems-bsp=erc32log=run.logr	emove all RTEMS internal uses except in 19 months ago
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bescripti on <b>#3381</b> Summar y bescripti on <b>#3382</b> Summar y bescripti on	*         Move it to rtems-tools.         2 years ago       fi         Move rtems-bin2c progra         The rtems-bin2c program         2 years ago       fi         rtems-test command lin         https://docs.rtems.org/         This may also apply to c         2 years ago       fi         Testsuite Makefile merg         Merge the nested Makefile.am for         not spot.       2 years ago         2 years ago       fi         Requireenable-rtemst         There is a limited numb	c1 /F 1000 ixed ixed im (tools/build ixed ixed ixed ixed ived ived ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited ited i	c 6.0 packhex.c tool tools l/rtems-bin2c.c) is doc tion appears to be ster/user/tools/tes s and branch speci build group of tests to a single file per ot practical at this build able-smp orenat	Sebastian Huber exported to the standard RTEMS t Joel Sherrill out of date ter.html#command-line-help does fitc tickets filed. Chris Johns group of tests. point in time because a test is an o Chris Johns ble-multiprocessor	Sebastian Huber build infrastructure via the BIN2C variable. Mov Chris Johns not look like the current output of thehelp. Chris Johns estimated 7 lines and with over 750 tests this n Chris Johns	e it to rtems-tools. 23 hours ago 19 months ago neans the file would be too big and a confl 2 years ago

Common systems like Linux and FreeBSD define int32_t to int. This means a lot of third party code works well in these cases:

	#include <stdi< th=""><th>nt.h&gt;</th><th></th><th></th><th></th><th></th></stdi<>	nt.h>				
	void f(int32 t					
	<pre>void f(int);</pre>	, ,				
	void g(int32_t	^);				
	<pre>void h(void) {</pre>					
	int i; g(&i); }					
	On RTEMS you get h	owever in C				
	<pre>test.c:5:6: er void f(int); ^</pre>	ror: conflicting	g types for 'f'			
	test.c:3:6: no void f(int32	te: previous dec t);	claration of 'f	' was here		
	<pre>test.c: In fun test.c:12:4: w g(&amp;i); ^</pre>		argument 1 of	'g' from incompatible point	er type [-Wincompatible-pointer-types]	
escripti	test.c:7:6: no void g(int32_		nt32_t * {aka lo	ong int *}' but argument is	s of type 'int *'	
on	and C++					
	test.c:12:4: e g(&i);	ction 'void h()' rror: invalid co		'int*' to 'int32_t* {aka lo	ong int*}' [-fpermissive]	
	^~ test.c:7:6: no void g(int32_		ing argument 1 o	of 'void g(int32_t*)'		
					ease the use of third party software in RTEMS we small GCC patch is required to do this:	should override this option and use int fo
		cc/config/rtems.				
		cbb9b1408efe6f		/icens.n		
	+++ b/gcc/conf	ig/rtems.h				
	00 -48,3 +48,7 -latomic -lc		oup %{!qnolinkc	mds: -T linkcmds%s}}}"		
	#define TARGE	T_POSIX_IO				
	+ +/* Use int fo	or int32_t (see s	stdint-newlib.h	). */		
	+#undef STDINT +#define STDIN					
	2 years ago Generate an error if	fixed	build	Chris Johns	Chris Johns	2 years ago
У				_		
escripti on	Generate an error w	ien the user runs o	configure if one ca	annot be found in the path.		
	2 years ago	invalid	admin	Chris Johns	Amar Takhar	19 months ago
ummar y	Trac's git changeset	browsing is suspec	ct.			
	It is critical this inter	face works becaus	e we have moved	I to Trac for release notes and th	ne release notes contain links to the changesets	because we reference the tickets in the
	commits.					
	Some requests work	:				
		ee34cd7a6f1c03c8 ab1ef375ecce55e8				
escripti		38a472842cbc9bc4				
on	and others do not:					
	a. 2afb22b7e1el	bcbe40373ff7e0efa	ae7d207c655a9/rt	ems		
	Notes:					
		3. are either side o and then the Nex		<ul> <li>in the commit history of RTEM also fails.</li> </ul>	S.	
2207					Sebastian Huber	1 months ==
	2 years ago Add subdir-objects to	wontfix o automake flags	build	Chris Johns	Sebastian Huber	4 months ago
У	This will be fixed by	the new build syste	em, see <b>#3818</b> .			
y escripti		fixed	tool	Joel Sherrill	chrisj@	3 weeks ago
y escripti on	2 years ago					
y escripti on			r gemubreb-altive	c on exclude SMP confiduration		
y escripti on			r qemuprep-altive	c on exclude SMP configuration		
y escripti on • <b>3388</b> ummar			r qemuprep-altive	c on exclude SMP configuration		
y escripti on 3388 ummar			r qemuprep-altive	c on exclude SMP configuration		

		sci sistea aci oss ali				
	These failures have p Failures:		,			
		uprep-altivec:				
			rpc-rtems5enable-rte	emsbsp=gemuprep-altivec\pr	fix=/home/joel/rtems-work/bspsenable-netwo	orkingenable-smp
			ror message found!		,	
	powerpc/qem	uprep-altivec:				
				emsbsp=qemuprep-altivec\pre	fix=/home/joel/rtems-work/bspsenable-debug	genable-smp
scripti			ror message found!			
on		uprep-altivec:				
			rpc-rtems5enable-rte ) error: no error messa		fix=/home/joel/rtems-work/bspsenable-debug	genable-\ networking -
		uprep-altivec:				
			rpc-rtems5enable-rte	emsbsp=gemuprep-altivec\pr	fix=/home/joel/rtems-work/bspsenable-smp l	d/collect2:0 error: no
	5	nessage found!		· · · · · · · · · · · · · · · · · · ·	, <u>.</u>	.,
	I checked and it look	s like qemuprep-a	ltivec is listed in the sn	np excludes section.		
	It is the only BSP wit	th a - in the name	that is in smp-excludes	s. Could it be that the matching	ails in this case?	
	· · · · · · · · · · · · · · · · · · ·			a different mechanism.		
<del>3389</del>	2 years ago	fixed	config ecent autoconf changes	Joel Sherrill		19 months ago
ummar y	warning hags have o	isappeared with re	ccent autocom changes			
escripti	As of March 30, the	compiler invocatior	ns had warnings flags.	As of today (4/11), there are no	warnings flag on most of the compiler invocation	s.
on	Something has been	lost in the updates		*		
<del>3390</del>	2 years ago	fixed	network/legacy	Sebastian Huber	Sebastian Huber	2 years ago
ummar y	NFS: Remove support	ie ioi cexp				
	There is some suppo	rt for cexp and tes	ts in the NFS client dire	ectory:		
		rc/nfsclient/src/cex rc/nfsclient/src/rpc		/nfsclient/src/dirutils.c cpukit/lib	s/src/nfsclient/src/nfs.modini.c cpukit/libfs/src/r	nfsclient/src/nfsTest.c
escripti					ld	and subside of the main DTEMC
on			t be present in the RTI		ld move elsewhere, e.g. some general cexp supp	port outside of the main RTEMS
	See also:					
	https://lists.rtems.or	rg/pipermail/users/	/2018-April/032182.htr	nl		
<del>3392</del>	2 years ago	fixed	tool/rsb	Chris Johns	Chris Johns	2 years ago
ummar V	infinite loop in RSB's	path when a prefi	x path is not writable			
escripti on	The code gets the d	irname() of the p	ath stepping up until th	nere is no path however dirnam	('/') is / so the path never has a length of 0	].
011						
	2 years ago	fixed	tool	Chris Johns	Chris Johns	2 years ago
<b>3395</b> ummar			tool when there is an output		Chris Johns	2 years ago
<b>3395</b> ummar y	rtems-ld does not re	move executable v	when there is an output	error		2 years ago
<b>3395</b> ummar y	rtems-ld does not re An error when outpu	move executable v	when there is an output	error file and leaves an incorrect forr		2 years ago
3395 ummar y escripti on	rtems-ld does not re An error when outpu	move executable v	when there is an output	error file and leaves an incorrect forr		2 years ago 2 years ago
and the second s	rtems-ld does not re An error when outpu This is happening wit	move executable v tting an executable th the beagle bone <b>fixed</b>	when there is an output e does not clean up the black BSP and test dl0 tool	error file and leaves an incorrect forr 16.	at file.	
argent states and a second	rtems-ld does not re An error when outpu This is happening wi 2 years ago rtems-ld does not ha	move executable v tting an executable th the beagle bone <b>fixed</b> andle R_ARM_V4BX	when there is an output e does not clean up the black BSP and test dlo tool relocation records	error file and leaves an incorrect forr I6. Chris Johns	at file. Chris Johns	2 years ago
and the second s	rtems-Id does not re An error when outpu This is happening wil 2 years ago rtems-Id does not ha The R_ARM_V4BX do	tting an executable v tting an executable th the beagle bone fixed indle R_ARM_V4BX	when there is an output e does not clean up the black BSP and test dlo tool relocation records	error file and leaves an incorrect forr I6. Chris Johns	at file.	2 years ago
3395 Jammar y escripti on 3396 Jammar y escripti on	rtems-ld does not re An error when outpu This is happening wi 2 years ago rtems-ld does not ha	tting an executable v tting an executable th the beagle bone fixed indle R_ARM_V4BX	when there is an output e does not clean up the black BSP and test dlo tool relocation records	error file and leaves an incorrect forr I6. Chris Johns	at file. Chris Johns	2 years ago
3395 Immar y escripti on 3396 Immar y escripti on 3397 Immar	rtems-Id does not re An error when outpu This is happening wi 2 years ago rtems-Id does not ha The R_ARM_V4BX do Ignore this relocation	move executable v tting an executable th the beagle bone fixed indle R_ARM_V4BX bes not have a sym n record. fixed	when there is an output e does not clean up the black BSP and test dl0 tool relocation records abol and this raised an unspecified	error file and leaves an incorrect forr 16. Chris Johns error with dl06 with a ARMv7 ins	at file. Chris Johns ruction set when merging sections when creatin	g a RAP image.
3395 Jummar y escripti on 3396 Jummar y escripti on	rtems-Id does not re An error when outpu This is happening wit 2 years ago rtems-Id does not ha The R_ARM_V4BX do Ignore this relocation 2 years ago The register keyword	move executable v tting an executable th the beagle bone fixed andle R_ARM_V4BX bes not have a sym n record. fixed d is deprecated in (	when there is an output e does not clean up the black BSP and test dIC tool relocation records abol and this raised an unspecified C++11	error e file and leaves an incorrect forr 16. Chris Johns error with dl06 with a ARMv7 ins Sebastian Huber	at file. Chris Johns ruction set when merging sections when creatin	g a RAP image.
3395 Jammar y escripti on 3396 Jammar y escripti on 3397 Jammar	rtems-Id does not re An error when outpu This is happening wil 2 years ago rtems-Id does not ha The R_ARM_V4BX do Ignore this relocation 2 years ago The register keyword The following code g	move executable v tting an executable th the beagle bone fixed andle R_ARM_V4BX bes not have a sym n record. fixed d is deprecated in (	when there is an output e does not clean up the black BSP and test dl0 tool relocation records abol and this raised an unspecified	error e file and leaves an incorrect forr 16. Chris Johns error with dl06 with a ARMv7 ins Sebastian Huber	at file. Chris Johns ruction set when merging sections when creatin	g a RAP image.
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#### EMS 5 1 Rel Not RT

<del>¥3407</del>	2 years ago					18 months ago
ummar	Move Gaisler.org and	I Gaisler.se hosted	RSB patches to rtem	is.org		
У						
escripti	Jiri has suggested th	at the patches use	d in the RSB that are	hosted by him be moved to rtems.	org and the RSB patches link be changed.	
on	This impacts at least	qemu.				
<del>#3409</del>	2 years ago	wontfix	build	Sebastian Huber	Sebastian Huber	4 months ago
Summar	Strip down configure	checks to the bare	e minimum			
У	These and a lat of an	- <b>6</b> :				
Descripti on	There are a lot of col	ntigure checks white	ch produce HAVE_* d	lefines which are no longer used or	superfluous since we demand a recent Newlib version anyway	
#3410	2 years ago	fixed	arch/i386	Sebastian Huber	Sebastian Huber	2 years ago
Summar	Remove bin2boot pro			Sebastian nuber	Sebastian huber	2 years ago
y	Kennove binzboot pr	Sgram used by 150	0 001 5			
Descripti	For which boot loade	r is this? Can it be	removed?			
on	The sources have no	copyright information	tion.			
<del>#3411</del>	2 years ago	fixed	arch/powerpc	Joel Sherrill	Sebastian Huber	2 years ago
Summar	qemuppc does not in	stall linkcmds.base	2			
У						
Descripti	examples-v2 fail to o	compile qemuppc b	ecause linkcmds.bas	e is not installed.		
on	They build OK for sp	arc/erc32. This mu	st be a minor glitch f	rom the build system changes.		
<del>#3413</del>	2 years ago	fixed	arch/powerpc	Joel Sherrill	Sebastian Huber	4 months ago
Summar	examples-v2 both_h	ello and triple_peri	od fail to build			
У	overnlee v2 descrit	build for comune	both bollo foile bog	auco of comothing going on with st	ame ld. Taking that out of the wearing results in getting furthe	r but faile with an
	undefined error for t		Jour_neilo falls Dec	ause of something going on with ft	ems-ld. Taking that out of the wscript results in getting furthe	i bucians with an
Descripti			verpc-rtems5-acc: be	ello-deep.o: In function `wranThrea	d_Life_action_handler': /home/joel/rtems-work/tools/5/bin/po	owerpc-rtems5-acc
on	/home/joel/rtems-wo	ork/examples-v2/b	uild/powerpc-rtems5	-qemuppc/ hello-deep.c:1304: und	efined reference to `_Thread_Life_action_handler' /home/joel,	
	work/tools/5/bin/pov	verpc-rtems5-gcc:	collect2: error: ld ref	turned 1 exit status		
	error: linking: Linker	error				
<del>#3415</del>	2 years ago	fixed	admin	Joel Sherrill	Chris Johns	19 months ago
Summar	Add examples and te	ests as components	5			
y Decemienti	It seems as if we sho	uld have tests and	evamples as compo	nents		
Descripti on	It seems as it we sho	ulu nave tests and	examples as compo	nents.		
<del>#3416</del>	2 years ago	fixed	tool	Joel Sherrill	Joel Sherrill <joel@></joel@>	5 months ago
		Instructions for 17			,	
Summar						
Summar y						
		.5 of the RSB guide	e for Ubuntu seems t	o work for 17.10 but on at least one	e system gives the error:	
	The command in 3.1	-			e system gives the error:	
У	The command in 3.1 Error :: You must pu	t some 'source' UR	Is in your sources.lis		e system gives the error:	
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<del>#3425</del>	2 years ago	fixed	unspecified	Joel Sherrill		23 months ago
Summar y	examples-v2: Powe		rat_ramuisk			
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89/130

Most applications use a clock driver and most BSPs use the clock driver framework provided by clockimpl.h. This framework offers a Clock_driver_support_shutdown_hardware() hook which is used like this.

90/130

	#ifdef Clock_d: /**	river_support_s	shutdown_hardware			
	* @brief Clo	ck_exit				
	* * This routin	ne allows the d	clock driver to e	xit by masking the interrupt	and	
	* disabling * */	the clock's cou	unter.			
	void Clock_exit	t( void )				
	Clock_driver	_support_shutdo	own_hardware();			
Descripti on	/* do not res	store old vecto	or */			
011	#endif					
	<pre>#ifdef Clock_d:     atexit( Clock #endif</pre>		shutdown_hardware			
	The aim is to stop clo	ock tick interrupts	s at some late point	in the exit() procedure.		
	The use of atexit() p	ulls in malloc() w	hich pulls in errno. I	It is incompatible with the intention	on of the CONFIGURE_DISABLE_NEWLIB_R	EENTRANCY configuration option.
				accompanied clock tick interrup	ts should cause no harm. On the contrary, s	someone may assume a normal operating
	system operation, e.					
<del>#3437</del>	Remove the superflue 2 years ago	fixed	_support_shutdown_ unspecified	_hardware() hook. Sebastian Huber	Sebastian Huber	2 years ago
Summar	Replace use of printk	() in free() with a	· ·			
y Descripti	An invalid hean usag	e such as a doub	le free is usually a f	atal error. Replace the use of prin	ntk() in free() with a fatal error. Introduce a	new fatal error source for heap errors
on	An invalia neap usag		ie nee is usually a la	attai error. Replace the use of phi		new latar error source for neap errors.
<del>#3443</del>	23 months ago	fixed	tool	Sebastian Huber	Sebastian Huber	23 months ago
Summar v	Remove shgen progr	am				
, Descripti	Rename it to rtems-s	shgen.				
on	22 11	<b>C</b> 1				22
<b>#3444</b> Summar	23 months ago Remove nios2gen pro	fixed ogram	tool	Sebastian Huber	Sebastian Huber	22 months ago
y						
Descripti on	Rename it to rtems-r	nios2gen				
#3445	23 months ago	fixed	tool	Sebastian Huber	Sebastian Huber	23 months ago
Summar	Remove multigen scr	ipt				
y	This script is unused	and out dated				
Descripti on	This script is unused	and out dated.				
<del>#3446</del>	23 months ago	fixed	tool	Sebastian Huber	Sebastian Huber	23 months ago
Summar y	Remove cvsignore-ac	ld.sh script				
, Descripti	This script is obsolete	e since moving to	Git.			
on			4 I	Cohestian United	Cohootion Unitor	22 months and
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RTEMS 5.1 Release N	otes
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<del>#3458</del>	23 months ago	fixed	tool	Chris Johns	Chris Johns <chrisj@></chrisj@>	23 months ago
Summar y	rtems-test should	not use the env	PATH to find covoa	r		
1	The rtems-test issues that could be		ld know where cove	ar is when invoking it. It cannot	t use the environment's path. The path can contain	invalid or outdated versions with subtle
Docarinti	There should be n	io need to run in	stall to use and test	rtems-test with coverage.		
Descripti on					example using an absolute path to rtems-tests is to be taught to find the development version of a	
	Note, currently connected to be handle		ternal tools and this	is currently using the environme	nt's path however there is work underway to remo	ve this dependence so there case does not
<del>#3459</del>	23 months ago	fixed	score	Sebastian Huber	Sebastian Huber	14 months ago
Summar y	Rework initialization	on and interrupt	stack support			
	We need an initial	lization stack to	run the sequential s	ystem initialization before multita	asking is enabled. The system initialization is done	with interrupts disabled.
			errupt processing. T I, stack demands of		tack overhead for interrupt processing. The size for	interrupt stack is application dependent,
	The initialization a	and interrupt sta	cks are needed for e	each processor in the system.		
	limited RAM sizes.	. We need the in	itialization stack bef	ore a proper C run-time environr	the interrupt stack for the initialization stack. This i nent is set up e.g. we cannot assume that the acce ). On some BSPs, e.g. ARM, this is done via the linl	ess to global data is available. The stack
					ZE configuration option and not via some magic stu	
	Many BSPs set the	e BSS area to ze	ero during system ini	tialization. Thus, the initializatior	n stack must not be contained in the BSS area.	
	The interrupt stac	k implementatio	on is currently contro	lled by the following CPU port de	fines:	
	/** * Does RTEM	4S manage a de	dicated interrupt	stack in software?		
	*			ref ISR Handler initializat	tion.	
	* If FALSE, *	, nothing is d	one.			
		is generally t		pt stack in hardware, of the BSP to allocate it		
	* the porte	er has two opt the interrupt	ions: (1) execute	interrupt stack, then interrupts on the have RTEMS manage a dedicate	ed	
	* If this i	is TRUE, @ref	CPU_ALLOCATE_INTE	RRUPT_STACK should also be ?	FRUE.	
	* @ref CPU_ * possible * is unclea	_HAS_HARDWARE_ that both are	FALSE for a part ould imply about	RUPT_STACK and hould be set to TRUE. It is icular CPU. Although it the interrupt processing	3	
	*	cific Informat				
	* * XXX docum	nent implement	ation including r	eferences if appropriate		
	*/		INTERRUPT STACK F			
	/**		_			
	*		* *	a dedicated interrupt stack	k?	
			be installed duri allation is perfo	ng initialization. rmed.		
	* If this i	is TRUE, @ref	CPU_ALLOCATE_INTE	RRUPT_STACK should also be ?	FRUE.	
	* @ref CPU_ * possible * is unclea	_HAS_HARDWARE_ that both are	FALSE for a part ould imply about	RUPT_STACK and hould be set to TRUE. It is icular CPU. Although it the interrupt processing	5	
	* Port Spec	cific Informat	ion:			
		nent implement	ation including r	eferences if appropriate		
	*/ #define CPU_	HAS_HARDWARE_	INTERRUPT_STACK T	RUE		
	/** * Does RTEM	4S allocate a	dedicated interru	pt stack in the Interrupt Ma	anager?	
				uring initialization. during initialization.		
	* This shou	uld be TRUE is	CPU_HAS_SOFTWARE	_INTERRUPT_STACK is TRUE.		
	* Port Spec	cific Informat	ion:			
Deservisti	* XXX docum	nent implement	ation including r	eferences if appropriate		
Descripti on		ALLOCATE_INTE	RRUPT_STACK TRUE			
	Do the following s	steps to unify an	d simplify the initiali	zation and interrupt stack suppor	rt.	
				RTEMS_DEFINE_GLOBAL_SYMB ow level initialization code.	OL() macros to basedefs.h, to allow a global symbol	ol definition via C code, e.g. in confdefs.h, to
			".rtemsstack" to the tightly-coupled men		placement of the interrupt stacks. The BSPs can placement	rovide the optimal memory location for this

This makes the CPU_HAS_SOFTWARE_INTERRUPT_STACK and CPU_HAS_HARDWARE_INTERRUPT_STACK CPU port defines superfluous, since the low level initialization code has all information available via global symbols.

This makes the CPU_ALLOCATE_INTERRUPT_STACK CPU port define superfluous, since the interrupt stacks are allocated by confdefs.h for all architectures. There is no need for BSP-specific linker command file magic.

The optional _CPU_Interrupt_stack_setup() is still useful to customize the registration of the interrupt stack area in the per-CPU information.

<del>#3460</del>	23 months ago	fixed	tool/gdb	Chris Johns	Chris Johns	15 months ago
Summar	GDB 8 SIS LEON2 L		1001/940			
y Descripti	Jiri patch for gdb-8	.0.1.				
on #3461	23 months ago	fixed	tool	Sebastian Huber	Sebastian Huber	23 months ago
Summar y	Canadian cross com	pilation of RTEN	1S tools not supported	d for x86_64-w64-mingw32		
<del>#3463</del>	23 months ago	fixed	tool	Chris Johns		19 months ago
Summar y	Convert covoar to u	ise DWARF funct	tion data			
Descripti on	Convert covoar to u boundaries.	ise DWARF func	tion data for the exec	utable symbol table. Objdump is s	till needed for the instruction decode whic	h is needed to find the instruction address
<del>#3465</del>			fs/jaffs2	Sebastian Huber	Sebastian Huber	19 months ago
	22 months ago	fixed				
Summar y			3.11 to v4.17 made in			

	The Git command to ge	nerate the patc	hes is:			
	git format-patch fs/jffs2/compr_rt fs/jffs2/jffs2_fs	v3.11v4.17 time.c fs/jffs _i.h fs/jffs2	include/uapi/l: s2/compr_rubin.c fs 2/jffs2_fs_sb.h fs,	s/jffs2/compr_zlib.c fs/j:	ENCE fs/jffs2/acl.h fs/jffs2/build.c fs/ Efs2/debug.c fs/jffs2/debug.h fs/jffs2/e 2/nodelist.h fs/jffs2/nodemgmt.c fs/jffs	rase.c fs/jffs2/gc.c
	We need a source file tr					
	sed -i 's%/fs/jff	52%/cpukit/li	ibfs/src/jffs2/src	%g' 00*		
	To support the first com	ımit:				
	From: Cody P Scha Date: Thu, 23 Jan	afer <cody@lin a 2014 15:56:1</cody@lin 	nux.vnet.ibm.com> 11 -0800	on Sep 17 00:00:00 2001 torder iteration helper in	istead	
cripti on	of opencoding an	alternate pos	storder iteration (	estroy the rbtree instead that modifies the tree		
	Cc: Michel Lespin Cc: Jan Kara <jac Cc: David Woodhou Signed-off-by: An</jac 	hasse <walken( ck@suse.cz&gt; hse <dwmw2@inf hdrew Morton &lt;</dwmw2@inf </walken( 		tion.org>		
	we have to a postorder					
	The remaining 23 patch					
<b>3471</b> mmar	22 months ago fi Update libfdt as of date	xed 2018-07-09	lib	Sebastian Huber	Sebastian Huber	22 months ago
У			Undata it to the years	ion as of data 2018 07 00		
scripti on	The initial import of libro	at was in 2015.	Update it to the vers	ion as of date 2018-07-09.		
<del>3472</del>		xed	network/libbsd	Sebastian Huber	Sebastian Huber	15 months ago
mmar y	Update of libbsd to a ve		The Freedod 12 Teledo	e		
	The FreeBSD project is	about to prepar	e the FreeBSD 12 rele	ease soon:		
scripti	https://www.freebsd.org	g/releases/12.0	R/schedule.html			
on				SD trunk version close to the	FreeBSD 12 release	
<del>3475</del>		xed	score	Sebastian Huber	Sebastian Huber	21 months ago
ummar	Add RTEMS_PREDICT_T				Sebasilari Huber	21 11011013 890
y		0			ints	
	A		ha faultha CNUL autor			
				nsion builtin_expect(). Use RT	ints EMS_PREDICT_TRUE() and RTEMS_PREDICT_F/ uiltin_expect(), however, the FreeBSD names s	
on	and predict_false(). Alter 21 months ago fi	ernatives are the xed		nsion builtin_expect(). Use RT	EMS_PREDICT_TRUE() and RTEMS_PREDICT_F/	
on <b>3478</b> mmar	and predict_false(). Alte	ernatives are the xed	e Linux likely() and u	nsion builtin_expect(). Use RTI nlikely() or directly the GCC b	MS_PREDICT_TRUE() and RTEMS_PREDICT_F/ uiltin_expect(), however, the FreeBSD names s	eem to be the most easy to understand.
on <b>3478</b>	and predict_false(). Alter 21 months ago fi	ernatives are th x <b>ed</b> nup Needed	e Linux likely() and u	nsion builtin_expect(). Use RTI nlikely() or directly the GCC b Joel Sherrill	MS_PREDICT_TRUE() and RTEMS_PREDICT_F/ uiltin_expect(), however, the FreeBSD names s	eem to be the most easy to understand.
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escripti	The function rtems_r	rfs_buffer_sync() e	erroneously calls rtems	s_disk_release(). This screws u	up the reference counting of the disk.	
on <b>3486</b>	21 months ago	fixed	rtems	Sebastian Huber	Sebastian Huber	21 months ago
ummar y	21 months ago Use uintptr_t and siz		t32_t in rtems_partitio	- i		21 months ago
scripti on					line with rtems_region_create(). On 64-bit targ all address range, e.g. m32c sizeof(uintptr_t) > s	
<b>3488</b> mmar y	21 months ago Remove CONFIGURE	fixed _HAS_OWN_MOUI	config NT_TABLE	Sebastian Huber	Sebastian Huber	20 months ago
scripti on	RTEMS has the confi option.	guration option CO	)NFIGURE_HAS_OWN_	_MOUNT_TABLE since 1999. Th	is configuration option is broken since RTEMS 4	11. Remove this broken configuration
<del>3489</del>	21 months ago	fixed	config	Sebastian Huber	Sebastian Huber	20 months ago
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on #3504	21 months ago	fixed	arch/powerpc	Joel Sherrill	Sebastian Huber	20 months ago
Summar	Warning and format	tting in bsps/pow	verpc/mpc55xxevb/de	ev/dspi.c		
У	This is a printf form	at warning. Also	the file is formatted	with tabs and not two spaces.		
		-			////bsps/powerpc/mpc55xxevb/dev/dsp	i.c:32:0:
	//////r	tems/c/src/lib/lib /rtems/rtems/cpu	obsp/powerpc/mpc55	xxevb/////bsps/powe	rpc/mpc55xxevb/dev/dspi.c: In function 'mpc	
	RTEMS_SYSI	LOG_PRINT( "%s	:: " fmt, <u>func</u> , ## <u>VA_</u>	ARGS)		
	/home/joel/rtems-w	vork/rtems-testir	ıg/rtems/rtems/cpuki	t/include/rtems/status-checks.	n:76:15: note: in definition of macro 'RTEMS_	SYSLOG_PRINT'
	printk( fmt,	## <u>VA_ARGS</u> )				
escripti) on	/home/joel/rtems-w	vork/rtems-testir	ıg/rtems/rtems/cpuki	t/include/rtems/status-checks.	n:109:3: note: in expansion of macro 'RTEMS_	SYSLOG'
	RTEMS_SYSI	LOG( "Error: " fm	nt, ## <u>VA_ARGS</u> ) ~~			
	//////r	tems/c/src/lib/lib	bsp/powerpc/mpc55	xxevb/////bsps/powe	rpc/mpc55xxevb/dev/dspi.c:122:3: note: in e	xpansion of macro 'RTEMS_SYSLOG_ERROR'
	RTEMS_SYSI	LOG_ERROR( "eD	MA error: 0x%08x\n	", error_status); ~		
	///////r	tems/c/src/lib/lib	bsp/powerpc/mpc55	xxevb/////bsps/powe	rpc/mpc55xxevb/dev/dspi.c:122:41: note: for	mat string is defined here
	RTEMS_SYSI	LOG_ERROR( "eE	0MA error: 0x%08x\n	", error_status);		
	<del> %08</del>	<del>llx</del>				
<del>#3505</del>	21 months ago	wontfix	arch/powerpc	Joel Sherrill	Sebastian Huber	19 months ago
Summar	powerpc/virtex rede	efined warning				
У	This looks like ppc4	02 and ppc40E a	ra both dofinad but I	am not cooing course of the pr	c405 definition. The warning is in this section	of codo:
					define exier 0x042 /* DCR: external interrupt	
					ne exier 0x0C2 /* DCR: external interrupt ena	
	In file included from	n /home/joel/rter	ns-work/rtems-testin	g/rtems/rtems/cpukit/include/i	tems/score/percpu.h:25:0,	
	from//./	////rtem	s/c/src/lib/libbsp/pow	verpc/virtex//////bsps	'powerpc/shared/exceptions/ppc_exc_async_n	ormal.S:16:
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escripti	#define exis	r 0x0C0 /* DCR:	external interrupt sta	atus register */		
on	/home/joel/rtems-w	vork/rtems-testir	ıg/rtems/rtems/cpuki	t/score/cpu/powerpc/include/rt	ems/asm.h:224:0: note: this is the location of	the previous definition
	#define exis	r 0x040 /* DCR:	autornal interrupt at-			
			external interrupt sta	atus register */		
	/home/joel/rtems-w	vork/rtems-testir		5 .	ems/asm.h:229:0: warning: "exier" redefined	
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The API should cover the A Sebastian Huber rency Kit) with epoch based ree highly efficient acces affinity and overrules the proc Joel Sherrill pc6866 i386/pcp4 powerpc/brs5 Joel Sherrill ,,//tems/c/src/lib/libbs from integer without a cast [ fferent size [-Wint-to-pointer-cast],/.//trems priong int [-Wformat=],/.//trems</pre>	ems/asm.h:225:0: note: this is the location of Chris Johns ill not have a bin/ directory. If thertems-too tools=/home/joel/rtems-work/tools/5r es-v2 es-v2/build Sebastian Huber PIs defined by the Linux <linux percpu.h=""> and Sebastian Huber lamation (EBR) in May 2018. The goal of this s s to per-processor data. This gives raise to ad essor affinity settings of a thread. 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The API should cover the A Sebastian Huber  rency Kit) with epoch based ref hms need highly efficient acces affinity and overrules the proc Joel Sherrill  pc686 i386/pcp4 powerpc/brs5 Joel Sherrill  //.rtems/c/src/lib/libbs from integer without a cast [- fferent size [-Wint-to-pointer-C inter-cast]/./.tems]///ment 2 has type 'long int' [-Wform</pre>	ems/asm.h:225:0: note: this is the location of Chris Johns ill not have a bin/ directory. If thertems-too tools=/home/joel/rtems-work/tools/5r es-v2 es-v2/build Sebastian Huber PIs defined by the Linux <linux percpu.h=""> and Sebastian Huber lamation (EBR) in May 2018. The goal of this es to per-processor data. This gives raise to ad essor affinity settings of a thread. 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<del>+3512</del>	20 months ago	duplicate	tool/rsb	Justin		19 months ago
ummar	sb-check:No python	command with Pyt	hon 2 and Python 3	installed		
У	On Ubuntu 18.04.1 L	TS there is no com	mand named Pytho	n. I have the following Python c	ommands:	
			10n3.6m-config			
escripti on	python3m-confi python3.6m			.6-config python3-config	python3	
	I am going to symlin	k python2.7 to pyt	hon to make it work	but there should be a better so	plution.	
+ <del>3513</del>	20 months ago Convert tgm8xx cons	fixed	arch/powerpc	Sebastian Huber	Sebastian Huber	20 months ago
ummar y	Convert tymoxx cons	sole univer to new	Termios API			
<del>3516</del>	20 months ago	fixed	tool/rsb	Joel Sherrill	Chris Johns	19 months ago
ummar y	sb-set-builder should	l report disk usage	of build			
escripti on				or their VM images to not have ould be fed into the Users Guide	enough disk space for the build to complete. It wo	uld be useful if the set-builder could
<del>3517</del>	It is frustrating and a 20 months ago	bad experience to fixed	o watch the build fail doc	90% of the way through. Joel Sherrill	Joel Sherrill <joel@></joel@>	19 months ago
ummar	RSB Ubuntu Host Re	1	1	Joer Sherrin		15 11011113 490
У	Add libncurses5-dev	and zlib1a-dev to	Ubuntu ant-get instr	ictions		
escripti on	Also bison and flex s					
<del>3518</del>	20 months ago	fixed	doc	Joel Sherrill	Chris Johns	19 months ago
ummar	RSB MacOS Nits					
У	The MacOS section o	f the RSB manual	has some minor thin	gs that need to be fixed:		
escripti	<ul> <li>says Serria w</li> </ul>	hen it should be Si	erra (I think).			
on			a formatting error			
	Please review as a M		-			
<del>3519</del>	20 months ago	fixed	tool/rsb	Chris Johns	Chris Johns	20 months ago
ummar	RSB does not strictly	check args				
y escripti	The RSB loose argun	nent parsing in the	RSB needs to chang	e. The RSB needs to strictly che	eck arguments to avoid simple user errors.	
on	_		-	- -	- ·	
<b>3520</b> ummar	20 months ago Remove CONFIGURE	fixed _HAS_OWN_FILES	CONFIG	Sebastian Huber	Sebastian Huber	20 months ago
y escripti	This configuration is	untested and undo	cumented. Remove	t to avoid a potential exposure	of internal data structures to the application domai	in.
on				· · · · · · · · · · · · · · · · · · ·	· · · · · · · · · · · · · · · · · · ·	
<del>3522</del>	20 months ago Update mDNSRespor	fixed	network/libbsd	Sebastian Huber	Sebastian Huber	20 months ago
ummar y	opulic individuespoi	ider to Apple versi	011 007 0.50.1			
escripti on	765.20.4.tar.gz mDN 878.30.4.tar.gz from	ISResponder-765.3	30.11.tar.gz mDNSR(	esponder-765.50.9.tar.gz mDNS	Responder-878.1.1.tar.gz mDNSResponder-878.20	J.3.tar.gz mDNSResponder-
	https://opensource.a	pple.com/tarballs/	mDNSResponder/			
+ <del>3523</del>	Merge each update in 20 months ago	nto libbsd/mDNSRe fixed	sponder. network/libbsd	Sebastian Huber	Sebastian Huber	20 months ago
ummar	Add FEC network inte			Sebastian Huber	Sebastian huber	20 months ago
y escripti	Import legacy netwo	rk driver and port	it to libbsd.			
on 3525	19 months ago	fixed	arch/arm	Sebastian Huber	Sebastian Huber	19 months ago
ummar	Add MMC/SDCard su		arch/arm al BSP	Sebastian nuber	Sebastian nuber	19 11011115 890
У	Port device drivers fr	om FreeBSD for i !	MX 7Dual uSDHC mo	dule.		
			200/10/110	-		
on						
on <del>3526</del>	19 months ago	fixed	dev/serial	Sebastian Huber	Sebastian Huber	19 months ago
on <del>3526</del>	19 months ago Convert PTY driver to		dev/serial	Sebastian Huber	Sebastian Huber	19 months ago
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on 3526 Immar y escripti on	Convert PTY driver to	o new Termios API	-			
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У	RSB source only dow		cinc			
					tes or patches by host to work around specific h	ost issues. Currently a source only down
				the RSB is being run on.		
					n a release. This issue means some host specific	
	7.3.0-newlib-d13c	84eb07e35984bf7	7a974cd786a6cdac29e	6b9.cfg:	onfiguration scripts and this type of logic exists i	m computation mes such as rtems-gee
	%patch add go	crsb-file=f		ix.patch -p0 https://gcc.	gnu.org/bugzilla/attachment.cgi?id=41380 b387d02395cle8ff4a99225152387ale60a4	
	The simpler construc	t in rtems-tool	s-common-1.cfg of:			
Descripti on		mingw ming32 OOT_WAF=\$SB_BUI	LD_ROOT\$(echo %{_pr	refix}   cut -c 1-2)		
	SB_BUILD_RC %endif	OOT_WAF=\$SB_BUI	LD_ROOT			
		t sources or patch	nes however it shows v		th also need to be followed and this could break hink creating a new variable such as <b>§{downloa</b>	
	%patch add go %else	c foobar-bsd.p	atch	l    %{_build_os} == darwi	n	
	%endif	c foobar-gnu.p				
#3533	We require the logic 19 months ago	to follow the %i: fixed	f True path and the rtems	<pre>%else path. Sebastian Huber</pre>	Sebastian Huber	19 months ago
Summar	Add rtems_task_exit		Ttems	Sebastian nuber	Sebastian Huber	19 months age
у		ete(RTEMS_SELF)	function does not ret	urn. In order to aid compilers	and static analysis tools provide an rtems_task_	exit() function which can be specified as
	no return function.	k exit(void) R	TEMS NO RETURN;			
on on	This is similar to the					
			alue_ptr)dead2;			
<del>#3535</del>	19 months ago	fixed	lib	Sebastian Huber	Sebastian Huber	19 months ago
Summar	Remove stdin, stdou	t, stderr convenie	ence routines for CEXP	•		
y Descripti on	These functions shou	uld be moved to a	a general CEXP suppor	t.		
<del>#3536</del>	19 months ago	fixed	tool	Chris Johns	Chris Johns	19 months ago
Summar y	Move RTEMS configu	iration data to a o	common config direc	ctory		
Descripti on	of tools access to the	e arch/bsp data			project and create an <code>rtems.py</code> module to han	dle the configuration. This allows a numb
		y ui uata can muv	tool	Chris Johns	Chris Johns	
<del>#3537</del>	In time this directory 19 months ago	fixed		CIIII3 JOIIII3	CIIII3 JOIIII3	16 months ago
				Chills Johns		16 months ago
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Summar y Descripti on <b>#3538</b> Summar y Descripti on <b>#3542</b> Summar y Descripti on <b>#3543</b> Summar y Descripti on <b>#3545</b> Summar y Descripti on <b>#3545</b>	19 months ago RSB and RTEMS Too Upstream python do command. The RSB and RTEMS 19 months ago Classic API Barrier W The section title says Likely also applies to 19 months ago Remove CPU_PROVI Remove the CPU_PR 19 months ago Remove keep_stdio The Telnet service st support. This can be 19 months ago Change Telnet serve The Telnet server cu 19 months ago Support O_DIRECTO Use this flag in open 19 months ago	Is Support for pyles not create a processed of the python corfixed value of the python correctly of the python	thon2 and python3 python2 command any mmands need to be up doc Has Wrong Name score AD_BODY HREAD_BODY HREAD_BODY option to lib telet service telenetd_start() has a k (cation task on its own lib t resources during initi teresources needed for	more and creates python2 a dated and tested so they run Joel Sherrill Sebastian Huber Sebastian Huber Reeep_stdio feature. This just a Remove this functionality an Sebastian Huber alization r a client connection on dema	nd python3. Distributions and operating syster on Python2 and Python3 and support added to a Joel Sherrill Sebastian Huber al compilation. Sebastian Huber task and executes the command function in a lo d provide only the real Telnet services. Sebastian Huber nd. Allocate most resources during initialization	ns are starting to ship without the pytho use the available commands. 19 months ago 19 months ago 19 months ago pop. For this kind of service we do not lib 19 months ago to avoid sporadic resource shortage issu
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escripti on	This is a POSIX flag. Mak	e sure its u	se causes no open fait	ne.		
3549	19 months ago fix	ed	arch/powerpc	Sebastian Huber	Sebastian Huber	19 months ago
ummar	Obsolete powerpc/virtex	BSP				
У	This DCD is suits ald (		(1)	Marka and an array		
escripti on	This BSP is quite old (wa					
	https://lists.rtems.org/p				Sobaction Huber	19 months ago
3551	19 months ago fix Move default configuration	ed	lib te library	Sebastian Huber	Sebastian Huber	18 months ago
ummar y	Hove deladit configuration		ice indiary			
escripti on	problems: 1. Application config	uration erro out what ca	rs may pull in the defa	ult configuration which in turn	configuration.c. This default configuration is conta leads to multiply define symbols error. This is qui l to know what a linker map file is and how you g	te confusing. You have to consult the linker
		-	configuration items fo	r each subsystem in librtemscp	u.a. This can be used to reduce the size of the co	nfiguration itself.
				rate library, e.g. librtemsdefau		
	19 months ago fix cpu usage error in SMP r	ed	shell	jameszxj	Chris Johns	4 months ago
ummar v	cpu usage error in Shir i	noue				
1	CDU last dispets		)			
	CPU load dispaly	error in SMP	' mode			
	Load Average: 10	0.510% Loa	ad: 100.966% Idle: 99.	033%		
escripti	In fact,CPU is doi	ng nothing.				
on		lers priority	SMP mode is map to a core priori lay the infomation	ty, I think unmap		
			-			
3553	I create a patch about th 19 months ago fix	is,see the a	build	Joel Sherrill		18 months ago
ummar	rtems-libbsd Missing waf					
у						
escripti on	libbsd.				nown to work for the users' convenience. rtems-li	ibsd is missing one. Add one to rtems-
2554	Also (if there are other r				Cohastian Unhar	15 months ago
<b>3554</b> ummar v	19 months ago fix rtems-libbsd README.wa	ed of Needs an	network/libbsd Update Sweep	Joel Sherrill	Sebastian Huber	15 months ago
on	It is out of date at least	oy mentionii	ng 4.12 instead of 5. If	there are other nits or issues,	they need to be addressed while updating the rel	lease info.
<del>3555</del>	19 months ago fix	ed	admin	Amar Takhar	Amar Takhar	19 months ago
ummar y	IRC bots need to be regi	-				
on			-	h #rtems. The bots both need		10 months ago
	19 months ago fix Test ticket	ed	admin	Amar Taknar	Amar Takhar	19 months ago
ummar y						
escripti	Using this as a test ticke	t to test out	my fix.			
on						
<del>3558</del>	5	ed	admin	Amar Takhar	Amar Takhar	19 months ago
ummar	Update TracSpamFilter?					
y escripti	Updated to the latest Tra	ic Spam Filt	er and upgraded captc	ha to v2 to avoid any errors. T	nis was reported a while back and should fix any i	ssues.
on	There are more please o					
2550				Amor Takhar	Amar Takhar	10 months ago
<del>3559</del> ummar	19 months ago fix Fix NavAdd? plugin.	ed	admin	Amar Takhar		19 months ago
y	Frogrin					
	I had no idea but this ha	d gotten rer	moved in the last upgra	ade I've re-added it.		
	This makes a few change	es to the nav	vigation:			
escripti	<ul> <li>"New Ticket" now</li> </ul>					
on	<ul> <li>There is a new but</li> </ul>	tton "New T	"icket (direct)" in the u		o directly go to creating a ticket.	
		-		the new wiki: MyTickets page.		
2560					anyone has complaints about it coming back if no	
<b>3560</b> ummar	19 months ago fix Fix FlexibleAssignTo?	ed	admin	Amar Takhar	Amar Takhar	19 months ago
y y						
	When we first went to tra	ac we had re	estrictions on the 'owne	er' to developers only. When tr	ack was upgraded this broke completely but all th	ne code to handle this was already in place.
_	I took the time to fix it to	oday so we	have dropdowns again.			
escripti on	I've created this ticket to	see if anyo	one has an issue with th	nis should we keep it? Drop it?	I know it's been years but it was our original choi	ice.
		-			ese are based on trac permissions so we can alwa	
				ner so we can ensure tickets a		the deal more in we need it but it really
<del>3561</del>	19 months ago fix	ed	admin	Amar Takhar	Amar Takhar	17 months ago
ummar	Migrate to CommitTicket	Updater?				
У	The old series	+	tod The second the fit	off and moved to the fatter of	atom dependences	
escripti				off and moved to the internal s	ystem described nere:	
on	https://trac.edge	wall.org/wik	i/CommitTicketUpdate	r		
011	This should handle all sc	enarios if it	does not please let me	know. I will leave this ticket o		: :
• <b>3562</b> ummar		ed	tool/rsb	Chris Johns	Chris Johns	18 months ago

Descripti	BuildRoot: %{	_tmppath}/%{nam	ne}-root-%(%{id	L_u} -n)		
on	This is for a shared	STEMP path plus	the name is not sh	ortened so on Windowss these p	paths become long. Remove the BuildRoot from	all configuration files and add support for
<del>#3568</del>	shortened temporar		needs short paths	due to the 256 max. path length Sebastian Huber		16 months ago
Summar	18 months ago RSB: UnboundLocal	- i	tool/rsb able 'build_max_siz	e_human' referenced before assi		10 months ago
У	(answers builden/ab	ant builden aus		E (shares and b		
	/source-builder/sb	-set-builderpre	sitx=/build/items/5	S/Items-ofic		
	config: tools/			96f678cf5e5968acaed0bae9fd1		
	warning: gcc-4	4.9.3-orlk.patc	ch: no hash found			
				6396f678cf5e5968acaed0bae9f -08eab6396f678cf5e5968acaed	d129983b-x86_64-linux-gnu-1 0bae9fd129983b-x86_64-linux-gnu-1	
loccrinti	Build FAILED See error re	eport: rsb-repo	ort-or1k-rtems5-c	cc-4.9.3-newlib-08eab6396f6	78cf5e5968acaed0bae9fd129983b-x86_64-linu:	k-gnu-1.txt
on on		ng orlk-rtems5- t: build@rtems.		-08eab6396f678cf5e5968acaed	Obae9fd129983b-x86_64-linux-gnu-1	
	Traceback (mos	st recent call	last):	.py", line 26, in <module></module>		
	setbuilder	r.run()		wurce-builder/sb/setbuilder.	py", line 619, in run	
	b.build(de	eps, mail = mai	il)	ource-builder/sb/setbuilder.		
	body += 'N	Maximum build u	usage: ' + build_	<pre>max_size_human + os.linesep size human' referenced beferenced</pre>		
	:		-			
<b>#3569</b> Summar	18 months ago waf version in variou	fixed us rtems-reposito	build pries incompatible w	Malte Münch	Chris Johns	2 months ago
y						
escripti	The current waf vers	sion included in r	tems-tools is waf 1.	.9.9 (389f3f3b289f6b835a21ad4	e128076cdb463d34d <b>)</b>	
on	it crashes wh	ien executed with	1 python3. The curr	ent version of waf is 2.0.12 and i	resolves this issue.	
<del>‡3576</del>	18 months ago	fixed	tool/qdb	Joel Sherrill	Joel Sherrill	18 months ago
Summar	gdb 8.0.1 sis does n	iot build on Cygw	/in	:		
У	Cyawin no longer ha	as libtermcap, od	b/sim/erc32 needs	a patch to find libncurses. Upstre	eam odb patch already merged.	
escripti					e36e1aa40f3bbadcef9b5d9ddc041	
on				to gdb 8.0.1 and to track adding		
<del>¥3577</del>	18 months ago	fixed	tool/gcc	Sebastian Huber	Sebastian Huber	18 months ago
Summar y	Avoid CLooG and IS	L host depencent	cies for target GCC			
escripti					ooG and ISL. This helps to ensure that the same t	
on	RTEMS.	ps to avoid GCC b	build issues in case	future versions of ISL and CLooG	G available on the host system are incompatible to	o the GCC version picked up by the RSB f
<del>\$3579</del>	18 months ago	fixed	admin	Chris Johns	Chris Johns <chrisj@></chrisj@>	18 months ago
Summar y	testsuite's rtems-tes	st-cneck.py pytho	on version support			
escripti	This command used	in the testsuite r	needs to find a suita	able python or the build system r	needs to find it and invoke it with that python.	
on <b>#3583</b>	18 months ago	fixed	rtems	Sebastian Huber	Sebastian Huber	18 months ago
Summar	Add rtems_malloc()	and rtems_callor	c()			
У	The standard C/POS	IX functions mal	loc() and calloc() se	et errno in case of an error. A der	pendency to errno pulls in getreent() which pulls	in a lot of data structures and functions.
escripti				asic board support package initia		
00	Provide rtems_mallo	oc() and rtems_ca	alloc() functions dee	clared in <rtems malloc.h=""> which</rtems>	h do the same as the corresponding C/POSIX fun	ctions except setting errno.
on		- C		not have this issue with the errne		
	18 months ago	fived	score	Sebastian Huber	Sebactian Huber	5 months ago
<del>#3585</del>	18 months ago Deprecate proc_ptr	fixed	score	Sebastian Huber	Sebastian Huber	5 months ago
<del>#3585</del>	Deprecate proc_ptr	:	score	Sebastian Huber	Sebastian Huber	5 months ago
<b>#<del>3585</del></b> Summar		:	score	Sebastian Huber	Sebastian Huber	5 months ago
<b>#3585</b> Gummar Y	Deprecate proc_ptr See comment in bas /** * XXX: Ever	sedefs.h	SCORE		Sebastian Huber	5 months ago
<b>#3585</b> Summar y	Deprecate proc_ptr See comment in bas /** * XXX: Ever */	sedefs.h			Sebastian Huber	5 months ago
<b>#3585</b> Gummar y Descripti on	Deprecate proc_ptr See comment in bas /** * XXX: Ever */ typedef voic	sedefs.h ntually proc_pt d * proc_ptr;	tr needs to disap	pear!!!		
<b>#3585</b> Gummar y Descripti on	Deprecate proc_ptr See comment in bas /** * XXX: Ever */	<pre>sedefs.h ntually proc_pt d * proc_ptr; fixed</pre>			Sebastian Huber Sebastian Huber	5 months ago 18 months ago
#3585 Gummar y Descripti on #3587 Gummar y	Deprecate proc_ptr See comment in bass /** * XXX: Ever */ typedef voic 18 months ago Deprecate rtems_co	<pre>sedefs.h ntually proc_pt d * proc_ptr; fixed ontext</pre>	tr needs to disap	pear!!! Sebastian Huber	Sebastian Huber	18 months ago
#3585 Gummar y Descripti on #3587 Gummar y	Deprecate proc_ptr See comment in bass /** * XXX: Ever */ typedef voic 18 months ago Deprecate rtems_co	sedefs.h ntually proc_pt d * proc_ptr; fixed ontext typedef as no cor	tr needs to disap	pear!!! Sebastian Huber		18 months ago
#3585 Gummar y Descripti on #3587 Gummar y Descripti on	Deprecate proc_ptr See comment in bas /** * XXX: Ever */ typedef voic 18 months ago Deprecate rtems_co The rtems_context t RTEMS. Deprecate it 18 months ago	sedefs.h ntually proc_pt d * proc_ptr; fixed ontext typedef as no cor t in this release. fixed	tr needs to disap	pear!!! Sebastian Huber	Sebastian Huber	18 months ago
#3585 Gummar y Descripti on \$ Summar y Descripti on \$	Deprecate proc_ptr See comment in bas /** * XXX: Ever */ typedef voic 18 months ago Deprecate rtems_co The rtems_context t RTEMS. Deprecate it	sedefs.h ntually proc_pt d * proc_ptr; fixed ontext typedef as no cor t in this release. fixed	tr needs to disap	pear!!! Sebastian Huber user can do nothing with it. It is c	Sebastian Huber only used in cpukit/libmisc/monitor/mon-monitor	18 months ago .c and cpukit/libmisc/monitor/mon-editor
#3585 Gummar y Descripti on *3587 Gummar y Descripti Gummar y Descripti	Deprecate proc_ptr See comment in bas /** * XXX: Ever */ typedef voic 18 months ago Deprecate rtems_co The rtems_context t RTEMS. Deprecate it 18 months ago Deprecate rtems_co	sedefs.h ntually proc_pt d * proc_ptr; fixed ntext typedef as no corr t in this release. fixed ntext_fp	rtems rtems rtems	spear!!! Sebastian Huber user can do nothing with it. It is o Sebastian Huber	Sebastian Huber only used in cpukit/libmisc/monitor/mon-monitor	18 months ago .c and cpukit/libmisc/monitor/mon-editor 18 months ago
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<pre>#3585 Gummar y Descripti on #3587 Gummar y Descripti on #3589 Gummar y Descripti on #3589 Gummar y </pre>	Deprecate proc_ptr See comment in bas /** * XXX: Ever */ typedef voic 18 months ago Deprecate rtems_co The rtems_context t RTEMS. Deprecate it 18 months ago Deprecate rtems_co	sedefs.h ntually proc_ptr; fixed ntext typedef as no corr t in this release. fixed ntext_fp fp typedef as no fixed	rtems	spear!!! Sebastian Huber user can do nothing with it. It is o Sebastian Huber	Sebastian Huber only used in cpukit/libmisc/monitor/mon-monitor Sebastian Huber	18 months ago .c and cpukit/libmisc/monitor/mon-editor 18 months ago
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*3585 iummar y bescripti on *3587 iummar y bescripti on *3591 iummar y vescripti on *3591 iummar y *3593 iummar y	Deprecate proc_ptr See comment in bas /** * XXX: Ever */ typedef void 18 months ago Deprecate rtems_context t RTEMS. Deprecate it 18 months ago Deprecate rtems_context_ 18 months ago Deprecate region_inf The rtems_context_ 18 months ago Deprecate region_inf The region_informat cpukit/libmisc/cpuus 18 months ago Deprecate rtems_th The rtems_thread_c	sedefs.h ntually proc_ptr d * proc_ptr; fixed ontext typedef as no cort t in this release. fixed ntext_fp fp typedef as no fixed iformation_block tion_block typedef se/cpuusagetop.cc fixed iread_ccpu_usage_ cpu_usage_t type	tr needs to disap rtems rresponding API. A u rtems corresponding API. rtems ef as no correspondi c and cpukit/libmisc, rtems t t ed as no correspondi	pear!!!  Sebastian Huber User can do nothing with it. It is of Sebastian Huber  A user can do nothing with it. It Sebastian Huber  ing API. It has no proper namesp /shell/main_mallocinfo.c in RTEM Sebastian Huber	Sebastian Huber         only used in cpukit/libmisc/monitor/mon-monitor.         Sebastian Huber         is only used in cpukit/libmisc/monitor/mon-edito         Sebastian Huber         Sebastian Huber	18 months ago .c and cpukit/libmisc/monitor/mon-editor 18 months ago r.c in RTEMS. Deprecate it in this release 18 months ago ly used in 18 months ago

<del>#3598</del>	cpukit/include/rtem			ate it in this release.		
#3330	18 months ago	fixed	rtems	Sebastian Huber	Sebastian Huber	17 months ago
Summar y	Move internal types	of API objects to	o separate header fil	2		
	necessary for the a <ul> <li><rtems li="" rtem<=""> </rtems></li></ul>	pplication configu ns/XYZ.h> ns/XYZimpl.h> ns/XYZdata.h>		ntation details via the definition o	f internal data structures, e.g. the *_Control we have:	structures of the API objects. They are only
	Potential new head	er files are:				
Descripti on	<ul> <li>rtems/rtems</li> <li>rtems/rtems</li> <li>rtems/rtems</li> <li>rtems/rtems</li> <li>rtems/rtems</li> </ul>	s/asrdata.h //barrierdata.h //dpmemdata.h /eventdata.h //messagedata.h //partdata.h //ratemondata.h //ragiondata.h //semdata.h				
<del>#3599</del>	18 months ago	fixed	arch/m32c	Sebastian Huber	Sebastian Huber	18 months ago
Summar	Remove m32c arch					
y Descripti on	There are no known	n users:	plete, e.g. important ers/2018-January/03		rt are missing. It never run on real hardware	e. The tools are out dated and unmaintained
<del>#3600</del>	18 months ago	fixed	arch/or1k	Joel Sherrill	Joel Sherrill	5 months ago
Summar	Update or1k tools t	o use GCC maste	er			
У	Chaffe and Llaws a second					for my the second se
escripti on	from his repository	before the merge	e so it should be wor	king.	We can now switch from gcc 4.9.3 to a hash nt to release using gcc 4.9.3 for any target.	from the gcc master. I was testing periodic
<del>#3602</del>	18 months ago	fixed	arch/or1k	Sebastian Huber	Sebastian Huber	18 months ago
ummar	Update or1k tool ch					· · · · · ·
У						
<del>¥3603</del>	18 months ago	fixed	score	Sebastian Huber	Sebastian Huber	17 months ago
ummar y	Remove support for	16-bit object ide	entiners			
, escripti					16-bit object identifiers. If someone really v	wants to use RTEMS on a 16-bit target, ther
on				around with object identifier opti		
<del>#3604</del>	18 months ago	fixed	lib/dl non section on i386/	Joseph Hickey	Chris Johns	17 months ago
Summar	KIL UIIIESUIVEU Syl	hous nom comm	Ion section on 1380/	(cloned)		
У						
У	Cloned from #3527	:				
	By default GCC puts symbols are not res	s uninitialized glo solved as expecte	ed.		hen attempting to load the resulting ELF file	at runtime using dlopen(), these global
Descripti	By default GCC put symbols are not res The RTL reports un	s uninitialized glo solved as expecte resolved symbols	ed. s, and runtime code t	hat take the address of the globa	l get NULL instead.	
escripti	By default GCC put: symbols are not res The RTL reports un This is reproducible	s uninitialized glo solved as expecte resolved symbols using the libtest	ed. s, and runtime code t s/dl01 example by a	hat take the address of the globa dding a global variable to the mo	I get NULL instead. dule code. I will attach a patch that replicates	
Descripti on	By default GCC put: symbols are not res The RTL reports un This is reproducible	s uninitialized glo solved as expecte resolved symbols using the libtest	ed. s, and runtime code t s/dl01 example by a	hat take the address of the globa	I get NULL instead. dule code. I will attach a patch that replicates	
escripti on #3605 Gummar	By default GCC put symbols are not res The RTL reports un This is reproducible Test platform is QE 18 months ago	s uninitialized glo solved as expecte resolved symbols using the libtest MU using pc686 f <b>fixed</b>	ed. s, and runtime code t s/dl01 example by a BSP, RTEMS source v	hat take the address of the globa dding a global variable to the mo ersion 4.11.3 (latest on 4.11 git Kevin Gordon	I get NULL instead. dule code. I will attach a patch that replicates	s the issue.
escripti on	By default GCC put symbols are not res The RTL reports un This is reproducible Test platform is QE 18 months ago RTL Allows Unloadin	s uninitialized glo solved as expecter resolved symbols using the libtest MU using pc686 f fixed ng a Module othe	ed. s, and runtime code t s/dI01 example by a BSP, RTEMS source v lib/dl	hat take the address of the globa dding a global variable to the mo ersion 4.11.3 (latest on 4.11 git Kevin Gordon	I get NULL instead. dule code. I will attach a patch that replicates	s the issue.
escripti on #3605 Gummar	By default GCC put symbols are not res The RTL reports un This is reproducible Test platform is QE <b>18 months ago</b> RTL Allows Unloadin Cloned from <b>#3195</b>	s uninitialized glo solved as expecter resolved symbols using the libtest MU using pc686 f fixed ng a Module othe	ed. s, and runtime code t s/dl01 example by a BSP, RTEMS source v lib/dl r Modules Depend U	hat take the address of the globa dding a global variable to the mo- rersion 4.11.3 (latest on 4.11 git Kevin Gordon oon (cloned)	I get NULL instead. dule code. I will attach a patch that replicates branch as of this writing)	s the issue.
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Descripti on #3605 Gummar	By default GCC put symbols are not res The RTL reports un This is reproducible Test platform is QE 18 months ago RTL Allows Unloadin Cloned from #3195 Consider the follow	s uninitialized glo solved as expecte resolved symbols using the libtest MU using pc686 f fixed ng a Module othe : ing example usin	ed. s, and runtime code t s/dl01 example by a BSP, RTEMS source v lib/dl r Modules Depend U g ELF .o files from co	hat take the address of the globa dding a global variable to the mo- rersion 4.11.3 (latest on 4.11 git Kevin Gordon pon (cloned)	I get NULL instead. dule code. I will attach a patch that replicates branch as of this writing)	s the issue. 5 months ago
Descripti on # <b>3605</b> Summar y	By default GCC put symbols are not res The RTL reports un This is reproducible Test platform is QE <b>18 months ago</b> RTL Allows Unloadin Cloned from #3195 Consider the follow module-0.o is loade by RTL. The RTL function dl subsequent call to	s uninitialized glo solved as expecte resolved symbols using the libtest MU using pc686 f fixed ng a Module othe :: ing example usin ed using dlopen() cose() returns no module1Function	ed. s, and runtime code t s/dl01 example by a BSP, RTEMS source v lib/dl r Modules Depend Up g ELF .o files from co with no code or data o error when module: 1() in the currently-l	hat take the address of the global dding a global variable to the mo- rersion 4.11.3 (latest on 4.11 git Kevin Gordon opon (cloned) mpiled source files module-0.c a a dependencies. module-1.o is log 0 is unloaded, when it should ret	I get NULL instead. dule code. I will attach a patch that replicates branch as of this writing) dumodule-1.c from ticket #3194: wided using dlopen() with both code and data urn an error and not unload module-0. This Is s shared_resource_0[] and calls module0Fu	s the issue. 5 months ago dependencies on module-0 which are resolv becomes quite dangerous because a
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	https://devel.rtems.c	org/ticket/3598#comment	:40 https://d	evel.rtems.org/ticket/3598#c	omment:41	
escripti on	Compare with Git cor					
	https://git.rtems.org,					
<del>3621</del>	id=eaa5ea84eaf1b3d 17 months ago	lab72d7a7a6578f0dc59e5 fixed score		ge&q=1947449a5d6f01a44cc Sebastian Huber	c61eda3e78ef7e06da9525fc727fe77a632f9d Sebastian Huber	f38161a8474007dab020608 4 months ago
ımmar		ject information structure				
У	Statically initialize the	a object information struct	turos to make	the configuration easier to r	eview and simplify the debugging.	
				.h> looks currently like this:	eview and simplify the debugging.	
				= { ( ( ( (ssize t) (((	//1 +	
	0) != 0 ? 1 : 0	0) * ((Objects_Maximum	) ((1 + 0)			
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		iguration_Thread_contr rotection_block_begin)		<pre>sizeof(uintptr_t) + eap_Protection_block_end)</pre>	))) +	
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		(Configuration_Initial				
	sizeof(User_ext	uration_Initial_Extens tensions_Switch_contro	1) + (2 * s	izeof(uintptr_t) +		
	((((sizeof(Hear	p_Block)) + (8) - 1) -	((sizeof(H	<pre>eap_Protection_block_end) eap_Block)) + (8) - 1) %</pre>	(8))) -	
	(8)))))) + 0 +	0 + (0 * 1024) + ((((	2 * sizeof(	eof(Heap_Block)) + (8) - uintptr_t) +		
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	sizeof(Heap_Pro	otection_block_end))))	+ (8) - 1)	% (8)) ),		
2622	The object controls re 17 months ago	1	r fixed object ecified	count configuration. Using a Sebastian Huber	statically allocated array makes it easier to fine Sebastian Huber	d the objects during debugging. 17 months ago
immar	-	les working with a process		Sebastiali Hubei	Sebastian Huber	17 months ago

	<pre>/**  * @brief Flushes multiple data cache lines for a set of processors  *</pre>
	<ul> <li>* Dirty cache lines covering the area are transferred to memory.</li> <li>* Depending on the cache implementation this may mark the lines as invalid.</li> </ul>
	* * This operation should not be called from interrupt context. *
	<pre>* @param[in] addr The start address of the area to flush. * @param[in] size The size in bytes of the area to flush. * @param[in] setsize The size of the processor set. * @param[in] set The target processor set.</pre>
	<pre>*/ void rtems_cache_flush_multiple_data_lines_processor_set(     const void *addr,     size_t size,     const size_t setsize,     const cpu_set_t *set );</pre>
	/**
	* @brief Invalidates multiple data cache lines for a set of processors
	* The cache lines covering the area are marked as invalid. A later read * access in the area will load the data from memory. *
	* In case the area is not aligned on cache line boundaries, then this * operation may destroy unrelated data.
	* This operation should not be called from interrupt context.
	<pre>* @param[in] addr The start address of the area to invalidate. * @param[in] size The size in bytes of the area to invalidate. * @param[in] setsize The size of the processor set. * @param[in] set The target processor set.</pre>
cripti on	<pre>*/ void rtems_cache_invalidate_multiple_data_lines_processor_set(     const void *addr,     size_t size,     const size t setsize,</pre>
	<pre>const cpu_set_t *set );</pre>
	/**  * @brief Flushes the entire data cache for a set of processors  *
	* This operation should not be called from interrupt context.
	<pre>* @see rtems_cache_flush_multiple_data_lines(). *</pre>
	* @param[in] setsize The size of the processor set. * @param[in] set The target processor set.
	<pre>*/ void rtems_cache_flush_entire_data_processor_set(     const size_t setsize,     const cpu_set_t *set</pre>
	); /** + Obmied Tauglidetes the action cashs for a set of approximate
	* @brief Invalidates the entire cache for a set of processors * * This function is responsible for performing a data cache
	* invalidate. It invalidates the entire cache for a set of * processors.
	* This operation should not be called from interrupt context.
	* @param[in] setsize The size of the processor set. * @param[in] set The target processor set.
	<pre>*/ void rtems_cache_invalidate_entire_data_processor_set(     const size_t setsize,     const cpu_set_t *set );</pre>
<b>524</b> 1	7 months ago wontfix build Joel Sherrill 6 months ago 6 months ago
mar N	MSYS2 builds appear to ignore tcfg file
b	Building m68k/mrm332 on Linux and MSYS2 to compare results. Builds with all tests on Linux. Multiple build failures on MSYS2. Some appear to be because on MSYS2, tests are being build which are marked as exclude in the .tcfg file. For example, https://git.rtems.org/rtems/tree/bsps/m68k/mrm332/config/mrm332-testsuite.tcfg#n11 says that sdosfsname01 should be excluded but it is being built as shown below:
	<pre>m68k-rtems5-gcc -mcpu=cpu32 -0s -g -fomit-frame-pointer -Wall -Wmissing-prototypes -Wimplicit-function-declaration -Wstrict-prototypes -Wnested- externs -B.//.lib/libbsp/m68k/mr332 -B/home/jrs007/rtems-work/rtems/bsps/m68k/mr332/start -specs bsp_specs -qrtems -L.//.opukit - L/home/jrs007/rtems-work/rtems/bsps/m68k/shared/start -W1,wrap=printf -W1,wrap=puts -W1,wrap=putchar -o fsdosfsname01.exe fsdosfsname01/fsdosfsname01-init.o support/fsdosfsname01-ramdisk_support.o .//.lib/libbsp/m68k/mr332/librtemsbsp.a .//.opukit/librtemscpu.a</pre>
ripti	c:/msys64/home/jrs007/rtems-work/tools/5/bin//lib/gcc/m68k-rtems5/7.3.0///m68k-rtems5/bin/ld.exe: fsdosfsname01.exe section `.text' will not fit in region `rom'
n	c:/msys64/home/jrs007/rtems-work/tools/5/bin//lib/gcc/m68k-rtems5/7.3.0///m68k-rtems5/bin/ld.exe: region `rom' overflowed by 874128 bytes
	collect2.exe: error: ld returned 1 exit status
	<pre>make[5]: *** [Makefile:1910: fsdosfsname01.exe] Error 1</pre>
	Zerenthe and Shid Uk (d) Uk (d) Carden (heis Jahan 17 and the and
	.7 months ago     fixed     lib/dl     Kevin Gordon     Chris Johns     17 months ago       RTL Allows Unloading a Module other Modules Depend Upon (cloned)     Image: closed of the second

	Cloned from #3195					
			-	compiled source files module-0.c an ta dependencies, module-1.o is load	id module-1.c from ticket #3194: ded using dlopen() with both code and data dependencies	s on module-0 which are resolv
	by RTL.		with no code of dat	a dependencies. module-1.0 is load		
escripti on	subsequent call to r	module1Function:	1() in the currently-		urn an error and not unload module-0. This becomes quite s shared_resource_0[ ] and calls module0Function0(), will	
	The erroneous succ	essful unload() o	f module-0 aside, it	appears as though the resources a	are not actually deleted and I believe this ticket is related	to tickets #3192 and #3194.
	Architecture is span	c-leon3 using bot	th the RTEMS 4.11.	1 public release and rtems master (	@f043b9bd3bf25626fb1a311dd7fa041eacc68adc with rter	ms-source-builder
	@55f2d69e9b67cde	23d61375fa34ef	5b0f04a985d.			
<del>#3626</del>		and the second	1	ON3 UT700 hardware.	Sebastian Huber	17 months ago
ummar	17 months ago sigtimedwait() need	fixed led when POSIX	is disabled	Joel Shernii	Sebastian Huber	17 months ago
у						
	When POSIX is disa	ibled, psxmsgq01	does not link. Shou	Ild we enable sigtimedwait() when	POSIX is disabled or disable this test?	
escripti on	`wait_for_sig /home/joel/rt	nal': ems-work/rtems 5/testsuites/p	-testing/rtems/bu	uild-powerpc-ss555-rtems/power	<pre>////powerpc-rtems5/bin/ld: psxmsgq01/psxms ppc- stsuites/psxtests/psxmsgq01/init.c:932: undefine</pre>	
<del>#3629</del>	17 months ago	fixed	tool/rsb	Chris Johns	Chris Johns	3 weeks ago
ummar	Add RSB reporting s			CIIIIS JOIIIIS		5 Weeks ago
у						
	As reported in this e	email https://lists	s.rtems.org/piperma	ail/users/2018-November/032802.h	ntml:	
escripti	which of all t	those config files	of RSB I have to us		coolchain with RTEMS source builder & kernel masters but RM, x86-64). This is pretty frustrating and very disappoin /kernel."	
on	there is no docume	nted way to get a	a configuration repo	rt of an RSB configuration. The doc	cumentation needs to be updated to show how this can be	e done. For example:
	\$ ./source-bu \$ less 5-rtem		ts 5/rtems-sparc			
<b>#3630</b> iummar y	17 months ago Build of rtems-tools	fixed fails with i686-w	arch/arm v64-mingw32	Markus Bernd Moessner	Chris Johns	27 hours ago
у	Hi,					
	I am following					
escripti	https://docs	.rtems.org/branc	hes/master/user/ho	osts/windows.html		
on		-			s with: "unknown host: i686-w64-mingw32".	
					ects a host called "mingw32" or "x86_64-w64-mingw32".	My naive solution would be to
	simply extend the li	ist with "i686-w6	4-mingw32", but I'v	ve just started with RTEMS so I mig	ht have choosen a wrong path in an earlier step.	
<b>#3636</b> ummar	17 months ago Add rtems_schedule	fixed	rtems	Sebastian Huber	Sebastian Huber	17 months ago
y	/lau reemb_benedan	er_get_maximum	_p.101107()			
	The maximum task	priority depends	on the scheduler in	stance. It is a configuration parame	eter. Add a function to get it at runtime.	
	/**					
	*			the specified scheduler insta	ince.	
			Identifier of the nter to a task pr	e scheduler instance. riority value.		
escripti	*		Successful opera			
on	* @retval RT	EMS_INVALID_AD		tion.		
		EMS_INVALID_ID	DRESS The @a pric	ation. prity parameter is @c NULL. er instance identifier.		
			DRESS The @a pric Drvalid schedule eduler_get_maximu	ority parameter is @c NULL. er instance identifier.		
	rtems_id		DRESS The @a pric Invalid schedule eduler_get_maximu duler_id,	ority parameter is @c NULL. er instance identifier.		
	rtems_id	 code rtems_sch sche	DRESS The @a pric Invalid schedule eduler_get_maximu duler_id,	ority parameter is @c NULL. er instance identifier.		
+ <del>3637</del>	rtems_id rtems_task_	 code rtems_sch sche	DRESS The @a pric Invalid schedule eduler_get_maximu duler_id,	ority parameter is @c NULL. er instance identifier.	Sebastian Huber	17 months ago
ummar	<pre>rtems_id   rtems_task_ );</pre>	code rtems_sch sche priority *prio	DRESS The @a pric Invalid schedule eduler_get_maximu duler_id, .rity rtems	prity parameter is @c NULL. pr instance identifier. um_priority(	Sebastian Huber	17 months ago
ummar y	rtems_id rtems_task_ ); 17 months ago Fix rtems_task_rest	code rtems_sche sche priority *prio fixed tart() argument t	DRESS The @a pric Invalid schedule eduler_get_maximu duler_id, rity rtems type	ority parameter is @c NULL. er instance identifier. m_priority( Sebastian Huber	Sebastian Huber s_task_start(). This is a severe issue on 64-bit targets sin	
ummar y	rtems_id rtems_task_ ); 17 months ago Fix rtems_task_rest The argument type values to the task.	code rtems_sche sche priority *prio fixed tart() argument t	DRESS The @a pric Invalid schedule eduler_get_maximu duler_id, rity rtems type	prity parameter is @c NULL. r instance identifier. m_priority( Sebastian Huber ems_task_restart() similar to rtems	s_task_start(). This is a severe issue on 64-bit targets sin	
ummar y escripti on	rtems_id rtems_task_ ); 17 months ago Fix rtems_task_rest The argument type values to the task. 17 months ago	code rtems_sch sche priority *prio fixed tart() argument t must be rtems_t	DRESS The @a prid Invalid schedule eduler_get_maximu duler_id, rity rtems type task_argument in rt	ority parameter is @c NULL. er instance identifier. m_priority( Sebastian Huber		
ummar y escripti on <b>#3649</b> ummar	rtems_id rtems_task_ ); 17 months ago Fix rtems_task_rest The argument type values to the task.	code rtems_sch sche priority *prio fixed tart() argument t must be rtems_t	DRESS The @a prid Invalid schedule eduler_get_maximu duler_id, rity rtems type task_argument in rt	prity parameter is @c NULL. r instance identifier. m_priority( Sebastian Huber ems_task_restart() similar to rtems	s_task_start(). This is a severe issue on 64-bit targets sin	ice it prevents to pass pointer
Summar y Descripti on	rtems_id rtems_task_ ); 17 months ago Fix rtems_task_rest The argument type values to the task. 17 months ago Error with IRC anou	code rtems_sche sche priority *prio fixed tart() argument t must be rtems_t fixed incing in example	DRESS The @a pric Invalid schedule eduler_get_maximu duler_id, rity rtems type task_argument in rte admin es-v2 commits.	prity parameter is @c NULL. r instance identifier. m_priority( Sebastian Huber ems_task_restart() similar to rtems	s_task_start(). This is a severe issue on 64-bit targets sin	ice it prevents to pass pointer
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iummar y onscripti on <b>#3649</b> iummar y esscripti	<pre>rtems_id rtems_task_ rtems_task_ rtems_task_ rest rtems_task_rest rtems_task_rest rtems_task_rest rtems_task_rest rtems_task_rest rtems_task_rest remotes the task. remote: 1: ma remote: 2: up remote: 4: IR</pre>	code rtems_sch sche priority *prio fixed tart() argument t must be rtems_t fixed incing in example il vc@rtems.or date github C	DRESS The @a pric Invalid schedule eduler_get_maximu duler_id, rity rtems type task_argument in rto admin es-v2 commits.	ority parameter is @c NULL. r instance identifier. m_priority( Sebastian Huber ems_task_restart() similar to rtems Joel Sherrill	s_task_start(). This is a severe issue on 64-bit targets sin	ice it prevents to pass pointer
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Summar y Descripti on <b>#3649</b> Summar y Descripti on <b>#3651</b>	<pre>rtems_id rtems_id rtems_task_ ); 17 months ago Fix rtems_task_rest The argument type values to the task. 17 months ago Error with IRC anou remote: 1: ma remote: 2: up remote: 4: IR remote: usage remote: 5: Bu To ssh://joel ced65422 17 months ago</pre>	<pre>code rtems_sche sche priority *prio fixed tart() argument t must be rtems_t fixed incing in example il vc@rtems.or date github c : <file r<br="" with="">ildbot @dispatch.rtem 76a025Å am -&gt; fixed</file></pre>	DRESS The @a pric Invalid schedule eduler_get_maximu duler_id, rity rtems type task_argument in rte admin ees-v2 commits. g ev-list> <repo na<br="">is.org/data/git/ey master doc</repo>	<pre>prity parameter is @c NULL. pr instance identifier.  m_priority(  Sebastian Huber  ems_task_restart() similar to rtems Joel Sherrill  ame without .git&gt; kamples-v2.git</pre>	s_task_start(). This is a severe issue on 64-bit targets sin	tce it prevents to pass pointer 15 months ago
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y scripti on <b>3649</b> immar y scripti on <b>3651</b>	<pre>rtems_id rtems_id rtems_task_ ); 17 months ago Fix rtems_task_rest The argument type values to the task. 17 months ago Error with IRC anou remote: 1: ma remote: 2: up remote: 4: IR remote: usage remote: 5: Bu To ssh://joel ced65422 17 months ago</pre>	<pre>code rtems_sche sche priority *prio fixed tart() argument t must be rtems_t fixed incing in example il vc@rtems.or date github c : <file r<br="" with="">ildbot @dispatch.rtem 76a025Å am -&gt; fixed</file></pre>	DRESS The @a pric Invalid schedule eduler_get_maximu duler_id, rity rtems type task_argument in rte admin ees-v2 commits. g ev-list> <repo na<br="">is.org/data/git/ey master doc</repo>	<pre>prity parameter is @c NULL. pr instance identifier.  m_priority(  Sebastian Huber  ems_task_restart() similar to rtems Joel Sherrill  ame without .git&gt; kamples-v2.git</pre>	s_task_start(). This is a severe issue on 64-bit targets sin	ice it prevents to pass pointer 15 months ago

		control sequent	ce.					
	<argument> \</argument>		E	(102)				
	1	re}, \hyperinde	exformat{\spxpagem}	{183}				
escripti on	The Tex is:           \item[{Architecture\index{Architecture@\spxentry{Architecture} spxpagem}\phantomsection\label{\detokenize{glossary/index:term-architecture}}}]							
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	And the IDX entry		_					
	(\item \spxen	try (Architect)	ire}, \hyperindexic	ormat{\spxpagem}{183}				
<del>#3664</del>	17 months ago	fixed	tool/rsb	Chris Johns	Chris Johns	16 months ago		
y y	RSB config parsing		, slow and this slows th	o config filo parsing				
escripti on								
<b>#3665</b> ummar	17 months ago Add low level ever	fixed t recording infras	lib structure	Sebastian Huber	Sebastian Huber	4 months ago		
У	Add low level ever	t recording infras	structure for system a	nd user defined events. The infras	ructure should be able to record high frequency event	s such as		
	<ul> <li>SMP lock ad</li> </ul>							
	<ul> <li>interrupt er</li> </ul>	try/exit,						
	<ul> <li>thread swit</li> <li>UMA zone a</li> </ul>							
		cket input/outpu	t, etc.					
					n the record buffer, the newest event overwrites the o	oldest event. It should be possible		
					g. to display the system state in real-time.			
	The framework sh	ould support high	-end SMP machines (r	more than 1GHz processor frequer	cy, more than four processors).			
					ugh for high-end production systems. The main issues	are the variable-size buffers and		
	the use of SMP loc	ks for synchroniz	ation. To fix this, the	API would change significantly.				
	Add a new API ins record events.	ead. The implem	entation should use p	er-processor data structures and r	o atomic read-modify-write operations. It is pretty mu	ich a per-processor ring buffer fo		
				9 - 91				
			e of events. Combine	it with periodic uptime events to s	nchronize it with CLOCK_REALTIME.			
	Here is an example	e of the						
	/**							
	* @brief Pr *	oduces a record	i item.					
			event without a ti lata for the item.	me stamp for the item.				
escripti on	*/							
	void rtems_r	ecord_produce (	rtems_record_event	event, rtems_record_data dat	.a );			
	function PowerPC	nachine code gei	nerated by GCC:					
	00000000 <rt< td=""><td>ems_record_prod</td><td>iuce&gt;:</td><td></td><td></td><td></td></rt<>	ems_record_prod	iuce>:					
		) 00 a6 mfr ) 01 46 wr						
	8: 7d 2		spr r9,526					
	c: 7d 5 10: 81 4		sprg r10,0 r10,692(r10)					
	14: 55 2	9 50 2a rl	vinm r9,r9,10,0,21					
		3 1b 78 or a 00 00 lw:	r3,r9,r3 r9,0(r10)					
	20: 80 c	a 00 08 lw:	r6,8(r10)					
	24: 38 e 28: 7d 2							
	2c: 55 2		vinm r9,r9,3,0,28					
	30: 7d 2 34: 90 6	a 4a 14 ado 9 00 48 stu						
	38: 90 8 3c: 7c 2		v r4,76(r9) sync					
	40: 90 e	a 00 00 str	r7,0(r10)					
	44: 7d 0 48: 4e 8	) 01 06 wri ) 00 20 bl:	tee r8					
	Just 19 instruction	s no branches r	o stack frame no ato	mic-read-modify-write just a light	weight synchronization to ensure that the consumer i	reads not half finished items		
<del>‡3666</del>	17 months ago	fixed	lib	Sebastian Huber	Sebastian Huber	16 months ago		
ummar	Add support for C-	+17 std::aligned	d_alloc					
У	In C++17 there is	a std∵aligned a						
escripti			/memory/c/aligned_a	lloc				
on	Unfortunately, it d	pesn't work with	RTEMS currently:					
	https://gcc.gnu.or	g/bugzilla/show_	bug.cgi?id=85904					
	Provide aligned_al	oc() and memali	gn() (as a strong alias	to aligned_alloo()) by RTEMS.				
<del>‡3667</del>	16 months ago	fixed	arch/arm	Sebastian Huber	Sebastian Huber	16 months ago		
ummar y	Support data cach	e disable on ARM	v7-AR					
<del>#3668</del>	16 months ago	fixed	admin	Christian Mauderer	Amar Takhar	4 weeks ago		
ummar	Commit message i	n examples-v2 a	nd libbsd didn't trigge	r a ticket update.				
У	On 19.12.2018 I n	ushed a commit	to the rtems-libbsd wi	th a keyword that should have up	lated a ticket. But the ticket didn't pick up the commit	:		
				91566dda7f52b5eba04df159770b				
escripti on		-		91300000a71320360a040[159//0D	T/ 37 00032120			
011	Ticket: https://dev	_						
				rtems-source-builder. Did I somet		d E en en state e		
12662			doc	Chris Johns	Amar Takhar	15 months ago		
<mark>≠3669</mark> ummar	16 months ago	fixed	Sphinx 1.8.2 and 1.8.3	and the second				

	generate Makefile		1.8. Recent posts indicat	te some changes to support u	inicode indexes via xindy have caused this and the solution be	ing discussed is to use the
	https://github.com		rs ora/issues/AAEA			
				t it contains is important as it	defines what needs to happen.	
				the User Manual (user.tex)		
occrinti				phantomsection\det phinxurl{http://www.waf.i	:okenize{glossary/index:term-waf}}}] \leavevmode .o/}	
escripti on	Our current build u	ses pdflatex	directly and there is an e	error as spxpagem is not defi	ned.	
	If you inspect a ver should be considered	-	ated Makefile the comma	ind latexmk is used. This wr	raps the PDF generation so the correct number of passes are per	formed. Using this tool
	The Makefile con					
	export XINDYC	PTS = -L engl	ish -C utf8 -M sphi	nx.xdy		
	The sphinx.xdy C	ontains the nee	eded spxpagem. I can or	nly conclude sphinx needs to	be built with xindy because the reference is always generated.	
			is xindy is not an avail			
<del>#3670</del>	16 months ago	fixed	examples	Chris Johns	joel@	16 months ago
Summar	· · · · · · · · · · · · · · · · · · ·	deprecated or ol	bsolete RTEMS interface	S		
У	The examples need	to be change to	o use what ever is the n	ewer method of doing someth	ning	
					.c open' is deprecated [-Wdeprecated-declarations]	
	open_entry		tems_blkdev_generic_		copent is deprecated [-wdeprecated-dectatations]	
	In file inclu				_zynq_zedboard/lib/include/rtems/ramdisk.h:17:0,	
		is/rtems/kern _generic_open			ude/rtems/blkdev.h:408:1: note: declared here	
on on		tem/fat_ramdi y: r	.sk/init.c:47:3: warn tems_blkdev_generic_		c_close' is deprecated [-Wdeprecated-declarations]	
			(			
	/opt/work/chr CONFIGURE_TER	is/rtems/kern MIOS_DISABLED	configuration optio			
<del>#3672</del>	16 months ago	fixed	arch/i386	Joel Sherrill		15 months ago
Summar			cache manager changes			
on on	rtems5/c/pc486/lib	/libbsp/i386/pc3	1struction': /home/joel/r 386////////rl	tems-work/rtems-testing/rter	ms5/bin/ld: .//.ib/libbsp/i386/pc386/librtemsbsp.a(cache.o): ns/build-i386-pc486-rtems/i386- 386/////.shared/cache////shared/cache/	
on <b>#3673</b> Summar	rtems5/c/pc486/lib	/libbsp/i386/pc _cache_invalida fixed	nstruction': /home/joel/r 386////////r ate_entire_instruction' arch/arm	tems-work/rtems-testing/rter	ns/build-i386-pc486-rtems/i386-	
on #3673 Summar y	rtems5/c/pc486/lib reference to `_CPU 16 months ago xilinx_zynq_a9_qer This is with POSIX	/libbsp/i386/pc: _cache_invalida <mark>fixed</mark> nu - fails to link disabled.	struction': /home/joel/r 386//./././././rl ate_entire_instruction' arch/arm < psxconfig01	tems-work/rtems-testing/rter tems/c/src/lib/libbsp/i386/pc. Joel Sherrill	ns/build-i386-pc486-rtems/i386- 386//////bsps/i386/shared/cache////shared/cache/ Sebastian Huber	cacheimpl.h:350: undefined
on #3673 Summar y	rtems5/c/pc486/lib reference to `_CPU 16 months ago xilinx_zynq_a9_qer This is with POSIX /home/joel/rtems-v	/libbsp/i386/pc: _cache_invalida <b>fixed</b> nu - fails to link disabled. work/rtems-test nq_a9_qemu/te:	struction': /home/joel/rl 386//.//././. ate_entire_instruction' arch/arm c psxconfig01 ing/rtems/build-arm-xili	tems-work/rtems-testing/rter tems/c/src/lib/libbsp/i386/pc: Joel Sherrill nx_zynq_a9_qemu-rtems/arr	ns/build-i386-pc486-rtems/i386- 386//////bsps/i386/shared/cache////shared/cache/ Sebastian Huber	cacheimpl.h:350: undefined
on #3673 Gummar y Descripti on	rtems5/c/pc486/lib reference to `_CPU 16 months ago xilinx_zynq_a9_qer This is with POSIX /home/joel/rtems-v rtems5/c/xilinx_zyr	/libbsp/i386/pc: _cache_invalida <b>fixed</b> nu - fails to link disabled. work/rtems-test nq_a9_qemu/te:	struction': /home/joel/rl 386//.//././. ate_entire_instruction' arch/arm c psxconfig01 ing/rtems/build-arm-xili	tems-work/rtems-testing/rter tems/c/src/lib/libbsp/i386/pc: Joel Sherrill nx_zynq_a9_qemu-rtems/arr	ns/build-i386-pc486-rtems/i386- 386//////bsps/i386/shared/cache////shared/cache/ Sebastian Huber n-	cacheimpl.h:350: undefined
on #3673 Summar y Descripti on #3674	rtems5/c/pc486/lib reference to `_CPU 16 months ago xilinx_zynq_a9_qer This is with POSIX /home/joel/rtems- rtems5/c/xilinx_zyr Id returned 1 exit s	/libbsp/i386/pc: _cache_invalida fixed nu - fails to link disabled. vork/rtems-test q_a9_qemu/te tatus fixed	Istruction': /home/joel/ri 386//.//./././ ate_entire_instruction' arch/arm c psxconfig01 ing/rtems/build-arm-xili stsuites/psxtests///.	tems-work/rtems-testing/rter tems/c/src/lib/libbsp/i386/pc: Joel Sherrill nx_zynq_a9_qemu-rtems/arr ///.rtems/c/src///tests	ns/build-i386-pc486-rtems/i386- 386/////bsps/i386/shared/cache////shared/cache/ Sebastian Huber n- uites/psxtests/psxconfig01/init.c:499: undefined reference to `t	'cacheimpl.h:350: undefined 16 months ago imer_create' collect2: error
on #3673 Summar y Descripti on Summar y Descripti on	rtems5/c/pc486/lib reference to `_CPU 16 months ago xilinx_zynq_a9_qer This is with POSIX /home/joel/rtems-v rtems5/c/xilinx_zyr Id returned 1 exit s 16 months ago Raspberry Pi Fails t ////// ARM mode//./ `isb' in ARM mode support `isb' in ARI (3ff) after fixup/. fixup gmake[6]: *	/libbsp/i386/pc: _cache_invalida fixed nu - fails to link disabled. work/rtems-test q_a9_qemu/te tatus fixed o Build "tems/c/src/lib/l .///.rtem ///.rtem ///// M mode//././ [cache-v7ar-d	struction': /home/joel/ri 386//././././././ri ate_entire_instruction' arch/arm c psxconfig01 ding/rtems/build-arm-xili stsuites/psxtests//././ arch/arm libbsp/arm/raspberrypi/. s/c/src/lib/libbsp/arm/re //rtems/c/src/lib/libbsp/arm/re //rtems/c/src/lib/libbsp/arm/re //rtems/c/src/lib/libbsp/arm/re //rtems/c/src/lib/libbsp/arm/re //rtems/c/src/lib/libbsp/arm/re	tems-work/rtems-testing/rter tems/c/src/lib/libbsp/i386/pc. Joel Sherrill nx_zynq_a9_qemu-rtems/arr ////rtems/c/src//.tests Joel Sherrill Joel Sherrill ////bsps/arm/share aspberrypi////sps Jarm/raspberrypi////sps Jarm/raspberrypi////	ns/build-i386-pc486-rtems/i386- 386/////.bsps/i386/shared/cache///.shared/cache/ Sebastian Huber n- uites/psxtests/psxconfig01/init.c:499: undefined reference to `t Sebastian Huber d/cache/cache-v7ar-disable-data.S: Assembler messages: d/cache/cache-v7ar-disable-data.S:47: Error: selected processo /arm/shared/cache/cache-v7ar-disable-data.S:53: Error: select //bsps/arm/shared/cache/cache-v7ar-disable-data.S:77: Error: /bsps/arm/shared/cache/cache-v7ar-disable-data.S:92: Error: ir	tacheimpl.h:350: undefined 16 months ago imer_create' collect2: error 16 months ago r does not support `dmb' in ed processor does not suppr : selected processor does not storror: invalid constant valid constant (7fff) after
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The autoconf and a single prefix such as the standard prefix there is a chance parts view this prefix as a production level path, and we prefer to place te place the various versions we need for development. For exan opt/rtems/4.11.1. These are recommendations and the choice o 4.11."	imer_create' collect2: error 16 months ago imer_create' collect2: error 16 months ago r does not support `dmb' in ed processor does not suppor : selected processor does not solution to a support : selected processor does not solution to a support : selected processor does not : selected procesor does not : selected processor do

106/130 The xilinx_zynq_a9_qemu BSP contains a memcpy that is ARM mode code and not THUMB. This can be seen with hello.exe and vlan01.exe in the libbsd examples.

	fromfuture import sys					
	for line in sys ls = line.s	plit()				
	addr =	int(ls[1], 1		d ls[3] == 'FUNC':		
		: & 1 == 0: .nt(ls[7])				
	Command with output	t:				
	\$ arm-rtems5-re	adelf -a `fi	ndname hello	.exe`   python ./arm-thumb.py		
	memcpy					
					the building of the memcpy function. Exam ARM code. The section of the output from	ination with <pre>rtems-exeinfo</pre> shows the code is :
escripti on	\$ rtems-exeinfo	-a`find.	-name hello.exe`			
	is:	. 14				
	GNU AS 2.31.3   arm_exc_in					
	bpabi.S	.peron derudi				
	bsp-start-   cpu asm.S	memcpy.S				
	liblfuncs					
	liblfuncs					
	memchr.S   memcpy-arr	w7a.S				
	start.S   strcmp-arr					
	strlen-arr					
	dhcpcd.c as section	.rel.text.d	hcpcd_handle_hwad	ddr contains a R_ARM_THM_JUMP2	relocation record which requires a venee	stence of this code breaks libdl's loading of er in large memory application as well as bl to
<del>3678</del>	15 months ago	fixed	added but I am no arch/riscv	Sebastian Huber	upport for something that should not happ Jiri Gaisler	5 months ago
ummar	Add RISC-V BSP with	support for the	e grlib			
y 3682	15 months ago	fixed	arch/arm	Sebastian Huber	Sebastian Huber	4 months ago
	Add BCD for Vilipy 70	ng UltraScale?	+ MPSoC platform			
	AUU DSP IUI AIIIIX ZY					
У			or the Cortex-A53 p	rocessors in AArch32 mode. There	are currently no plans to support the Cort	ex-R5 or the AArch64 mode.
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recommended. Note, small data is system wild which means a default size of (a) allows only 8192 8 byte variables. Provide an allocator to manage the available small data memory. IS months ago fixed doc Chris Johns S months ago fixed field for the provide fails to build with python3 Y Secretaring generated-posite-compliance.rst fails with Python3 as reported in https://github.com/sphine-doc/sphin/susce/6021#issuecomment-460/01861 IS months ago fixed lib/dl Chris Johns Chris Johns IS months ago memory withe unlock/lock for sections Y The allocator does no bonour write unlock/a do k for read-only sections as it should. This can used to write protect executable memory. The allocator does no bonour write unlock/a do k for read-only sections as it should. This can used to write protect executable memory. The allocator does no bonour write unlock/lock for sections Y The allocator does no bonour write unlock/and lock for read-only sections as it should. This can used to write protect executable memory. The allocator does no bonour write unlock/and lock for read-only sections as it should. This can used to write protect executable memory. The allocator does no bonour write unlock/and lock for read-only sections as it should. This can used to write protect executable memory. This issue was reported back is 2016 and it aligned through. I am creating a ticket here to track the issue. This issue was reported back is 2016 and it aligned through. I am creating a ticket here to track the issue. This issue was reported back is 2016 and it aligned through. I am creating a ticket here to track the issue. This issue was reported back is 2016 and it aligned through. I am creating a ticket here to track the issue. This issue was reported back is 2016 and it aligned through. I am creating a to cannot be true. This issue does reproceed to the place in same file:	recommended.         Note, small data is system wide which means a default size of (a) allows only 8192 8 byte variables.         Provide an allocator to manage the available small data memory.         Provide an allocator to manage the available small data memory.         V         Generating (generated-polis-compliance.rst) fails with Python3 as reported in         0         Ntres://pithub.com/sphinedor/phin//ssues/6021 Pisuecomment-46701861         Provide and honour write unlock/lock for sections         V         V         Ibid does not honour write unlock/lock for sections         V         V         Ibid loces not honour write unlock/lock for read-only sections as it should. This can used to write protect executable memory.         on         Is months ago       fixed         Is months ago       fixed         ibid incorrectly handles MIPS16hi/lor relocs         ummar       Ibid incorrectly handles MIPS16hi/lor relocs         v       This issue was reported back is 2016 and it slipped through. I am creating a ticket here to track the issue.         on       https://list.rems.org/plemail/users/2016-January/029740.html         P       Coverity ID: 1399706 and 1399714 File: shmopen.c Method: shm_open for first         29       file of af e 0_RDOUNY 1 {         10       11399706 (41 of 1): Logica	escripti	Add PowerPC sdata smaller however the .sdata and .sbss therefore a BSP nee	a and sbss sup e variable is refe regions and se ds a way to defi	pport. The PowerPowerPowerPowerPowerPowerPowerPower	C can support a <b>small data</b> 64K c 16bit offset from the register r1 symbol which is loaded into r1: he linker needs to allocate it. Prov	ntinuous system wide region of memory. Sm ]. This register is offset from the region's ba . Any run-time loaded code with small data ide a way for PowerPC BSPs to add extra me	nall data accesses are faster as the instruction se address by 32K. The linker creates the support needs space in the small data region mory to the small data region.				
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Bescription       The allocator does no honour write unlock and lock for read-only sections as it should. This can used to write protect executable memory.       Image: Standard S	Bescripti on       The allocator does no honour write unlock and lock for read-only sections as it should. This can used to write protect executable memory.       Image: Secription of Section 2014         Secription       15 months ago       fixed       lib/dl       Chris Johns       Chris Johns       25         Secription       This issue was reported back is 2016 and it slipped through. I am creating a ticket here to track the issue.       Image: Secription       This issue was reported back is 2016 and it slipped through. I am creating a ticket here to track the issue.       Image: Secription of fixed       14         Secription       fixed       fs       Joel Sherrill       Gedare Bloom       14         Secription       1399706 and 1399714 File: shmopen.cd Method: shm_open for first       Gedare error_condition: The condition of fig 6 0 cannot be true.       29       if (of fig 6 0 cannot reach this s		libdl does not honou	r write unlock/lo	ock for sections							
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y       This issue was prove back is 2016 and t slipped through. I an creating a ticket here to track the issue.         escription       https://lists.rtems.org/pipermail/users/2016-January/029740.html         information       fixed       fs       Joel Sherrill       Gedare Bloom       14 months ago         information       shm_open has logically unreachable code (Coverity ID: 1399706, 1399714)       V       V         coverity ID: 1399706 and 1399714 File: shmopen.c Method: shm_open for first       Image: Coverity ID: 1399706 and 1399714 File: shmopen.c Method: shm_open for first         289       if (oflag 4 0_RDONX) (       (CID 1399706 (#1 of 1): Logically dead code (DEADCODE))       dead_error_line: Execution cannot reach this statement: flags  = 2U;.       290       flags  = LIPI0_FIAGS_READ;       291       else (       14 months ago       is (oflag 4 0_RDONX) (       CID 1399706 (#1 of 1): Logically dead code (DEADCODE)       dead_error_line: Execution cannot reach this statement: flags  = 2U;.       291       else (       Image: Coverity Convergents.htm#v29811/p10069/fileInstanceId=1530842818defectInstanceId=425580128mergedDefectId=13997068dileStart=18fileEnd=250       Same issue at other place in same file:       Image: Coverity (#1 of 1): Logically dead code (DEADCODE)       dead_error_line: Execution cannot neach this statement: flags = 4;.       199       in f flags; Coverity (#1 of 1): Logically dead code (DEADCODE)       dead_error_line: Execution cannot neach this statement: flags = 4;.       199       in f flags = RTEMS_FERMS_READ;	y       This issue was reported back is 2016 and it slipped through. I am creating a ticket here to track the issue.         escription       https://lists.rtems.org/pipermail/users/2016-January/029740.html         14 months ago       fixed       fs       Joel Sherrill       Gedare Bloom       14         ummar       shm_open has logically unreachable code (Coverity ID: 1399706, 1399714)       Image: State of the state of t	<del>3693</del>	15 months ago	fixed	lib/dl	Chris Johns	Chris Johns	25 hours ago				
This issue was reported back is 2016 and it slipped through. I am creating a ticket here to track the issue.         on       https://lists.tems.org/pipermail/users/2016-January/029740.html         P3694       14 months ago       fxed       fs       Joel Sherrill       Gedare Bloom       14 months ago         ummar       shm_open has logically unreachable code (Coverity ID: 1399706, 1399714)       V       V       V         Coverity ID: 1399706 and 1399714 File: shmopen.c Method: shm_open for first       Image: Coverity ID: 1399706 and 1399714 File: shmopen.c Method: shm_open for first       V         289       if (oflag & 0_RDONLY) {       (Coverity ID: 1399706 (fil of 1): Logically dead code (DEADCODE))       dead_error_inte: Execution cannot reach this statement: flags  = 20;.       220       flags  = LIBIO_FLAGS_BEAD;       221   elae {       V         0n       Same issue at other place in same file:       Image: flags  = flags   fl	This issue was reported back is 2016 and it slipped through. I am creating a ticket here to track the issue.         escription         https://lists.rtems.org/pipermail/users/2016-January/029740.html         Gedare Bloom         14 months ago       fixed       fs       Joel Sherrill       Gedare Bloom       14         ummar       shm_open has logically unreachable code (Coverity ID: 1399706, 1399714)       y       14         v       coverity ID: 1399706 and 1399714 File: shmopen.c Method: shm_open for first       289 if (oflag 6 0_RDONLY ) {       14         (CID 1399706 (#1 of 1): Logically dead code (DEADCODE)       dead_error_line: Execution cannot reach this statement: flags  = 20;.       290 flags  = LIBIO_FLAGS_READ;         291 else {       URL: https://scans.coverity.com/reports.htm#v29811/p10069/fileInstanceId=153084281&defectInstanceId=42558012&mergedDefectId=1399706&fileStart=1&file         00       Same issue at other place in same file:       197 int flags; dead_error_cline: Execution cannot reach this statement: flags = 4;.         198 if (oflag & 0_RDONLY ) {       (CID 1399714 (#1 of 1): Logically dead code (DEADCODE)       dead_error_line: Execution cannot reach this statement: flags = 4;.         198 if (oflag & 0_RDONLY ) {       (CID 1399714 (#1 of 1): Logically dead code (DEADCODE)       dead_error_line: Execution cannot reach this statement: flags = 4;.         198 if (oflag & 0_RDONLY ) {       198 if (oflag & 0_RDONLY ) {       199 flags = RTEMERS;	immar	libdl incorrectly han	dles MIPS16hi/lo	o relocs							
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289 if ( oflag & 0_RDONLY ) {         CID 1399706 (#1 of 1): Logically dead code (DEADCODE)         dead_error_line: Execution cannot reach this statement: flags  = 2U;.         290 flags  = LIBIO_FLAGS_READ;         291 } els {         VRL: https://scan5.coverity.com/reports.htm#v29811/p10069/fileInstanceId=153084281&defectInstanceId=42558012&mergedDefectId=1399706&fileStart=1&fileEnd=250         Same issue at other place in same file:         197 int flags; dead_error_condition: The condition oflag & 0 cannot be true.         198 if ( oflag & 0_RDONLY ) {         CID 1399714 (#1 of 1): Logically dead code (DEADCODE) dead_error_line: Execution cannot reach this statement: flags = 4;.         199 flags = RTEMS_FS_PERMS_READ;         200 } else {         Sebastian Huber         Basic Support for Trace Compass	<pre>289 if ( oflag &amp; 0_RDONLY ) {     CID 1399706 (#1 of 1): Logically dead code (DEADCODE)     dead_error_line: Execution cannot reach this statement: flags  = 2U;.     200 flags  = LIBIO_FLAGS_READ;     291 } else {     URL: https://scan5.coverity.com/reports.htm#v29811/p10069/fileInstanceId=153084281&amp;defectInstanceId=42558012&amp;mergedDefectId=1399706&amp;fileStart=1&amp;file     Same issue at other place in same file:         197 int flags;         dead_error_condition: The condition oflag &amp; 0 cannot be true.         198 if ( oflag &amp; 0_RDONLY ) {         CID 1399714 (#1 of 1): Logically dead code (DEADCODE)         dead_error_line: Execution cannot reach this statement: flags = 4;.         199 flags = RTEMS_TS_TERMS_READ;     } } </pre>											
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200 } else {       #3696       14 months ago       fixed       tool       Sebastian Huber       Sebastian Huber       4 weeks ago       ummar       Basic Support for Trace Compass			CID 1399714 (#1 of 1): Logically dead code (DEADCODE) dead_error_line: Execution cannot reach this statement: flags = 4;.									
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MILLING · · ·	+3696     14 months ago     fixed     tool     Sebastian Huber     4 w	<del>3696</del>	14 months ago	fixed	tool	Sebastian Huber	Sebastian Huber	4 weeks ago				
	ummar Basic Support for Trace Compass		Basic Support for Tr	ace Compass								

The Trace Compass is a tool to analyse and display trace data. Trace data can be gathered from RTEMS applications via various means, for example:

- **RTEMS Trace Linker**
- Event Recording Capture Engine
- The goal of this project is to enable the Trace Compass to analyse and display some basic information using the Event Recording infrastructure. Basic information is defined by the Linux kernel trace support (Ittng) and includes (see Trace Compass project explorer Tracing -> Traces -> Something):

kernel

- Views
   CPU usage
   CPU
  - CPU usage
  - IRQ Analysis
    - IRQ Statistics IRQ Table
    - IRO vs Count
    - IRQ vs Time
  - Linux Kernel
    - Control Flow Resources

Example data can be obtained from the Trace Visualization Labs

Descripti Advanced support for Trace Compass could include dynamic memory traces, stack usage, network packet flow, etc.

There are four main problems.

- 1. Generation of sufficient trace events, currently the interrupt entry/exit events are not available for example.
- 2. The trace data must be transferred from the target system running the RTEMS application to a host computer running the Trace Compass (transfer via TCP is available, for UDP based transfer see #3695).
- 3. The Trace Compass must be able to analyse and display the information obtained from the Event Recording.

4. The RTEMS user must be able to use this infrastructure. This requires that it is easy to use, availability of tutorials and documentation.

To tackle problem 3, there are two approaches possible. You can extend the Trace Compass to work with the trace data provided by RTEMS as is. Alternatively, the RTEMS trace data could be converted to Linux kernel trace data (Ittng) which Trace Compass already understands.

Related topics are Common Trace Format, Babeltrace, barectf, #2961 and #3028.

#### Skills Needed

You need good C and C++ skills with a proven record. You need to show socket level and networking programming skills. In case Trace Compass needs to be extended this requires Java skills and familiarity with the Eclipse framework. Knowledge of YAML and XML is helpful. High end RTEMS targets can generate a huge number of events per second (10MiB/s trace data is 1310720 events per second; on a 4GHz host processor this is 3051 instructions per event under real-time processing conditions) which imposes a considerable work load to modern host computers, so the host programs must work efficiently.

#### Difficulty

	We consider this a	r darancea proj	Ject.			
<del>#3699</del>	14 months ago	fixed	arch/arm	Kinsey Moore	Sebastian Huber	14 months ago
ummar y	Wrong system regi	ster specified fo	or ARM virtual timer valu	e retrieval		
escripti on					pu/arm-cp15.h, the system register specified by or the virtual timer value as used in the setter.	"p15, 0, %[val], c14, c2, 0" is actually th
<del>#3720</del>	14 months ago	fixed	shell	Sebastian Huber	Sebastian Huber	14 months ago
Summar y	mfill shell comman	d uses the wror	ng arguments for the me	mset()		
<del>#3724</del>	14 months ago	fixed	arch/arm	Sebastian Huber	Sebastian Huber	14 months ago
Summar y	bsp/lpc24xx: Conv	ert SSP driver t	o Linux API			
<del>#3725</del>	14 months ago	fixed	arch/arm	Sebastian Huber	Sebastian Huber	12 months ago
Summar y	bsp/lpc24xx: Conv	ert I2C driver to	o Linux API			
Descripti on						
<del>#3728</del>	13 months ago	fixed	bsps	Chris Johns	Chris Johns	13 months ago
У						
Descripti on	These are large me	emory targets th	nat can support libdl. Ma	ke the small data memory the	naximum size.	
on	These are large me 13 months ago	emory targets th	nat can support libdl. Ma	ke the small data memory the solution Sebastian Huber	naximum size. Sebastian Huber	13 months ago
on <b>#3731</b>	<u>,</u>	fixed	rtems			13 months ago
on # <b>3731</b> Summar y	13 months ago Add rtems_schedul Add rtems_schedul	fixed ler_get_process ler_get_process	rtems	Sebastian Huber		: <b>-</b>
on #3731 Summar y Descripti on	13 months ago Add rtems_schedul Add rtems_schedul	fixed ler_get_process ler_get_process	or() as a replacement fo	Sebastian Huber	Sebastian Huber	: <b>-</b>
on #3731 Summar y Descripti on #3732	13 months ago Add rtems_schedul Add rtems_schedul Manager. This is in	fixed ler_get_process line with the gl	rtems sor() sor() as a replacement fo libc sched_getcpu() funct rtems	Sebastian Huber	Sebastian Huber (). The rtems_get_current_processor() is a bit of	rphaned. Adopt it by the Scheduler
#3731 Summar y Descripti on #3732 Summar	13 months ago Add rtems_schedul Add rtems_schedul Manager. This is in 13 months ago Add rtems_schedul Add rtems_schedul	fixed ler_get_process line with the gl fixed ler_get_process ler_get_process t is also mislear	rtems cor() cor() as a replacement for libc sched_getcpu() funct rtems cor_maximum() cor_maximum() as a repl	Sebastian Huber r rtems_get_current_processor ion. Sebastian Huber acement for rtems_get_proces	Sebastian Huber (). The rtems_get_current_processor() is a bit of	rphaned. Adopt it by the Scheduler 13 months ago s a bit orphaned. Adopt it by the Schedule
on #3731 Summar y Descripti on #3732 Summar y Descripti	13 months ago Add rtems_schedul Add rtems_schedul Manager. This is in 13 months ago Add rtems_schedul Add rtems_schedul Manager. The cour	fixed ler_get_process line with the gl fixed ler_get_process ler_get_process t is also mislear	rtems cor() cor() as a replacement for libc sched_getcpu() funct rtems cor_maximum() cor_maximum() as a repl	Sebastian Huber r rtems_get_current_processor ion. Sebastian Huber acement for rtems_get_proces	Sebastian Huber (). The rtems_get_current_processor() is a bit of Sebastian Huber sor_count(). The rtems_get_processor_count() is	rphaned. Adopt it by the Scheduler 13 months ago s a bit orphaned. Adopt it by the Schedule

(gdb) target remote 10.10.5.45:1122
Remote debugging using 10.10.5.45:1122
Truncated register 19 in remote 'g' packet

It looks to me like gdb is now smart enough to know this ARM arch has a NEON and floating point registers:

	(gdb) main					
	Name	Nr H	Rel Of	fset	Size	Type
	r0	0	0	0		uint32_t
	r1	1	1	4	4	uint32_t
	r2	2	2	8	4	uint32_t
	r3	3	3	12	4	uint32_t
	r4	4	4	16	4	uint32_t
	r5	5	5	20	4	uint32_t
	r6	6	6	24	4	uint32 t
	r7	7	7	28	4	uint32_t
	r8	8	8	32	4	uint32 t
	r9	9	9	36	4	uint32_t
	r10	10	10	40	4	uint32 t
	r11	11	11	44	4	uint32 t
Descripti	r12	12	12	48	4	uint32 t
on .	sp	13	13	52	4	*1
011	lr	14	14	56	4	uint32 t
	pc	15	15	60	4	*1
	fO	16	16	64	12	arm ext
	fl	17	17	76	12	arm ext
	f2	18	18	88	12	arm ext
	£3	19	19	100	12	arm ext
	£4	20	20	112	12	arm ext
	f5	21	21	124	12	arm ext
	f6	22	22	136	12	_arm_ext
	£7	23	23	148	12	arm ext
	fps	24	24	160	4	uint32 t
	cpsr	25	25	164	4	uint32_t
	t					

The target support in libdebugger is a simple array of 32bit ints. This needs to change to handle registers at various offsets. The lack of fp regs was a simplification at the time I first implement this server. Loos like I need to sort this out.

<del>#3734</del>	13 months ago	fixed	score	Sebastian Huber	Sebastian Huber	13 months ago
Summar y	Add RTEMS_CONST	attribute				
Descripti on	Add RTEMS_CONST	attribute to m	ake the compiler specific	attribute ((const)) available.		
<del>#3735</del>	13 months ago	fixed	config	Sebastian Huber	Sebastian Huber	4 months ago
Summar y	Remove CONFIGUR	E_HAS_OWN_	MULTIPROCESSING_TAB	E		
				TABLE configuration option. The guration option had a note in the guratic section option had a note in the guratic section option o	ne RTEMS configuration should be done via explicit configuratio the documentation:	n options to allow more
Descripti on	"This is a configurat RTEMS Users mailir		which is very unlikely to	be used by an application. If	you find yourself wanting to use it in an application, please rec	onsider and discuss this on t
	No discussion took	place in a coup	le of years about this top	ic.		
<del>#3736</del>	13 months ago	fixed	arch/powerpc	Chris Johns	Sebastian Huber <sebastian.huber@></sebastian.huber@>	6 months ago
-3/30			ions broken			

Testing master on a Zynq reports:

Running cdtest.exe fails. I am wondering if there is an issue in the linkcmd scripts this BSP uses. The psim works.

110/130

The trace is: config addr is 0xf1000cf8 config data is 0xf1000cfc Welcome to RTEMS rtems-5.0.0 (PowerPC/Generic (classic FPU)/beatnik) CPU: MPC7457 Board Type: MVME5500-0161 (S/N E1712C9) Bus Clock Freq: 133333333 Hz CPU Clock Freq: 1000000000 Hz Memory: 536870912 bytes Now BSP_mem_size = 0x1fe00000 Configuration.work_space_size = a170 Page table setup finished, will activate it NOW... Coing to start PCI buses scanning and initialization Number of PCI buses found is : 3 MSR 0x2003032 Exit from bspstart 
 Display
 Display

 Universe II PCI-VME bridge detected at 0x82000000, IRQ 76

 Universe Master Ports:

 Port VME-Addr Size
 PCI-Adrs Mode:

 0:
 0x20000000 0x00000000 0x90000000 0x32, D64 [MBLT], Dat, Sup

 1:
 0x00000000 0x000f0000 0x9f000000 A24, D64 [MBLT], Dat, Sup

 2:
 0x00000000 0x00010000 0x9ff00000 A26, D64, Dat, Sup
 7: 0x0000000 0x01000000 0x9e000000 CSR, D64, Dat, Sup Universe Slave Ports: PCI-Adrs Ort VME-Addr Size PCI-Adrs Mode: 0: 0x90000000 0x1fe00000 0x00000000 A32, Pgm, Dat, Sup, Usr, PWEN, PRE vmeUniverse IRQ manager: looking for registers on VME... Trying to find CRG on VME... vmeUniverse IRQ manager - registers not found on VME; falling back to PCI +** BEGIN OF TEST CONSTRUCTOR/DESTRUCTOR ***
*** TEST VERSION: 5.0.0.8a8b95aald6932ba9d2acd7a785100f7d0919205-modified
*** TEST STATE: EXPECTED-PASS
*** TEST BUILD: RTEMS_NETWORKING RTEMS_POSIX_API
*** TEST BUILD: RTEMS_NETWORKING RTEMS_POSIX_API
alf952fbde56, Newlib ld35a003f)
GLOBAL: Hey I'm in base class constructor number 1 for 0x5c404.
GLOBAL: Hey I'm in base class constructor number 2 for 0x5c410.
GLOBAL: Hey I'm in base class constructor number 3 for 0x5c410.
LOCAL: Hey I'm in base class constructor number 5 for 0x6cbd0.
LOCAL: Hey I'm in base class constructor number 5 for 0x6cbd0.
LOCAL: Hey I'm in base class constructor number 6 for 0x6cbd0.
LOCAL: Hey I'm in base class constructor number 7 for 0x6cbd0.
LOCAL: Hey I'm in base class constructor number 7 for 0x6cbd8.
LOCAL: Hey I'm in derived class constructor number 7 for 0x6cbd8. *** BEGIN OF TEST CONSTRUCTOR/DESTRUCTOR *** LOCAL: Hey I'm in base class constructor number / for Ux6cb88. LOCAL: Hey I'm in derived class constructor number 8 for 0x6cb88. IO Stream not tested LOCAL: Hey I'm in derived class destructor number 8 for 0x6cb88. Derived class - Instantiation order 8 LOCAL: Hey I'm in base class destructor number 7 for 0x6cb88. Derived class - Instantiation order 8 LOCAL: Hey I'm in base class destructor number 6 for 0x6cbc4. Derived class - Instantiation order 6 LOCAL: Hey I'm in base class destructor number 5 for 0x6cbd0. Derived class - Instantiation order 5 LOCAL: Hey I'm in base class destructor number 4 for 0x6cbdc. Derived class - Instantiation order 5 *** TESTING C++ EXCEPTIONS *** fatal source: RTEMS_FATAL_SOURCE_EXIT
bsp_fatal_extension(): RTEMS terminated

dufault

Chris Johns

fixed

У	libdl loading ELF objects from libbsd N	IFS file system ends	in a deadlock		
	For ELF files the run-time loader calls	this chain:			
Descripti on	<ul> <li>rtems_rtl_elf_file_load()</li> <li>rtems_rtl_alloc_lock()</li> <li>rtems_rtl_alloc_heap()</li> <li>_RTEMS_Lock_allocator()</li> </ul>				
<del>#3742</del>		ap operations. RTL t test	hen calls read() and for NFS file Chris Johns	e systems the NFS threads try to use the heap, locking up the system. joel@	12 months ago
Summar	T_config conflicting type qualifiers for	'config'			
У	Running the rtems-bsp-builder on	FreeBSD is give an	error in ttest01 for bsps:		
	<pre>arm/csb336 arm/csb337 arm/csb637 arm/kit637_v6 mips/csb350</pre>				
	The error being reported is:				
	error: testsuites/libtests/tt	test01/init.c:146	:23: error: conflicting ty	pe qualifiers for 'config'	
escripti	The builder command line is:				
on	<pre>/opt/work/chris/rtems/rt/rtem rtems-tools=/opt/work/rtem rtems=/opt/work/chris/rtem log=everything-tests \ profile=everything \ build=tests \ jobs=7/6</pre>	ns/5 \	-		
	A BSP configure command line is:				
	/opt/work/chris/rtems/kernel/				
	target=mips-rtems5enab enable-testsdisable-sm		50prefix=/opt/rtems/5 \		
<del>#3743</del>	12 months ago fixed	tool/rsb	Chris Johns	Chris Johns	12 months ago
ummar	RSB os and arch config logic is broken	1			
У	The config file processing of conditiona	als:			
	• %ifos				
escripti on	• %ifnos • %ifarch				
	do not correctly process lists of argum split evenly.	ients. The argumen	t list is split in 2 with the first e	element correct handled and the remaining treated as a lump. The argu	ument list needs to be
<del>‡3746</del>	12 months ago fixed	lib/dl	Chris Johns	Chris Johns <chrisj@></chrisj@>	12 months ago
ummar					
У	libdl test dl05.exe failing				
escripti	This test is failing because the second			ection referenced by a symbol has been loaded.	
escripti on	This test is failing because the second It is not clear yet if the lack of support	t in libdl for gro	up sections is a factor.		11 months and
escripti on #3747	This test is failing because the second	t in libdl for gro	up sections is a factor.	ection referenced by a symbol has been loaded. Sebastian Huber	11 months ago
escripti on #3747	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117	t in libdl for gro arch/arm	up sections is a factor. Sebastian Huber	Sebastian Huber	
escripti on <b>#3747</b> Jummar	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it fou	t in libdl for gro arch/arm	up sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra		
escripti on <b>#3747</b> Jummar	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it fou https://www.nxp.com/docs/en/errata/	t in libdl for gro arch/arm Ind that this chip is /ES_LPC177X_8X.p	up sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra	Sebastian Huber ta 602117. NXP didn't bother to document this in their errata sheet for	
Descripti on <b>#3747</b> Summar	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it fou https://www.nxp.com/docs/en/errata/	t in libdl for gro arch/arm and that this chip is /ES_LPC177X_8X.p bile everything with	up sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra df -mfix-cortex-m3-ldrd. This opt	Sebastian Huber	
escripti on <b>#3747</b> Jummar	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it fou https://www.nxp.com/docs/en/errata/ To avoid the issues, you have to comp	t in libdl for gro arch/arm arch/arm ind that this chip is /ES_LPC177X_8X.p bile everything with ultilibs to account for t-rtems b/gcc/con 57 100644	up sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra df -mfix-cortex-m3-ldrd. This opt or this errata. For example:	Sebastian Huber ta 602117. NXP didn't bother to document this in their errata sheet for	
escripti on <b>#3747</b> Summar	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it fou https://www.nxp.com/docs/en/errata/ To avoid the issues, you have to comp I think we have to change our GCC mm diffgit a/gcc/config/arm/t index 026a5895662e276bdf3e5	t in libdl for gro arch/arm arch/arm arch/arm and that this chip is /ES_LPC177X_8X.p bile everything with ultilibs to account for t-rtems b/gcc/con 57 100644	up sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra df -mfix-cortex-m3-ldrd. This opt or this errata. For example:	Sebastian Huber ta 602117. NXP didn't bother to document this in their errata sheet for	
Descripti on <b>#3747</b> Summar	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it four https://www.nxp.com/docs/en/errata/ To avoid the issues, you have to comp I think we have to change our GCC mu diffgit a/gcc/config/arm/t index 026a5895662e276b4f3e5 a/gcc/config/arm/t-rtems +++ b/gcc/config/arm/t-rtems e@ -1, 7 +1, 7 @@ # Custom RTEMS multilibs for MULTILIB_OPTIONS = mbig-end	t in libdl for gro arch/arm arch/arm and that this chip is /ES_LPC177X_8X.p bile everything with ultilibs to account for t-rtems b/gcc/con 57 100644 r ARM dian mthumb march	up sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra df -mfix-cortex-m3-ldrd. This opt or this errata. For example: fig/arm/t=rtems =armv6-m/march=armv7-a/mar	Sebastian Huber ta 602117. NXP didn't bother to document this in their errata sheet for	r the chip:
Descripti on #3747 Summar y	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it fou https://www.nxp.com/docs/en/errata/ To avoid the issues, you have to comp I think we have to change our GCC mu diffgit a/gcc/config/arm/t- index 026a5895662e276b4f3e5 a/gcc/config/arm/t-rtems ege -1,7 +1,7 eg # Custom RTEMS multilibs for -MULTILIB_DITNAMES = mbig-end di6/mfpu=fpv4-sp-dl6/mfpu=fpv -MULTILIB_DITNAMES = eb thum	t in libdl for gro arch/arm arch/arm arch/arm and that this chip is /ES_LPC177X_8X.p bile everything with ultilibs to account for t-rtems b/gcc/con 57 100644 c ARM dian mthumb march v5-d16 mfloat-abio o armv6-m armv7-a	up sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra df -mfix-cortex-m3-Idrd. This opt or this errata. For example: fig/arm/t-rtems =armv6-m/march=armv7-a/mar =hard armv7-r armv7-m cortex-m7	Sebastian Huber         ta 602117. NXP didn't bother to document this in their errata sheet for         ion is enabled by default, if you use -mcpu=cortex-m3.         :ch=armv7-r/march=armv7-m/mcpu=cortex-m7 mfpu=neon/mfpu=vfp/         neon vfp vfpv3-d16 fpv4-sp-d16 fpv5-d16 hard	r the chip:
bescripti on #3747 Summar y	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it fou https://www.nxp.com/docs/en/errata/ To avoid the issues, you have to comp I think we have to change our GCC mu diffgit a/gcc/config/arm/t- index 026a5895662e276b4f3e5 a/gcc/config/arm/t-rtems et + b/gcc/config/arm/t-rtems et -1,7 +1,7 ee # Custom RTEMS multilibs for MULTILIB_OPTIONS = mbig-end dl6/mfpu=fpv4-sp-dl6/mfpu=fpv 	t in libdl for gro arch/arm and that this chip is /ES_LPC177X_8X.p bile everything with ultilibs to account for t=rtems b/gcc/con 57 100644 r ARM dian mthumb march y5-d16 mfloat-abi o army6-m army7-a dian mthumb march 3-d16/mfpu=fpv4-s	<pre>sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra df -mfix-cortex-m3-ldrd. This opt or this errata. For example: fig/arm/t-rtems =armv6-m/march=armv7-a/mar =hard armv7-r armv7-m cortex-m7 =armv6-m/march=armv7-a/mar =armv6-m/march=armv7-a/mar =armv6-m/march=armv7-a/mar</pre>	Sebastian Huber ta 602117. NXP didn't bother to document this in their errata sheet for ion is enabled by default, if you use -mcpu=cortex-m3.	r the chip:
Descripti on #3747 Summar y	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it four https://www.nxp.com/docs/en/errata/ To avoid the issues, you have to comp I think we have to change our GCC mu diffgit a/gcc/config/arm/t-rtems ee -1,7 +1,7 ee # Custom RTEMS multilibs for MULTILIB_OPTIONS = mbig-enc dl6/mfpu=fpv4-sp-dl6/mfpu=vfpv3 +MULTILIB_ORTAMES = eb thum HULTILIB_DIRNAMES = eb thum	t in libdl for gro arch/arm and that this chip is /ES_LPC177X_8X.p bile everything with ultilibs to account for t=rtems b/gcc/con 57 100644 r ARM dian mthumb march y5-d16 mfloat-abi o army6-m army7-a dian mthumb march 3-d16/mfpu=fpv4-s	<pre>sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra df -mfix-cortex-m3-ldrd. This opt or this errata. For example: fig/arm/t-rtems =armv6-m/march=armv7-a/mar =hard armv7-r armv7-m cortex-m7 =armv6-m/march=armv7-a/mar =armv6-m/march=armv7-a/mar =armv6-m/march=armv7-a/mar</pre>	Sebastian Huber         ta 602117. NXP didn't bother to document this in their errata sheet for         ion is enabled by default, if you use -mcpu=cortex-m3.         :ch=armv7-r/march=armv7-m/mcpu=cortex-m7 mfpu=neon/mfpu=vfp/         'neon vfp vfpv3-d16 fpv4-sp-d16 fpv5-d16 hard         'ch=armv7-r/mcpu=cortex-m3/mcpu=cortex-m7	r the chip:
Descripti on #3747 Summar y	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it four https://www.nxp.com/docs/en/errata/ To avoid the issues, you have to comp I think we have to change our GCC mm I think we have to change our GCC mm I think we have to change our GCC mm diffgit a/gcc/config/arm/t-index 026a589562e276b4f3e5 a/gcc/config/arm/t-rtems et +- b/gcc/config/arm/t-rtems et	t in libdl for gro arch/arm arch/arm arch/arm arch/arm arch/arm arch/arm arch/arm arch/arm bile everything with ultilibs to account for t-rtems b/gcc/con 57 100644 t-rtems b/gcc/con 57 100644	y sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra df -mfix-cortex-m3-ldrd. This opt r this errata. For example: fig/arm/t-rtems =armv6-m/march=armv7-a/mar =hard armv7-r armv7-m cortex-m7 =armv6-m/march=armv7-a/mar p-d16/mfpu=fpv5-d16 mf1oat armv7-r cortex-m3 cortex-	Sebastian Huber ta 602117. NXP didn't bother to document this in their errata sheet for ion is enabled by default, if you use -mcpu=cortex-m3. cch=armv7-r/march=armv7-m/mcpu=cortex-m7 mfpu=neon/mfpu=vfp/ r neon vfp vfpv3-d16 fpv4-sp-d16 fpv5-d16 hard cch=armv7-r/mcpu=cortex-m3/mcpu=cortex-m4/mcpu=cortex-m7 -abi=hard m4 cortex-m7 neon vfp vfpv3-d16 fpv4-sp-d16 fpv5-d16 hard	r the chip:
Descripti on #3747 Summar y	This test is failing because the second It is not clear yet if the lack of support 12 months ago fixed Address Cortex-M3 Errata 602117 While testing on a NXP LPC1788 it fou https://www.nxp.com/docs/en/errata/ To avoid the issues, you have to comp I think we have to change our GCC mu diffgit a/gcc/config/arm/t-rtems eq -1,7 +1,7 @e # Custom RTEMS multilbs for MULTILIB_OPTIONS = mbig-enc dl6/mgu=fpv4-sp-dl6/mfgu=fpv4- -MULTILIB_DIRNAMES = eb thumk # Enumeration of multilbs @e -16,7 +16,8 @e MULTILIB_RE MULTILIB_REQUIRED += mthumb/	t in libdl for gro arch/arm arch/arm arch/arm ind that this chip is /ES_LPC177X_8X.p bile everything with ultilibs to account for t-rtems b/gcc/con 57 100644 c ARM dian mthumb march 3-d16/mfpu=fpv4-s b armv6-m armv7-a EQUIRED += mthumb /march=armv7-a	<pre>sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra df -mfix-cortex-m3-Idrd. This opt or this errata. For example: fig/arm/t-rtems =armv6-m/march=armv7-a/mar =hard armv7-r armv7-m cortex-m7 =armv6-m/march=armv7-a/mar p-d16/mfpu=fpv5-d16 mfloat armv7-r cortex-m3 cortex- /march=armv7-a/mfpu=neon/m</pre>	Sebastian Huber         ta 602117. NXP didn't bother to document this in their errata sheet for         ion is enabled by default, if you use -mcpu=cortex-m3.         cch=armv7-r/march=armv7-m/mcpu=cortex-m7 mfpu=neon/mfpu=vfp/         neon vfp vfpv3-d16 fpv4-sp-d16 fpv5-d16 hard         cch=armv7-r/mcpu=cortex-m3/mcpu=cortex-m7         .=abi=hard	r the chip:
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escripti on summar y escripti on escripti on	This test is failing because the second It is not clear yet if the lack of support         12 months ago       fixed         Address Cortex-M3 Errata 602117         While testing on a NXP LPC1788 it fou         https://www.nxp.com/docs/en/errata/         To avoid the issues, you have to comp         I think we have to change our GCC mm         diffgit a/gcc/config/arm/t-rtems         ed -1,7 +1,7 @e         # Custom RTEMS multilibs for         -MULTILIB_OPTIONS = mbig-enc         dl6/mpu=fpv4-sp-dl6/mfpu=fpv         -MULTILIB_OPTIONS = mbig-enc         mfpu=neo/mfpu=vfp/mpu=vfpv         + Enumeration of multilibs         @e -16,7 +16,8 @e MULTILIB_RE         MULTILIB_REQUIRED += mthumb/	t in libdl for gro arch/arm arch/arm arch/arm ind that this chip is /ES_LPC177X_8X.p bile everything with ultilibs to account for t-rtems b/gcc/con 57 100644 r ARM dian mthumb march 57 100644 r ARM dian mthumb march 3-d16 mfloat-abi 0 armv6-m armv7-a dian mthumb march 3-d16/mfpu=fpv4-s 0 armv6-m armv7-a /march=armv7-m/march=armv7-r /march=armv7-r /march=armv7-r /march=armv7-r /march=armv7-r /march=armv7-r /march=armv7-r /march=armv7-m/ /mcpu=cortex-m4/ /mcpu=cortex-m4/ /mcpu=cortex-m4/ /mcpu=cortex-m4/ /mcpu=cortex-m4/ /march=armv7-m/ bject file symbols ching the object file e can have a large lo	y sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra aff -mfix-cortex-m3-ldrd. This opt or this errata. For example: fig/arm/t-rtems =armv6-m/march=armv7-a/mar =hard armv7-r armv7-m cortex-m7 =armv6-m/march=armv7-a/mar p-d16/mfpu=fpv5-d16 mfloat armv7-r cortex-m3 cortex- /march=armv7-a/mfpu=neon/m pu=tpv4-sp-d16/mfloat-abi=ha pu=fpv4-sp-d16/mfloat-abi=ha fpu=fpv5-d16/mfloat-abi=ha chris Johns and searching the global symt scal and global set of symbols a de.	Sebastian Huber         ta 602117. NXP didn't bother to document this in their errata sheet for         ion is enabled by default, if you use -mcpu=cortex-m3.         *ch=armv7-r/march=armv7-m/mcpu=cortex-m7 mfpu=neon/mfpu=vfp/         *neon vfp vfpv3-d16 fpv4-sp-d16 fpv5-d16 hard         *ch=armv7-r/mcpu=cortex-m3/mcpu=cortex-m4/mcpu=cortex-m7         *abi=hard         *float-abi=hard         *rd         *hard         =hard         rrd         bolt table. Currently the object file search is linear and the global table search this can slow the loading process. This issue does not show up for	nfpu=vfpv3- 11 months ago search uses a hash small object files with
Descripti on #3747 Summar y Descripti on	This test is failing because the second It is not clear yet if the lack of support         12 months ago       fixed         Address Cortex-M3 Errata 602117         While testing on a NXP LPC1788 it fou         https://www.nxp.com/docs/en/errata/         To avoid the issues, you have to comp         I think we have to change our GCC mm         diffgit a/gcc/config/arm/t-rtems         ed -1,7 +1,7 @e         # Custom RTEMS multilibs for         -MULTILIB_OPTIONS = mbig-enc         dl6/mpu=fpv4-sp-dl6/mfpu=fpv         -MULTILIB_OPTIONS = mbig-enc         mfpu=neo/mfpu=vfp/mpu=vfpv         + Enumeration of multilibs         @e -16,7 +16,8 @e MULTILIB_RE         MULTILIB_REQUIRED += mthumb/	t in [libd] for gro arch/arm arch/arm arch/arm arch/arm arch/arm arch/arm arch/arm arch/arm arch/arm arch/arm arch/arm arch/armv7-a armv6-m armv7-a dian mthumb march 3-d16/mfpu=fpv4-s o armv6-m armv7-a dian mthumb march 3-d16/mfpu=fpv4-s a armv6-m armv7-a dian mthumb march 3-d16/mfpu=fpv4-s a armv6-m armv7-a march=armv7-r march=armv7-r march=armv7-r march=armv7-r fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=armv7-m fmarch=ar	y sections is a factor. Sebastian Huber affected by the Cortex-M3 Erra df -mfix-cortex-m3-ldrd. This opt or this errata. For example: fig/arm/t=rtems =armv6-m/march=armv7-a/mar =hard armv7-r armv7-m cortex-m7 =armv6-m/march=armv7-a/mar p=d16/mfpu=fpv5-d16 mf1oat armv7-r cortex-m3 cortex- /march=armv7-a/mfpu=neon/m pu=tpv4-sp-d16/mf1oat-abi=ha pu=fpv4-sp-d16/mf1oat-abi=ha fpu=fpv4-sp-d16/mf1oat-abi=ha fpu=fpv5-d16/mf1oat-abi=ha and searching the global symt becal and global set of symbols a de. bsearch). A hash table for ea	Sebastian Huber         ta 602117. NXP didn't bother to document this in their errata sheet for         ion is enabled by default, if you use -mcpu=cortex-m3.         cch=armv7-r/march=armv7-m/mcpu=cortex-m7 mfpu=neon/mfpu=vfp/         / neon vfp vfpv3-d16 fpv4-sp-d16 fpv5-d16 hard         rch=armv7-r/mcpu=cortex-m3/mcpu=cortex-m4/mcpu=cortex-m7         -abi=hard         m4 cortex-m7 neon vfp vfpv3-d16 fpv4-sp-d16 fpv5-d16 hard         ifloat-abi=hard         ird         chris Johns <chrisj@>         pol table. Currently the object file search is linear and the global table search this can slow the loading process. This issue does not show up for         ch object file would increase the in memory object file footprint by a s</chrisj@>	nfpu=vfpv3- 11 months ago search uses a hash small object files with

Summar v	No documentation	No documentation on Region Get Information Directives							
) Descripti on	rtems_region_get_ documented.	information and	rtems_region_get_fre	e_information are not documen	ted in the Classic API Users Guide. They have been present sir	nce at least 4.6 and should be			
<del>#3753</del>	11 months ago	fixed	config	Joel Sherrill	Sebastian Huber <sebastian.huber@></sebastian.huber@>	4 months ago			
Summar y	Rename CONFIGUE	RE_LIBIO_MAXI	MUM_FILE_DESCRIPTO	RS					
)escripti on	This ticket is to dis	cuss renaming (	CONFIGURE_LIBIO_MA	,	s that is an internal component/organization aid which should just CONFIGURE_MAXIMUM_FILE_DESCRIPTORS. ip old name to new.	not be visible to the user.			
#3754	11 months ago	fixed	doc	Joel Sherrill	Joel Sherrill	9 months ago			
Summar y	Users Guide Ubunti	u Instructions H	ave Typo						
Descripti on	sudu should be sud	lo							
<del>#3756</del>	11 months ago	fixed	arch/sparc	Sebastian Huber	Sebastian Huber	11 months ago			
Summar y	Condition codes in	PSR are destroy	ed by lazy FP context s	switch					
Descripti on	https://lists.rtems.	org/pipermail/d	evel/2019-June/02601	4.html					
<del>#3760</del>	11 months ago	fixed	arch/arm	Chris Johns	Chris Johns <chrisj@></chrisj@>	9 months ago			
Summar y	BBB MMU update c	rashes							
Descripti on	BBB's A8. The cpl A check of the A8 c be using it on the 2 While looking at thi because we may be	5 register is do loco from ARM s Zynq. I have no is code I was wo e asked to invali	cumented in the A8 ma ays this is for use in H idea why the Zync (A9 ondering why we do no	anual but the BBB device from T YP mode so should we be using ) does not complain, it may be t follow ARM's recommendation text section we are running in to	of 'break-make' updates of the TLB? I do not know we could s	also wondering if we use shou			
<del>#3762</del>	10 months ago	fixed	arch/arm	Chris Johns	Chris Johns	10 months ago			
Summar y	Return the current	handler from Al	RM cp15 set exception	call					
Descripti on	hardware.		_ • _		r. This lets code catch and return an exception handler, for ex	ample code to probe suspect			
<del>#3763</del>	10 months ago	fixed	tool/rsb	r debug v7 implementations. Chris Johns	Chris Johns <chrisj@></chrisj@>	9 months ago			
Summar y	RSB SIS build fails		000/130			9 months ago			

THe USE SUB Sub for RESC bis out FreeSo whit: CTLAGS="0.0 -pipe -Encoder depth=1024 -1/opt/work/chris/rems/rmb/rems-ource-builder.git/rems/huid/tmp/ab-chris/S/rems- for ADD - pipe -Encoder depth=1024 -1/opt/work/chris/rems/rmb/rems-ource-builder.git/rems/huid/tmp/ab-chris/S/rems-for ADD - pipe - for ADD - p

Descripti on

**#3768** 9 months ago fixed build

Chris Johns

Chris Johns

9 months ago

Summar Add staging support to Makefile.inc

114/130

<del>#3769</del>	9 months ago	fixed	tool/rsb	Chris Johns	Chris Johns	9 months ago		
ummar	RSB BSP Buildsets		1001/130	CIIIIS JOIIIIS		5 months ago		
У	Add support to the	DSB for BSD b	uild cots. The support in	ncludes building 3rd party pack	ages for a RSD			
	1. Add BSP bui		and sets. The support in	icidues building 5rd party pack	ages for a bor.			
escripti on	<ol><li>Build package</li></ol>	ges for a BSP						
	<ol> <li>Stage builds</li> <li>Fix packages</li> </ol>			endent packages and be built b	efore a package and used			
<del>‡3770</del>	9 months ago	wontfix	tool/rsb	Chris Johns		5 months ago		
ummar	RSB 3rd party pack							
У	The following pack:	ages do not bui	ld or have issues and w	vill be removed if they are not (	undated.			
	1. ntp	igeo do noc ba						
	2. microwindov	NS						
escripti on	3. nxlib 4. lwip							
					Makefile does not support DESTDIR and cannot			
					package installs directly to the prefix which is no	ot suppose to happen.		
+ <del>3773</del>		need to be fixed	d or removed before 5.1 arch/arm	<ol> <li>There is no point releasing th Chris Johns</li> </ol>	ne RSB with packages that are broken.	9 months ago		
ummar	RPi fails to boot	inted	ar chy ar nh					
У	The PDi RSD fails to	boot with the	current master A boos	t of the rope chows the failure	appears after this change [hdoc62c4/rtoms]			
escripti on	THE REI DOP THIS TO	, boot with the	Current Hidstef. A DSeC		appears after this change [bdec62c4/rtems].			
	9 months ago	fixed	arch/arm	Chris Johns	Sebastian Huber <sebastian.huber@< td=""><td>۵&gt; 9 months ago</td></sebastian.huber@<>	۵> 9 months ago		
ummar v	RPi2 SMP does not	build						
			mr7_a_mthumb	-noon _mfloot abi bard		data_postions ffurst/t/		
	-fdata-sectio	ons -Wall -Wm	issing-prototypes -W	Vimplicit-function-declarat	une=cortex-a7 -01 -g -ffunction-sections -: tion -Wstrict-prototypes -Wnested-externs -	-		
					ms.git/bsps/arm/raspberrypi/start -specs bs rap=printf -Wl,wrap=puts -Wl,wrap=putch			
			lloc.o .///cpuki u.a .///cpukit/l		.//lib/libbsp/arm/raspberrypi/librter	nsbsp.a		
	/opt/work/rte `VECTOR RAM'	ms/5/lib/gcc	/arm-rtems5/7.4.0/	.///arm-rtems5/bin/	ld: calloc.norun.exe section `.rtemsstack'	will not fit in region		
escripti on	/opt/work/rte		/arm-rtems5/7.4.0/ 00000040,00000000000		ld: section .start VMA [000000000008000,00	000000000085ff] overlaps section		
on					ld: region `VECTOR_RAM' overflowed by 11475	2 bytes		
	/opt/work/rtems/5/lib/gcc/arm-rtems5/7.4.0///arm-rtems5/bin/ld: region `VECTOR_RAM' overflowed by 114752 bytes							
		ems/5/lib/gcc	/afm=ftems5/7.4.0/					
			/arm=rcems5/7.4.0/			► In the second		
	Configured with	ris/rtems/ker	nel/rtems.git/confic	guretarget=arm-rtems5	-prefix=/opt/work/chris/rtems/kernel/5d:	·		
	Configured with	ris/rtems/ker	nel/rtems.git/confic					
	Configured with /opt/work/chr maintainer-mc 9 months ago	ris/rtems/ker deenable- fixed	nel/rtems.git/confic rtems-debugenable lib/dl	guretarget=arm-rtems5				
	Configured with	ris/rtems/ker deenable- fixed	nel/rtems.git/confic rtems-debugenable lib/dl	guretarget=arm-rtems5 s-testsenable-rtemsbsp=:	raspberrypi2enable-smp	Isable-networkingenable-		
ummar y Jescripti	Configured with /opt/work/chr maintainer-mo 9 months ago libdl does not hand	ris/rtems/ker odeenable- fixed We ARM mode re	nel/rtems.git/config rtems-debugenable lib/dl eloc tramp parsing	uretarget=arm-rtems5 s-testsenable-rtemsbsp=: Chris Johns	raspberrypi2enable-smp	sable-networkingenable- 9 months ago		
ummar y escripti on	Configured with /opt/work/chr maintainer-mc 9 months ago libdl does not hand The BBB fails on lib	ris/rtems/ker odeenable- fixed We ARM mode re	nel/rtems.git/config rtems-debugenable lib/dl eloc tramp parsing	uretarget=arm-rtems5 s-testsenable-rtemsbsp=: Chris Johns	raspberrypi2enable-smp Chris Johns	sable-networkingenable- 9 months ago		
ummar y escripti on <b>#3776</b> ummar	Configured with /opt/work/chr maintainer-mc 9 months ago libdl does not hand The BBB fails on lib	ris/rtems/ker enable- fixed le ARM mode r odl tests becaus fixed	nel/rtems.git/config rtems-debugenable lib/dl eloc tramp parsing se the trampoline parsir lib/dl	guretarget=arm-rtems5 -testsenable-rtemsbsp=: Chris Johns ng of reloc records does not ha	Chris Johns Chris relocs when the code is built in	Isable-networkingenable- 9 months ago ARM mode.		
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# https://devel.rtems.org/wiki/Release//5/5.1

Summar y	MSYS2 RSB build e					
/	I tried to update th	e compiler to RSE	master, and enco	untered an error. command line:		
	/source-bu	uilder/sb-set-build	erdry-runwith	-download 5/rtems-arm		
escripti	error messgae:					
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<del>#3785</del>		fixed	arch/riscv	pragnesh		12 days ago
ummar	Add RISC-V BSP w	th support for the	Freedom E310 Art	ty A7 FPGA		
у <b>#3789</b>	8 months ago	fixed	arch/arm	Andreas Werner	Sebastian Huber	4 months ago
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У						
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	Modified GCC, binu Endian Support as			ed compiler build with RTEMS Sol	rce Builder master(see patches for RTEMS Souc	re builder) I need a ARM Compiler with B
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	Bug Fix					
<del>#3792</del>	8 months ago	fixed	admin	defined( <u>ARM_ARCH_7R</u> ) see patc Jeff Mayes	Chris Johns	8 months ago
ummar	RSB fails to build o	n MSYS2				
У					s://docs.rtems.org/branches/master/user/hosts	
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<del>#3793</del>	5	fixed	tool	Chris Johns		5 months ago
Summar y	trace record tool do	bes not build on w	lindows			
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<del>#3794</del>	8 months ago	fixed	posix	Joel Sherrill	Joel Sherrill	7 months ago
	Initial DOCIV Signa	IS MASK INCOMECU				
Summar	Initial POSIX Signa					

RTEMS initial signal mask for the "process" does not match the behavior of Linux, FreeBSD, and Cygwin.

116/130

There are some subtle rules which need to be followed for the value of the created thread's signal mask. Because signals are part of C99 and enhanced by POSIX, both Classic API tasks and POSIX threads have to have them enabled.

- 1. Internal system threads should have no signals enabled. They have no business executing user signal handlers -- especially IDLE.
- 2. The initial signal mask for other threads needs to follow the implication of a pure C99 environment which only has the methods raise() and signal(). This implies that all signals are unmasked until the thread explicitly uses a POSIX methods to block some. This applies to both Classic tasks and POSIX threads created as initalization tasks/threads (e.g. before the system is up).
- 3. After the initial threads are created, the signal mask should be inherited from the creator. This can be done based on system state.

RTEMS behavior was incorrect by blocking all signals initially and for Classic API tasks.

#### Notes

on

- The default signal mask does not matter for any application that does not use POSIX signals.
- It is assumed that Classic API tasks should provide a compliant C run-time environment. Hence the defalt signal mask state matters

to process. If there is concern about which thread may process it, then it should be blocked in all threads that are not intended to process it. The following code can be used to block all signals. This method can be used in the initialization task/thread to mimic historical behavior:

	static void	block all sign	als(void)								
	{ int										
	sigset_t	sc; mask;									
	ec = eiafi	lleat ( smack )									
	<pre>sc = sigfillset( &amp;mask ); // check sc == 0</pre>										
	sc = pthre	ad sigmask( ST	G BLOCK, &mask, NUI	(.T. ):							
	// check s		o_22200nt, amaon, nor	··· //							
	[}										
<del>3796</del>	8 months ago	fixed	doc	Gedare Bloom	Joel Sherrill <joel@></joel@>	5 months ago					
nmar	docs/develenv dire										
у											
cripti		cture described ir	n the Development En	vironment Guide is outdated an	d does not reflect changes made in relocating BSPs	to the bsps/ directory and refactoring					
on	include paths.	<b>C</b> 1									
<del>797</del>	8 months ago	fixed	tool/rsb	Chris Johns	Chris Johns	5 months ago					
mmar y	Add LLVM as a pao	LNUYE									
scripti	Add 5/rtems-llv	m to build LLVM	for supported hosts.								
on				trace changes have created by	using LLVM symbol/dwarf support.						
<del>3798</del>	7 months ago	fixed	network/libbsd		Sebastian Huber	4 months ago					
mmar	Add sockatmark to		networky hobod								
у											
scripti			katmark() is the only r	method called out by any of the	four POSIX profiles in the FACE Technical Standard	(http://opengroup.org/face) which is					
on	provided by rtems										
<del>3802</del>	6 months ago	fixed	tool/rsb	Joel Sherrill	Hesham Almatary	5 months ago					
mmar y	RSB Build of Spike	2 Fails on Second	TIme (bug in upstrea	m spike)							
scripti	See https://aithub	.com/riscv/riscv-	-isa-sim/issues/348								
on				e RSB. Until then, use the attach	and natch						
3803	6 months ago	invalid	tool/rsb	Joel Sherrill	Chris Johns	3 weeks ago					
mmar	RSB ssl context er	ror fetching qem									
у											
	It looks like there	is a bug in the co	ode that fetches source	e/patches. Jiri's site is a simple	https:						
				ce-builder/bare/patches							
			qemu/0001-LEON3-Add A-plug-play.patch	i-emulation-of-AMBA-plug-pl	ay.patch -> /home/joel/rtems-work/rtems-sou	rce-builder/bare/patches/0001-					
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on	LEON3-Add-em download: (f play.patch download: ht download: ht error: downl This is a build with version=5 # variant="- time/so trace log=1 prefi devel/q 6 months ago	<pre>full) https://g. full) https://g. itps://gaisler coading https:// n the following sci couverture" purce-builder/sl fixed fixed</pre>	org/qemu/0001-LEON: org/qemu/0001-LEON: /gaisler.org/qemu/0 ript: b-set-builder \ us-work/tools/\${vers	3-Add-emulation-of-AMBA-plu 3-Add-emulation-of-AMBA-plu 0001-LEON3-Add-emulation-of sion} \ Joel Sherrill	g-play.patch -> patches/0001-LEON3-Add-emul. g-play.patch: error: <urlopen [errno<br="" error="">-AMBA-plug-play.patch: all paths have failed</urlopen>	-2] Name or service not known> d, giving up					

	This occurred on CentOS 7 with Python 2.7.5 and 3.6.
	<pre>\$/source-builder/sb-get-sources bare/gemu RTEMS Source Builder - Get Sources, 5 (5ecf0181b494)</pre>
	Traceback (most recent call last): File "/source-builder/sb/cmd-get-sources.py", line 26, in <module></module>
	getsources.run() File "/home/joel/rtems-work/rtems-source-builder/source-builder/sb/getsources.py", line 631, in run
Descripti	<pre>opts = load_options(args, argopts) File "/home/joel/rtems-work/rtems-source-builder/source-builder/sb/getsources.py", line 588, in load options</pre>
on	opts = options(argv, argopts, defaults)
	<pre>File "/home/joel/rtems-work/rtems-source-builder/source-builder/sb/getsources.py", line 167, ininit self.sb_git()</pre>
	File "/home/joel/rtems-work/rtems-source-builder/source-builder/sb/getsources.py", line 276, in sb_git if repo_mail is not None:
	UnboundLocalError: local variable 'repo_mail' referenced before assignment
<b>#3805</b> Summar	6 months ago invalid lib/debugger Joel Sherrill Chris Johns 6 months ago libdebugger build error on atsamv
у	
	This was caught in a build sweep using rtems-bsp-builder.
	Configure command:
	<pre>/home/joel/rtems-cron-5/rtems/configuretarget=arm-rtems5enable-rtemsbsp=atsamvprefix=/home/joel/rtems-cron-5/tools/5/bspsenable-rtems- debugdisable-smp</pre>
	Compiler output:
	arm-rtems5-gccpipe -DHAVE CONFIG H -II/home/joel/rtems-cron-5/b-atsam/arm-rtems5/c/atsamv/include -I/home/joel/rtems-cron-
	<pre>S/rtems/cpukit/include = I/home/joel/rtems-cron-5/rtems/pukit/score/cpu/arm/include = I/home/joel/rtems-cron-5/rtems/cpukit/libnetworking -mthumb -mcpu=cortex-m7 -mfpu=fpv5-dl6 -mfloat-abi=hard -O2 -g -ffunction-sections -fdata-sections -Wall -Wmissing-prototypes -Wimplicit-function- declaration -Wstrict-prototypes -Wnested-externs -MT libdebugger/rtems-debugger-arm.o -MD -MP -MF \$depbase.Tpo -c -o libdebugger/rtems-debugger- arm.o /home/joel/rtems-cron-5/rtems/c/src//.cpukit/libdebugger/rtems-debugger-arm.c &amp;&amp;\ mv -f \$depbase.Tpo \$depbase.Po</pre>
	<pre>mv =1 sdepbase.rp0 sdepbas</pre>
	/home/joel/rtems-cron-5/rtems/c/src///cpukit/libdebugger/rtems-debugger-arm.c: In function 'arm_debug_unlock_abort': /home/joel/rtems-cron-5/rtems/c/src///cpukit/libdebugger/rtems-debugger-arm.c:1593:53: error: 'arm_switch_reg' undeclared (first use in this
	function) #define EXCEPTION_ENTRY_EXC() (void) arm_switch_reg
	<pre>/home/joel/rtems-cron-5/rtems/c/src//./cpukit/libdebugger/rtems-debugger-arm.c:1609:3: note: in expansion of macro 'EXCEPTION_ENTRY_EXC' EXCEPTION_ENTRY_EXC(); ^^~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~</pre>
	<pre>/home/joel/rtems-cron-5/rtems/c/src//.cpukit/libdebugger/rtems-debugger-arm.c:1593:53: note: each undeclared identifier is reported only once for each function it appears in #define EXCEPTION_ENTRY_EXC() (void) arm_switch_reg</pre>
	/home/joel/rtems-cron-5/rtems/c/src///cpukit/libdebugger/rtems-debugger-arm.c:1609:3: note: in expansion of macro 'EXCEPTION_ENTRY_EXC' EXCEPTION_ENTRY_EXC();
escripti	/home/joel/rtems-cron-5/rtems/c/src//./cpukit/libdebugger/rtems-debugger-arm.c:1607:24: warning: variable 'frame' set but not used [-Wunused- but-set-variable] CPU_Exception_frame* frame;
on	/home/joel/rtems-cron-5/rtems/c/src//./cpukit/libdebugger/rtems-debugger-arm.c: In function 'target_exception_undefined_instruction': /home/joel/rtems-cron-5/rtems/c/src///cpukit/libdebugger/rtems-debugger-arm.c:1593:53: error: 'arm_switch_reg' undeclared (first use in this function)
	<pre>#define EXCEPTION_ENTRY_EXC() (void) arm_switch_reg</pre>
	<pre>/home/joel/rtems-cron-5/rtems/c/src//./cpukit/libdebugger/rtems-debugger-arm.c:1619:3: note: in expansion of macro 'EXCEPTION_ENTRY_EXC' EXCEPTION_ENTRY_EXC(); ^</pre>
	<pre>/home/joel/rtems-cron-5/rtems/c/src///cpukit/libdebugger/rtems-debugger-arm.c: In function 'target_exception_supervisor_call': /home/joel/rtems-cron-5/rtems/c/src///cpukit/libdebugger/rtems-debugger-arm.c:1593:53: error: 'arm_switch_reg' undeclared (first use in this function)</pre>
	<pre>#define EXCEPTION_ENTRY_EXC() (void) arm_switch_reg</pre>
	<pre>/home/joel/rtems-cron-5/rtems/c/src//.cpukit/libdebugger/rtems-debugger-arm.c:1642:3: note: in expansion of macro 'EXCEPTION_ENTRY_EXC' EXCEPTION_ENTRY_EXC();</pre>
	<pre>/home/joel/rtems-cron-5/rtems/c/src//./cpukit/libdebugger/rtems-debugger-arm.c: In function 'target_exception_prefetch_abort': /home/joel/rtems-cron-5/rtems/c/src//.cpukit/libdebugger/rtems-debugger-arm.c:1593:53: error: 'arm_switch_reg' undeclared (first use in this function)</pre>
	#define EXCEPTION_ENTRY_EXC() (void) arm_switch_reg
	/home/joel/rtems-cron-5/rtems/c/src//./cpukit/libdebugger/rtems-debugger-arm.c:1659:3: note: in expansion of macro 'EXCEPTION_ENTRY_EXC' EXCEPTION_ENTRY_EXC();
	/home/joel/rtems-cron-5/rtems/c/src//./cpukit/libdebugger/rtems-debugger-arm.c: In function 'target_exception_data_abort': /home/joel/rtems-cron-5/rtems/c/src///cpukit/libdebugger/rtems-debugger-arm.c:1593:53: error: 'arm_switch_reg' undeclared (first use in this function)
	<pre>#define EXCEPTION_ENTRY_EXC() (void) arm_switch_reg /home/joel/rtems-cron-5/rtems/c/src///cpukit/libdebugger/rtems-debugger-arm.c:1676:3: note: in expansion of macro 'EXCEPTION_ENTRY_EXC'</pre>
	EXCEPTION_ENTRY_EXC(); ^
	[-Wunned-function] arm_debug_unlock_abort (void)
	6 months ago fixed score Sebastian Huber 6 months ago Add fatal error for heap errors
У	Currently, the following fatal error is generate in case of heap errors:
	static void Heap Protection block error default (
	Heap_Control *heap,
Descripti	Heap_Block *block ) ,
on	{ /* FIXME */
	Terminate( INTERNAL ERROR CORE, 0xdeadbeef );

	6 months ago	fixed	tool/rsb	Chris Johns	Joel Sherrill	5 months ago
Summar	Fix gemu-couvert	ure-git RSB dow	nload file name			
У	The file areas for		, sit is a sit bash. This			
					ce directory that has no meaning	
	https://ftp.rtems.	org/pub/rtems/r	eleases/5/5.0.0/5.0.0	m1911/sources/		
	The release build	output shows the	e issue			
Descripti on				6bc5deae71-x86_64-pc-solaris		
			com/AdaCore/qemu/ar 39922f4e7672b6bc5de		2f4e7672b6bc5deae71.tar.gz ->	
	redirect: h	ttps://codeloa	ad.github.com/AdaCo	re/qemu/tar.gz/e9299f7591c8e	ecf3389922f4e7672b6bc5deae71	
	downloading:	sources/e929	917591c8ec133899221	4e7672b6bc5deae71.tar.gz - (	).0 bytes	
<del>#3809</del>	6 months ago	fixed	tool/rsb	Chris Johns	Joel Sherrill <joel@></joel@>	5 months ago
Summar	Fix epiphany-rtem	is5-gdb-7.8 RSB	download file name			
У	The file areas for			This was the in a file in a value of		
					e source directory that has no meaning	
	https://ftp	rtems.org/pub/r	tems/releases/5/5.0.0	)/5.0.0-m1911/sources/		
Descripti	The release build	output shows the	e issue			
on					096c7c42e6b2781946acbab15 <see log=""> -&gt;</see>	
			946acbab153a481ce3 ad.github.com/adapt		p/f05996c7c42e6b2781946ac <see log=""></see>	
				bab153a481ce3fd0b.zip - 0.0		
<del>#3810</del>	6 months ago	fixed	doc	Chris Johns	Chris Johns	23 hours ago
Summar	Use the release de			CITIES JOINIS	Chills Johns	25 Hours ago
y y	ose the release a					
Descripti	The docs in a rele	ase have the has	sh in the version, it sh	ould be the version number with	the snapshot details.	
on						
42044	6 months ago	fixed	admin	Chris Johns	Chris Johns <chrisj@></chrisj@>	6 months ago
#3811	6 months ago	lixeu	aumm	CITIES JOINIS	chino sonno kennoj@s	6 months ago
Summar	Release source pa					6 months ago
Summar y	Release source pa	th on ftp.rtems.	org is wrong			
Summar y Descripti	Release source pa	th on ftp.rtems.	org is wrong		ps://ftp.rtems.org/pub/rtems/releases/5/5.0.0-m19	
Summar y Descripti on	Release source pa	th on ftp.rtems.	org is wrong			11/sources
Summar y Descripti on #3812	Release source pa	th on ftp.rtems. ce directory in th fixed	org is wrong ne release snapshot is admin	wrong. The RSB is fetching htt	ps://ftp.rtems.org/pub/rtems/releases/5/5.0.0-m19	
Summar y Descripti on #3812	Release source pa The released sour 6 months ago	th on ftp.rtems. ce directory in th fixed	org is wrong ne release snapshot is admin	wrong. The RSB is fetching htt	ps://ftp.rtems.org/pub/rtems/releases/5/5.0.0-m19	11/sources
Summar y Descripti on <b>#3812</b> Summar	Release source pa The released sour 6 months ago Released RSB has	th on ftp.rtems. ce directory in th fixed no source set fo	org is wrong ne release snapshot is admin	wrong. The RSB is fetching htt Chris Johns	ps://ftp.rtems.org/pub/rtems/releases/5/5.0.0-m19	11/sources
Summar y Descripti on <b>#3812</b> Summar y	Release source pa The released sour 6 months ago Released RSB has Building a release script: 85:	th on ftp.rtems. ce directory in th fixed no source set for RSB fails in the rtems_tools_so	admin admin r rtems-tools rtems-tools build with purce="rtems-tools-	wrong. The RSB is fetching htt Chris Johns : 5.0.0-m1911"	ps://ftp.rtems.org/pub/rtems/releases/5/5.0.0-m19	11/sources
Summar y Descripti on <b>#3812</b> Summar y	Release source pa The released sour 6 months ago Released RSB has Building a release script: 85: script: 85:	th on ftp.rtems. ce directory in th fixed no source set fc RSB fails in the rtems_tools_so source_dir_rte	admin admin r rtems-tools rtems-tools build with bource="rtems-tools- tms_tools=\$(rtems_t	wrong. The RSB is fetching htt Chris Johns : 5.0.0-m1911" cols_source}	ps://ftp.rtems.org/pub/rtems/releases/5/5.0.0-m19 Chris Johns	11/sources
Summar y Descripti on <b>#3812</b> Summar y Descripti	Release source pa The released sour 6 months ago Released RSB has Building a release script: 85: script: 86: source setup	th on ftp.rtems. ce directory in th fixed no source set for RSB fails in the rtems_tools_source_dir_rtc source_dir_rtc	admin admin r rtems-tools rtems-tools build with bource="rtems-tools- tms_tools=\$(rtems_t	wrong. The RSB is fetching htt Chris Johns : 5.0.0-m1911" ools_source} rce rtems-tools -q -n \${rtem	ps://ftp.rtems.org/pub/rtems/releases/5/5.0.0-m19 Chris Johns	11/sources
Summar y Descripti on <b>#3812</b> Summar y Descripti on	Release source pa The released sour 6 months ago Released RSB has Building a release script: 85: script: 86: source setup error: no sc	th on ftp.rtems. ce directory in th fixed no source set for RSB fails in the rtems_tools_surce_dir_rte : rtems-tools.	admin admin r rtems-tools rtems-tools build with purce="rtems-tools= ftems_tools=\${rtems_t 5.0.0-m1911-1: sou is-tools (source-rt	<pre>wrong. The RSB is fetching htt Chris Johns : 5.0.0-m1911" ools_source; rce rtems-tools -q -n \${rtem ems-tools)</pre>	ps://ftp.rtems.org/pub/rtems/releases/5/5.0.0-m19 Chris Johns	11/sources 2 months ago
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Summar y Descripti on <b>#3812</b> Summar y Descripti on <b>#3814</b> Summar y	Release source particular release source particular released source particular released source and released RSB has building a release script: 85: script: 86: source setup error: no script: 80 Releasing creates the release snaps https://ftp.rtems.	th on ftp.rtems. ce directory in the fixed no source set for RSB fails in the rtems_tools_second source_dir_rte rtems-tools- rtems-tools- rtems-tools- rtems-tools- rtems-tools- rtems-tools- rtems-tools- fixed 2 copies or the la hot m1911 as for org/pub/rtems/r	admin admin admin admin admin admin rtems-tools build with burce="rtems-tools tools=\${rtems_t 5.0.0-m1911-1: sou as-tools (source-rt admin kernel and tools. bund here: eleases/5/5.0.0/5.0.0	wrong. The RSB is fetching htt Chris Johns : 5.0.0-m1911" ools_source} rce rtems-tools -q -n \${rtem ems-tools) Chris Johns -m1911/	ps://ftp.rtems.org/pub/rtems/releases/5/5.0.0-m19 Chris Johns	11/sources 2 months ago
Descripti on <b>#3812</b> Summar y Descripti on <b>#3814</b> Summar y Descripti	Release source particular         The released source         6 months ago         Released RSB has         Building a release         script: 85:         script: 86:         source setur         error: no sc         6 months ago         Releasing creates         The release snaps         https://ftp.rtems.         has a kernel (rte	th on ftp.rtems. ce directory in th fixed no source set for RSB fails in the rtems_tools_sr source_dir_rtc source_dir_rtc fixed 2 copies or the l hot m1911 as for org/pub/rtems/r ms-5.0.0-m1913	admin admin ar rtems-tools rtems-tools build with purce="rtems-tools= tools=%{rtems_tools= tools=%{rtems_tools= admin ternel and tools. build here: eleases/5/5.0.0/5.0.0 .tar.xz) in the top of	wrong. The RSB is fetching htt Chris Johns : 5.0.0-m1911" ools_source} rce rtems-tools -q -n \${rtem ems-tools) Chris Johns em1911/ directory and another copy in the	ps://ftp.rtems.org/pub/rtems/releases/5/5.0.0-m19 Chris Johns as_tools_source} Chris Johns sources directory.	11/sources 2 months ago 5 months ago
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I	Fails to build GDB for both the Arm and Sparc architectures.	
	RTEMS Tools Project - Source Builder Error Report	
	Build: error: building sparc-rtems5-qdb-8.3-x86 64-freebsd12.0-1	
	Command Line:/source-builder/sb-set-builderprefix=/home/mayes/dev/rtems/5 5/rtems-sparc	
	Python: 3.6.9 (default, Oct 24 2019, 01:18:01) [GCC 4.2.1 Compatible FreeBSD Clang 6.0.1 (tags/RELEASE_001/final 335540)]	
	git://git.rtems.org/rtems-source-builder.git/origin/9a1cf9a2d940a4f79cd822f05c8fb13a4c0ec3bb	
	FreeBSD rtbf64b 12.0-RELEASE-p10 FreeBSD 12.0-RELEASE-p10 GENERIC amd64	
	CXXLD qdb	
	/usr/bin/ld: error: undefined symbol: libiconv_open	
	>>> referenced by charset.c	
	>>> charset.o:(convert_between_encodings(char const*, char const*, unsigned char const*, unsigned int, int, obstack*,	
	transliterations))	
	/usr/bin/ld: error: undefined symbol: libiconv	
	>>> referenced by charset.c	
	>>> charset.o:(convert_between_encodings(char const*, char const*, unsigned char const*, unsigned int, int, obstack*,	
	transliterations))	
	/usr/bin/ld: error: undefined symbol: libiconv_close	
	>>> referenced by charset.c	
	>>> charset.o:(convert_between_encodings(char const*, char const*, unsigned char const*, unsigned int, int, obstack*,	
	<pre>transliterations))</pre>	
	/usr/bin/ld: error: undefined symbol: libiconv_close	
	>>> referenced by charset.c	
	>>> charset.o:(convert_between_encodings(char const*, char const*, unsigned char const*, unsigned int, int, obstack*, transliterations))	
	/usr/bin/ld: error: undefined symbol: libiconv_open	
	>>> referenced by charset.c	
	>>> charset.o:(wchar_iterator::wchar_iterator(unsigned char const*, unsigned long, char const*, unsigned long))	
	/usr/bin/ld: error: undefined symbol: libiconv_close	
	>>> referenced by charset.c	
	>>> charset.o:(wchar_iterator::~wchar_iterator())	
	/usr/bin/ld: error: undefined symbol: libiconv_close	
	>>> referenced by charset.c	
	>>> charset.o:(validate(gdbarch*))	
	c++: error: linker command failed with exit code 1 (use -v to see invocation)	
	gmake[2]: *** [Makefile:1889: gdb] Error 1	
ripti	<pre>gmake[2]: Leaving directory '/usr/home/mayes/dev/rsb/rtems/build/sparc-rtems5-gdb-8.3-x86_64-freebsd12.0-1/build/gdb' gmake[1]: *** [Makefile:8792: all-gdb] Error 2</pre>	
n	gmake[1]: '^^ [Wakerlets/92: all-gob] stror 2 gmake[1]: Leaving directory '/usr/home/mayes/dev/rsb/rtems/build/sparc-rtems5-gdb-8.3-x86 64-freebsdl2.0-1/build'	
	gmake[]: Leaving diffectory /ust/nome/mays/dev/isb/items/build/spart-items/gdb-0.5-x00_04-ireebsdi2.0-i/build gmake: *** [Makefile:849: al] Error 2	
	gmake	
	error: building sparc-rtems5-qdb-8.3-x86 64-freebs(2)-0-1	

<del>#3821</del>	6 months ago	fixed	network/libbsd	Sebastian Huber	Sebastian Huber	4 months ago
Summar y	Port NVMe support	from FreeBSD to	o libbsd			
<del>#3822</del>	6 months ago	fixed	admin	Chris Johns	Chris Johns	5 weeks ago
Summar y	Release created VE	RSION file in rte	ms-tools*.tar.xz is wro	ong		
Docorinti	A build of the VER	SION file in the r	release rtems-tools sou	urce package is reporting:		
Descripti on	invalid vers	ion file: erro	r: Invalid version fi	le: ./VERSION: No option '	revision' in section: 'version'	
<del>#3823</del>	5 months ago	fixed	rtems	Jonathan Brandmeyer	Sebastian Huber <sebastian.huber@></sebastian.huber@>	5 months ago
Summar y	Untar_ family does	n't handle neste	d directories			
	At least, not in sor	ne circumstances	i.			
Descripti on	For example, foo/b	ar.txt untar's jus	st fine, but foo/bar/baz.tx	xt does not. If sub-directory for	o/ exists first, then foo/bar/baz.txt does unpack correctly.	
	We only use the U	ntar_*() family d	uring our initial program	ming workflow, so hacking aro	and this problem wasn't too laborious for us.	
<del>#3826</del>	5 months ago	duplicate	shell	Chris Johns		5 months ago
Summar	top on SMP shows	invalid priorities				

	TD	NAME			TOTAL   CURRENT	
Descripti on	ID   +- 0x0a010008		+	I   TIME +	+ 7388000   2.437559   0.000	0   0.022
	The code in question	is https://git.rl	tems.org/rtems/tree	cpukit/libmisc/cpuuse/cpuusa	jetop.c#n447	
	What is best API to	show a priority tha	t makes this code ro	bust?		
<del>#3830</del>	5 months ago	fixed	tool/rsb	Sebastian Huber	Sebastian Huber	5 months ago
ummar y	Build problems with	user names which	contain space chara	icters		
escripti on	The RSB uses the us	er name as path c	omponents. This doe	es not work well if the user nam	e contains space characters. Use the user ID nu	umber instead.
<b>#3831</b>	5 months ago	fixed	doc	Joel Sherrill	Chris Johns	5 months ago
Summar y	Duplicate description	of Tiers and Rules	5			
					e the real site of this information. However,	
Descripti	I think the Wiki page	e can be reviewed	and removed. But ar	describes the tiers rules with sl ny references and sub-pages sh shows two sub-pages to deal wi	ould be dealt with at the same time. https://de	vel.rtems.org/search?
on	<ul> <li>https://devel</li> </ul>	.rtems.org/wiki/De	veloper/Architecture	es/ARM which is one sentence a		
	This would eliminate	a few pages and a	a point of duplication	n for a very important concept t	o the RTEMS Project.	
<del>#3833</del>	5 months ago	fixed	rtems	Sebastian Huber	Sebastian Huber	4 months ago
Summar y	Simplify RTEMS sem	aphore configurati	on			
Descripti on	the addition of self- the workspace. With	ontained synchron the availability of zation objects. Ren	ization objects. At the self-contained synchest	his time, the potential memory nronization options, e.g. POSIX	ONFIGURE_MAXIMUM_MRSP_SEMAPHORES. The reduction was justified considering the more con mutexes, this is no longer justified. Memory cor tes configuration option. This has only an impact	mplex configuration and additional use of nstrained applications should use the self-
<del>#3834</del>	5 months ago	fixed	dev	Sebastian Huber	Sebastian Huber	4 months ago
Summar y	Simplify clock driver					
Descripti				ver instead of using a legacy IC	driver. This makes the system initialization mo	re modular and removes a bit of overhead
on #3835	introduced by the le	fixed		Sebastian Huber	Sebastian Huber	8 weeks ago
Summar	5 months ago Support statically all	1	score	Sebastian Huber	Sebastian nuber	o weeks ago
У	In addition to the th 1. stack area			ted resources for all operating areas are currently allocated by		
	In addition to the th 1. stack area 2. TLS area 3. FP context 4. thread queue Currently the FP con size and place them	read control block, heads text and the TLS a in the stack area.	up to four memory rea are separately a This makes it also p	areas are currently allocated by illocated from the workspace. T ossible to move the stack area		
Descripti	In addition to the th 1. stack area 2. TLS area 3. FP context 4. thread queue Currently the FP con size and place them configured dependin	read control block, heads text and the TLS a in the stack area. g on the unlimited e stacks for interna	up to four memory rea are separately a This makes it also p objects option of the	areas are currently allocated by illocated from the workspace. T ossible to move the stack area e configuration.	r a thread: nis complicates the workspace size estimate. Ad	k to get the thread queue heads which is
Descripti on #3836	In addition to the th 1. stack area 2. TLS area 3. FP context 4. thread queue Currently the FP consize and place them configured dependin Statically allocate th Guide configuration 5 months ago	heads text and the TLS a in the stack area. g on the unlimited e stacks for interna chapter. fixed	up to four memory rea are separately a This makes it also p objects option of the al threads (e.g. idle a config	areas are currently allocated by illocated from the workspace. T ossible to move the stack area e configuration.	a thread: nis complicates the workspace size estimate. Ad allocation out of _Thread_Initialize(). Use a hoo	k to get the thread queue heads which is
Descripti on #3836 Summar	In addition to the th 1. stack area 2. TLS area 3. FP context 4. thread queue Currently the FP con size and place them configured dependin Statically allocate th Guide configuration	heads text and the TLS a in the stack area. g on the unlimited e stacks for interna chapter. fixed	up to four memory rea are separately a This makes it also p objects option of the al threads (e.g. idle a config	areas are currently allocated by illocated from the workspace. T ossible to move the stack area e configuration. and MPCI receive server) in a d	y a thread: his complicates the workspace size estimate. Ad allocation out of _Thread_Initialize(). Use a hoo edicated linker section (similar to the interrupt s	k to get the thread queue heads which is stacks). Mention this in the Classic API
Descripti on #3836	In addition to the th 1. stack area 2. TLS area 3. FP context 4. thread queue Currently the FP con- size and place them configured dependin Statically allocate th Guide configuration 5 months ago Specify the application The application conf	heads text and the TLS a in the stack area. g on the unlimited e stacks for interna chapter. fixed on configuration op guration is current	up to four memory rea are separately a This makes it also pr objects option of the al threads (e.g. idle a config obtions	areas are currently allocated by allocated from the workspace. T ossible to move the stack area e configuration. and MPCI receive server) in a d Sebastian Huber	y a thread: his complicates the workspace size estimate. Ad allocation out of _Thread_Initialize(). Use a hoo edicated linker section (similar to the interrupt s Sebastian Huber st cases, and the implementation. Add the appli	k to get the thread queue heads which is stacks). Mention this in the Classic API 4 weeks ago
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00	* @brief Get the local *	MPCI node number.							
UII	* @return The local MPC */	I node number.							
	uint16_t rtems_object_ge	et_local_node( void );							
	to avoid the direct use of intern	al data structures.							
<del>#3842</del>	5 months ago fixed	tool/rsb	Chris Johns	Chris Johns	5 months ago				
ummar	RSB RTEMS version message st	ing is fixed to the git has	h						
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	4 months ago fixed	config	Sebastian Huber	Sebastian Huber	3 months ago				
ummar	Add CONFIGURE_DIRTY_MEMO	λΥ							
У		V DCD antian (build time			(link time configuration)				
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#3844	4 months ago fixed	config	Sebastian Huber	Sebastian Huber	4 months ago				
Summar	Remove CONFIGURE_HAS_OWN	_DEVICE_DRIVER_TABLE							
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Descripti				TEMS configuration should be done via explicit con uration option had no test case. There was an atte					
on	option:								
	https://lists.rtems.org/pipermai	/users/2019-April/03313	1.html						
	4 months ago fixed	config	Sebastian Huber	Sebastian Huber	4 months ago				
ummar y	Remove Ada-specific configuration	on options							
Y	We have currently three Ada rel	ated configuration options	5:						
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	b08278e/rtems lea	ads to the follow	ving run-time error o	n PSIM:						
				09000000 hits DBAT 0 0000000 hits DBAT 0						
	The RAM overlaps									
	/*									
	* Setup BATs and enable MMU									
	*/									
	/* Memory */ setdbat(0, 0x0<<28, 0x0<<28, 1<<28, PAGE RW);									
	setibat(0, 0x0<<28, 0x0<<28, 1<<28, 0);									
	/* PCI setdbat(1.		<<24, 1<<24, IO H	PAGE) -						
			<<24, 1<<24, IO_I							
	Increasing the RAN	M size to 256MiE	3 (0x10000000) on P	SIM breaks also the shared men	nory support:					
	typedef stru	ct {								
	/* 0x0c000	$000 - 0 \times 0 \times 0 \times 0 = 0$	FFF - AMD 29F040 * 512 * 1024 ];	¢/						
			FFF - NVRAM/NVRAM 512 * 1024 ];	*/						
scripti on	/* 0x0c100 psim_rtc_t		007 - NVRAM/RTC */	, ,						
	/* 0x0c100 uint8_t ga		00F - NVRAM/RTC */	, ,						
		010 - 0x0c100 sem_t Semapho:	01b - System V IPC re;	C Semaphore */						
	/* 0x0c100 uint8_t ga		01f - NVRAM/RTC */	,						
		020 - 0x0c100) int8 t Ethtap	05F - Ethernet */ [ 64 ];							
	/* 0x0c100 uint8 t ga		FFF - NVRAM/RTC */	<i>,</i>						
	_			Channel Manager + /						
		aredMemory[ 12		C Shared Memory */						
				Contra llon +/						
			000 - OpenPIC IRQ C[ 256 * 1024 ];	Controller */						
	l neim regie	tore t.								
	) psim_registers_t; Proposed solution is to adjust the memory map so that 256MiB of RAM are supported. Probably needs changes in rtems-tools.									
<del>3856</del>	Proposed solution 3 months ago	fixed	posix	Joel Sherrill	Probably needs changes in rtems-tools. Joel Sherrill	3 months ago				
immar	posix_devctl - Add		· ·			o montalo ago				
У										
	The FACE Technica	al Standard, Edit	tion 3.0 and later rec	uire the definition of the subcon	mand SOCKCLOSE in <devctl.h>.</devctl.h>					
scripti on	Reference: https:/	/www.opengrou	ip.org/face							
	The SOCKCLOSE c	onstant has pre	viously been added t	o <devctl.h> in newlib.</devctl.h>						
<del>3857</del>	3 months ago	fixed	posix	Joel Sherrill	Joel Sherrill	3 months ago				
ımmar	Use EAGAIN for PC	OSIX mq wait in	ISR error							
У	POSIX mossage di	iouos which are	about to block in an	ISP currently return ENOMEM	bis is a status pat listed by POSIX. The batter	or status is EACAIN por				
scripti on				unctions/mq_receive.html.	his is a status not listed by POSIX. The bette	a status is LAGATIN PEI				
3859	3 months ago	fixed	shell	Chris Johns	Chris Johns	2 weeks ago				

Running a joel script in a telnet session results in the output being sent to the global stdout. For example:

	<pre>\$ telnet 1.2.: Trying 1.2.3.: Connected to 1 Escape charact RTEMS Shell or [/] # cat j #! joel ls -las / [/] # ./j [/] #</pre>	4 1.2.3.4. ter is '^]'.	Use 'help' to list	commands.		
	1			global stdout, stdin etc and H	nas no information about the parent's std	i handles. A joel script runs in it's own work
				shell env variable POSIX key h	andling and the use of the external call 🔽	tems shell main loop
	The telnet example					cems_sherr_harr_roop.
	static void telnet shell(d	char *name, v	oid *arg)			
	{ rtems_shell_e					
	memset(&env,		v));			
Descripti on	env.devname = env.taskname env.login_che env.forever =	= "TLNT"; eck = NULL;				
	rtems_shell_r }	main_loop(&en	v);			
	This is problematic a code.	as control of the	e env has been lost a	and this make backwards compta	table changes difficult. Control of this str	needs to be brought back under the shell
				en the run entry point is used. Th d in each run call and again in the		d to the shell's main loop task as an argument
			· · · · · · · · · · · · · · · · · · ·	oel script in a shell will set the k he shell code has to be handled es		called when the task is deleted. We have to assume
	Setting the key in the free 's the key's m			ause telnet code such as the exar	nple in libbsd uses a local stack shell_	env and the key has a destructor that blindly
	Changes:					
	<ol> <li>Remove all k</li> <li>Change rter</li> <li>Update rter</li> <li>Have the ma</li> <li>Check the ma</li> </ol>	ey sets and have not shell_init not shell_dup_d in loop use the agic field has	ve only one in the she env to get the curre current_env to set t parent std handles	ell's main loop code. ent tasks key and clone that befor he parent std handles. rather than the global handles.	e cloning the global env.	ree to only free the shell_env if managed.
<del>#3861</del>	3 months ago	fixed	config	Sebastian Huber	Sebastian Huber	3 months ago
Summar y	Add CONFIGURE_VE	ERBOSE_SYSTE	M_INITIALIZATION			
Descripti on	Add a configuration	option to print	some information dur	ring system initialization.		
<del>#3862</del>	3 months ago	fixed	config	Sebastian Huber	Sebastian Huber	3 months ago
Summar y	The CONFIGURE_ZE	RO_WORKSPA	he only configuration	configuration option is documented	ed to be a boolean feature macro (is definent of the state of the stat	ed or undefined). However, in confdefs.h it uses ke the other options. This affects existing
Descripti on	application configuration application configuration application configuration application		e: kspace_automatical	LY FALSE		
	An unintentional zer	o of the works	pace has an effect on	the system boot time. This is an	acceptable trade-off for the more canonica	al configuration.
<del>#3863</del>	3 months ago	fixed		Sebastian Huber	Sebastian Huber	3 months ago
Summar y	Kennove support for	the DSF_ZERO	_WORKSFACE_AUTO	MATICALLY BSP option		
Descripti on				VORKSPACE_AUTOMATICALLY via in influence this high level system		CALLY BSP option. There is no BSP which defines
<del>#3864</del>	3 months ago	fixed	tool	Joel Sherrill	Chris Johns	4 weeks ago
Summar y	rtems-tester does n	ot work with go	db simulators			
y Descripti		ething has brok	ken with the rtems-te	ster on gdb simulators. On the lat	est run, I had to kill the tester by hand w	hen it hung. On other times, it appears to abort
on #3865	with a lock issue. 3 months ago	fixed	score	Sebastian Huber	Sebastian Huber	3 months ago
Summar			small data area targe	i	Sebastian nuber	5 months ago
У	Some targets (e.g. '	32-hit PowerPC	) have a small-data a	rea. Linker set items are not in th	ne small data area. We have to tell this the	e compiler, otherwise linker error may occur due
			e are two options to d		ie sinun data area, we nave to ten tills tilt	e complicity outcombe initial error may occur utle
Descripti on		are items as an uld have to cha		size and define items as an array	with one element. The problem with this i	s that it breaks existing code, e.g. an item
					dedicated declaration macro for the ordere	ed items.
		ons are rarely i	used we calect option			
	Since item declaration					
#3868 Summar y	Since item declarations of the second	fixed	tool/rsb	Chris Johns	Chris Johns <chrisj@></chrisj@>	3 months ago

	1 Release No		links and Winds	and not have explicite and it is set	ulated as conv. This complicates a hodre even	tion of course			
	The following patch in newlib adds links and Windows does not have symlinks and it is emulated as copy. This complicates a bsdtar extraction of source								
	https://github.com/RTEMS/sourceware-mirror-newlib-cygwin/commit/cfc4955234828881145a20987c8a0a3cd373585c								
Descripti on	I tried the -E option that was added to the RSB to extract the tar file a second time, this has worked with other tar files with symlinks however it does not work. I have no investigated why.								
	We need Windows building to make a release.								
	Note, this is in th	tools/rsb o	component however	r is not an RSB bug. I need inp	It on what the RSB needs to do to fix this is	sue if that is the path we take.			
<del>#3871</del>	3 months ago	fixed	config	Sebastian Huber	Sebastian Huber	2 months ago			
Summar y	Remove rtems_co	nfiguration_get_	posix_api_configurati	ion()					
Descripti on	The corresponding	data structure i	not longer exists. This	s function was not tested.					
<del>#3873</del>	2 months ago	fixed	config	Sebastian Huber	Sebastian Huber	2 months ago			
Summar	Z months ago     inxed     comig     Sebastian Huber     Z months ago       Remove CONFIGURE_HAS_OWN_INIT_TASK_TABLE								
У									
У	The CONFIGURE_F probably unused, s		TASK_TABLE and CO	NFIGURE_POSIX_HAS_OWN_INIT	_THREAD_TABLE are the last *_HAS_OWN_* con	nfiguration options. These two options are			
	probably unused,	see also:				nfiguration options. These two options are			
Descripti	probably unused, s	see also: .org/pipermail/u	sers/2019-April/0331	29.html https://lists.rtems.org/pi	bermail/users/2019-April/033130.html	· · ·			
	probably unused, s https://lists.rtems Removing them sin	see also: .org/pipermail/u mplifies the conf	sers/2019-April/0331	29.html https://lists.rtems.org/pi a real user need which shows up a	permail/users/2019-April/033130.html fter the removal, we can resurrect them on dema	and.			
Descripti	probably unused, s https://lists.rtems Removing them sin	see also: .org/pipermail/u mplifies the conf _HAS_OWN_INI	sers/2019-April/0331	29.html https://lists.rtems.org/pi a real user need which shows up a	bermail/users/2019-April/033130.html	and.			
Descripti	probably unused, s https://lists.rtems Removing them sin Using CONFIGURE	see also: .org/pipermail/u mplifies the conf _HAS_OWN_INI	sers/2019-April/0331	29.html https://lists.rtems.org/pi a real user need which shows up a	permail/users/2019-April/033130.html fter the removal, we can resurrect them on dema	and.			
Descripti on	probably unused, s https://lists.rtems Removing them sin Using CONFIGURE configuration optic 2 months ago	see also: .org/pipermail/u mplifies the conf _HAS_OWN_INI ns. 	sers/2019-April/0331 iguration. If there is a T_TASK_TABLE would	29.html https://lists.rtems.org/pi a real user need which shows up a d have required the use of the und Sebastian Huber	permail/users/2019-April/033130.html fter the removal, we can resurrect them on dema ocumented CONFIGURE_INIT_TASK_TABLE and	and. CONFIGURE_INIT_TASK_TABLE_SIZE			
Descripti on <b>#3874</b> Summar	probably unused, s https://lists.rtems Removing them sin Using CONFIGURE configuration optic 2 months ago Remove CONFIGU	see also: org/pipermail/u mplifies the conf HAS_OWN_INI fixed RE_POSIX_HAS_ HAS_OWN_INIT_	sers/2019-April/0331 iguration. If there is a T_TASK_TABLE would config 	29.html https://lists.rtems.org/pi a real user need which shows up a d have required the use of the und Sebastian Huber TABLE	permail/users/2019-April/033130.html fter the removal, we can resurrect them on dema ocumented CONFIGURE_INIT_TASK_TABLE and	and. CONFIGURE_INIT_TASK_TABLE_SIZE 2 months ago			
Descripti on <b>#3874</b> Summar	probably unused, s https://lists.rtems Removing them sii Using CONFIGURE configuration optic 2 months ago Remove CONFIGU The CONFIGURE_F probably unused, s	see also: org/pipermail/u mplifies the conf _HAS_OWN_INI ms. fixed RE_POSIX_HAS_ HAS_OWN_INIT_ see also:	sers/2019-April/0331 iguration. If there is a T_TASK_TABLE would config _OWN_INIT_THREAD_ _TASK_TABLE and CO	29.html https://lists.rtems.org/pi a real user need which shows up a d have required the use of the und Sebastian Huber _TABLE NFIGURE_POSIX_HAS_OWN_INIT	bermail/users/2019-April/033130.html fter the removal, we can resurrect them on dema ocumented CONFIGURE_INIT_TASK_TABLE and Sebastian Huber	and. CONFIGURE_INIT_TASK_TABLE_SIZE 2 months ago			
Descripti on <b>#3874</b> Summar y	probably unused, s https://lists.rtems Removing them sin Using CONFIGURE configuration optic 2 months ago Remove CONFIGU The CONFIGURE_F probably unused, s https://lists.rtems	see also: org/pipermail/u mplifies the conf 	sers/2019-April/0331 iguration. If there is a T_TASK_TABLE would config _OWN_INIT_THREAD_ _TASK_TABLE and CO sers/2019-April/0331	29.html https://lists.rtems.org/pi a real user need which shows up a d have required the use of the und Sebastian Huber _TABLE NFIGURE_POSIX_HAS_OWN_INIT 29.html https://lists.rtems.org/pi	permail/users/2019-April/033130.html  fter the removal, we can resurrect them on dema cocumented CONFIGURE_INIT_TASK_TABLE and Sebastian Huber _THREAD_TABLE are the last *_HAS_OWN_* con	and. CONFIGURE_INIT_TASK_TABLE_SIZE 2 months ago nfiguration options. These two options are			
Descripti on <b>#3874</b> Summar y Descripti	probably unused, s https://lists.rtems Removing them sin Using CONFIGURE configuration optic 2 months ago Remove CONFIGU The CONFIGURE_F probably unused, s https://lists.rtems Removing them sin Using CONFIGURE	see also: org/pipermail/u mplifies the conf _HAS_OWN_INI ms. fixed RE_POSIX_HAS_ AS_OWN_INIT_ see also: .org/pipermail/u mplifies the conf _POSIX_HAS_O'	sers/2019-April/0331 iguration. If there is a T_TASK_TABLE would config _OWN_INIT_THREAD_ _TASK_TABLE and CO sers/2019-April/0331 iguration. If there is a	29.html https://lists.rtems.org/pi a real user need which shows up a d have required the use of the und Sebastian Huber TABLE INFIGURE_POSIX_HAS_OWN_INIT 29.html https://lists.rtems.org/pi a real user need which shows up a ABLE would have required the use	permail/users/2019-April/033130.html fter the removal, we can resurrect them on demi ocumented CONFIGURE_INIT_TASK_TABLE and Sebastian Huber _THREAD_TABLE are the last *_HAS_OWN_* con permail/users/2019-April/033130.html	and. CONFIGURE_INIT_TASK_TABLE_SIZE 2 months ago nfiguration options. These two options are and.			
Descripti on <b>#3874</b> Summar y Descripti	probably unused, s https://lists.rtems Removing them sin Using CONFIGURE configuration optic 2 months ago Remove CONFIGU The CONFIGURE_F probably unused, s https://lists.rtems Removing them sin Using CONFIGURE	see also: org/pipermail/u mplifies the conf _HAS_OWN_INI ms. fixed RE_POSIX_HAS_ AS_OWN_INIT_ see also: .org/pipermail/u mplifies the conf _POSIX_HAS_O'	sers/2019-April/0331 iguration. If there is a _TASK_TABLE would 	29.html https://lists.rtems.org/pi a real user need which shows up a d have required the use of the und Sebastian Huber TABLE INFIGURE_POSIX_HAS_OWN_INIT 29.html https://lists.rtems.org/pi a real user need which shows up a ABLE would have required the use	permail/users/2019-April/033130.html fter the removal, we can resurrect them on demi ocumented CONFIGURE_INIT_TASK_TABLE and Sebastian Huber _THREAD_TABLE are the last *_HAS_OWN_* con permail/users/2019-April/033130.html fter the removal, we can resurrect them on demi	and. CONFIGURE_INIT_TASK_TABLE_SIZE 2 months ago nfiguration options. These two options are and.			

Descripti on

The confdefs.h header file is large, complex, and hard to review. Split it up into component header files to make it easier to maintain and review.

The general approach is to place the default configuration of things in librtemscpu.a. The benefit is that the application configuration object file will only include data structures which have a user-defined value.

The component based header files include their dependencies explicitly. It should be possible to include component based header files separately to ease testing. For example we could use this template:

<pre>#ifndef _RTEMS_CONFDEFS_FOOBAR_H #define _RTEMS_CONFDEFS_FOOBAR_H</pre>	
<pre>#ifndefCONFIGURATION_TEMPLATE_h #error "Do not include this file directly, use <rtems #endif<="" confdefs.h="" pre=""></rtems></pre>	↓> instead"
<pre>#if defined(CONFIGURE_INIT) &amp;&amp; \     defined(CONFIGURE_FOOBAR_STUFF) &amp;&amp; \     defined(CONFIGURE_MORE_FOOBAR_STUFF)</pre>	
/* Foobar includes */	
<pre>#ifdefcplusplus extern "C" { #endif /*cplusplus */</pre>	
/* Configure foobar. */	
#ifdefcplusplus	
<pre>#endif /*cplusplus */</pre>	
<pre>#endif /* CONFIGURE_INIT */</pre>	
<pre>#endif /* _RTEMS_CONFDEFS_FOOBAR_H */</pre>	

In case CONFIGURE_INIT is not defined, then including <rtems/confdefs.h> should expose nothing to the C compiler.

Here is a first proposal to group the configuration in components:

rtems/	
confdefs.h	
This file just	includes the component based header files listed below.
confdefs/	-
bdbuf.h	
classicobj.h	
Classic API	objects
classictasksin	
	ialization task
driverclock.h	
	and related configuration, e.g. CONFIGURE MICROSECONDS PER TICK
driverconsoles	
	le driver configuration
driverlegacy.h	
	iver configuration table
extensions.h	
	ons, internal extensions
filesystem.h	
Filesystem c	onfiguration
general.h	
Basic stuff	which is mandatory to configure, e.g. ISR stacks, per-CPU information
libpci.h	
PCI library	configuration
malloc.h	
Malloc confi	guration
mpci.h	
MPCI specifi	c configuration options
msgq.h	
General mess	age queue configuration
obsolete.h	
Warning abou	t the use of obsolete configure options
posixkeys.h	
POSIX keys	
posixobj.h	
POSIX object	9
posixthreadsin	
	lization threads
scheduler.h	iizacton chieads
	- fi sunsti as
Scheduler co	niiguration
support.h	
	os for confdefs header files
threads.h	
	ad configuration (e.g. thread control block)
unlimited.h	
	jects configuration

<del>#3876</del>	2 months ago	fixed	config	Sebastian Huber	Sebastian Huber	2 months ago
Summar	Remove CONFIGURE	_DISABLE_SMP_C	ONFIGURATION			
У						
Descripti					is_smp_enabled() were added during the SMP support develop	
on	workaround to fix so	me testsuite failure	es in SMP configuration	<ol> <li>Replace this configuration option</li> </ol>	with tests for specific conditions. The configuration option was	undocumented.
<del>#3881</del>	2 months ago	fixed	rtems	Sebastian Huber	Sebastian Huber	8 weeks ago
Summar	Add API functions to	map a task priorit	y to/from a POSIX thre	ad priority		
У						

```
126/130
```

Mapping task priorities to/from POSIX thread priorities is probably done in many applications. There seems to be no API to help doing this. Add the following API functions to map a task priority to/from a POSIX thread priority: * @brief Map a task priority to the corresonding POSIX thread priority. @param scheduler_id Identifier of the scheduler instance. @param priority The task priority to map. @param[out] posix_priority Pointer to a POSIX thread priority value. * @retval RTEMS_SUCCESSFUL Successful operation. * @retval RTEMS_INVALID_ADDRESS The @a posix_priority parameter is @c NULL. * @retval RTEMS_INVALID_ID Invalid scheduler instance identifier. rtems_status_code rtems_scheduler_map_to_posix_priority( * @brief Map a POSIX thread priority to the corresonding task priority. * @param scheduler_id Identifier of the scheduler instance.
 * @param posix_priority The POSIX thread priority to map.
 * @param[out] priority Pointer to a task priority value. * @retval RTEMS_SUCCESSFUL Successful operation.
 * @retval RTEMS_INVALID_ADDRESS The @a priority parameter is @c NULL.
 * @retval RTEMS_INVALID_ID Invalid scheduler instance identifier.
 * @retval RTEMS_INVALID_RIORITY Invalid POSIX thread priority. Descripti on rtems_status_code rtems_scheduler_map_from_posix_priority( scheduler_id, posix_priority, rtems_id int rtems_task_priority *priority #3882 2 months ago fixed posix Sebastian Huber Sebastian Huber 2 months ago Add POSIX user environment pointer to TCB Summar The IO library uses a POSIX key to store an optional POSIX user environment pointer. This pulls in the POSIX keys support in every application configuration. Add a user Descripti environment pointer to the thread control block (TCB) instead. Applications which do not need the POSIX user environment will just get an overhead of one pointer per thread. on #3885 2 months ago fixed score Sebastian Huber Sebastian Huber 8 weeks ago Context switch extension is broken in SMP configurations Summar The context switch extensions are called during _Thread_Do_dispatch(): void _Thread_Do_dispatch( Per_CPU_Control *cpu_self, ISR_Level level ) Thread Control *executing; executing = cpu_self->executing; do { Thread_Control *heir; heir = Thread Get heir and make it executing( cpu self ); _User_extensions_Thread_switch( executing, heir ); _Context_Switch( &executing->Registers, &heir->Registers ); Descripti } while ( cpu_self->dispatch_necessary ); on In uniprocessor configurations, the context switch extensions are called for all thread switches except the very first thread switch to the initialization thread. However, in SMP configurations, the context switch may be invalidated and updated in the low-level _Context_Switch() routine. See: https://docs.rtems.org/branches/master/c-user/symmetric_multiprocessing_services.html#thread-dispatch-details In case such an update happens, a thread executes on the processor which was not visible to the context switch extensions. This can confuse for example event record consumers which use events generated by a context switch extension. Fixing this is not straight forward. The context switch extensions call must move after the low-level context switch. The problem here is that we may end up in _Thread_Handler().

Adding the context switch extensions call to _Thread_Handler() covers now also the thread switch to the initialization thread. We also have to save the last executing thread of the processor. Registers or the stack cannot be used for this purpose. We have to add it to the per-processor information. Existing extensions may be affected, since now context switch extensions use the stack of the heir thread.

Calling the thread switch extensions in the low level context switch is difficult since at this point an intermediate stack is used which is only large enough to enable servicing of interrupts.

	https://lists.rtems	.org/pipermail/d	evel/2020-February/0577	765.html						
escripti	https://lists.rtems.org/pipermail/devel/2020-February/057765.html Ifmail is used report the remote repos as:									
on	Remotes:									
	Remotes: [ removed, contact me@there.here for details ]									
	and keep the remo									
#3888	2 months ago Update rtems_waf	fixed	network/libbsd	Chris Johns	Chris Johns	3 weeks ago				
Summar y	opulie riens_wii	11110030								
Descripti on	Update master and	d the 5-freebsd-	12 branch.							
#3893	2 months ago	fixed	tool/rsb	Chris Johns	Chris Johns	8 weeks ago				
Summar	-		building a 3rd party pack	and the second						
У	The staging chang	oc lot a fully star	ned vertical stack to build	however building the package	es for an installed tool chain and BSP is broken.					
Descripti						her-heeslebershlash 5/store				
on	packages	IIder/sb-set-t	uiideriog-bbb-pkg.	txtprefix-/build/rtems	/install/5host=arm-rtems5with-rtems	-bsp-beagreboneblack Syltems-				
<del>#3894</del>	8 weeks ago	fixed	fs	Sebastian Huber	Sebastian Huber	4 weeks ago				
Summar	Replace the device	filesystem with	a specialization of the IM	IFS						
У	New device drivers	s (e.g. Termios.	I2C, SPI, libbsd) use IMF	S generic nodes to hook into tl	ne filesystem. This does not work with the device	filesystem enabled by the				
Descripti			'S AS BASE FILESYSTEM			,				
on	l.			pocialization of the IMEC						
<del>#3895</del>	8 weeks ago	in. Replace the d	levice filesystem with a s doc	Sebastian Huber	Sebastian Huber	2 weeks ago				
Summar	Add a migration to									
y Descripti	Add a chapter to t	he User Manual I	to aid the user to migrate	applications from previous RT	EMS versions to RTEMS 5					
on	, au a chapter to t		to the user to migrate	applications from previous KI						
<del>#3896</del>	8 weeks ago	fixed	tool/rsb	Chris Johns	Chris Johns	8 weeks ago				
Summar y	RSB optionsource	ce-only-download	d does not work with rele	ases						
Descripti	The RSB option -	-source-only-d	ownload does not work f	or releases						
on #3898	8 weeks ago	fixed	config	Sebastian Huber	Sebastian Huber	7 weeks ago				
Summar	Remove CONFIGU	RE_MAXIMUM_D								
y Descripti	This configuration	ontion was only	used by the device files	stem which will be replaced by	a special configuration of the IMFS.					
on	This configuration	option nuo oniy								
<del>#3900</del>	8 weeks ago	fixed	doc	Sebastian Huber	Sebastian Huber	4 weeks ago				
Summar y	New template for	oolean feature (	defines							
1	All boolean feature	e defines are und	lefined by default. The cu	rrent template is a bit awkwar	d. Change the application configuration option te	mplate for boolean feature defines to:				
	index:: C	ONFIGURE_XYZ								
	CONFIGUR	E_XYZ:								
	CONFIGURE XY	Z								
	CONFIGURE_X12									
	CONSTANT:	URE XYZ``								
Descripti on	OPTION TYPE:	_								
			ion is a boolean feat	ure define.						
	DEFAULT CONF If this		option is undefined,	then not ABC or something	else.					
	DESCRIPTION: In case		tion option is define	d, then ABC.						
	NOTES: Notes fo	r XYZ.								
	1									
<del>#3901</del>	8 weeks ago	fixed	doc	Sebastian Huber	Sebastian Huber	4 weeks ago				

	Change the docume									
	CONFIGURE_	XYZ:								
	CONFIGURE_XYZ									
	CONSTANT: `CONFIGUE	RE XYZ``								
	OPTION TYPE: This configuration option is an integer define.									
	This configuration option is an integer define.									
	The specified value must be greater than or equal to X and less than or equal to Y.									
	DEFAULT VALUE: The defaul	: lt value is Z.								
	DESCRIPTION: This confi	iguration optio	n defines the ABC.							
Descripti	NOTES: Notes for	XYZ.								
on	Use OPTION TYPE i	nstead of DATA 1	TYPE since we have t	to characterize the option an not	just the value of an option. This is in line with <del>#3900</del> .					
	7 weeks ago raspberrypi2 libbsd The build is failing w		network/libbsd s not build	Chris Johns	Christian Mauderer <christian.mauderer@></christian.mauderer@>	3 weeks ago				
<b>#3903</b> Gummar y Descripti on	raspberrypi2 libbsd The build is failing w /opt/work/rter /opt/work/chri 2b9172c9442d03 to `bsp_fdt_mi	5-freebsd-12 doe: /ith ns/5/lib/gcc/ar is/rtems/rsb/rt 5656fb16d667091 ap_intr'	s not build m-rtems5/7.5.0/ ems-source-builder e2ee3ac64009/build	///arm-rtems5/bin/ld: r.git/rtems/build/rtems-libb;	Christian Mauderer <christian.mauderer@> ./libbsd.a(rtems-kernel-nexus.c.18.0): in function sd-v2b9172c9d42d056b6fb16d667091e2ee3ac64009-arm-rte Fault///rtemsbsd/rtems/rtems-kernel-nexus.c:359:</christian.mauderer@>	`nexus_ofw_map_intr': ms5-1/rtems-libbsd-				
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	There are config file	es with the follow	wing:			
	#					
	#		ons. We use 2.x.x R	elease l.		
	%ifn %{pkgcon %include %{	fig check gli configdir}/gl				
Descripti on	If the glib package	is present the c	onfig file is not loaded	and the source is not download	ed with the sb-get-source package. Chang	ge the config to:
	#					
	#		ons. We use 2.x.x R			
		fig check gli configdir}/gl		<pre>l _rsb_getting_source}</pre>		
<del>#3921</del>	4 weeks ago	fixed	bsps	Sebastian Huber	Sebastian Huber	4 weeks ago
Summar	3		ne hardware clock tick		Sebasian naber	+ WCCK3 dg0
y escripti	The BCR initialization	on in qoriq_clock	k_initialize() is off by o	ne resulting in a wrong clock int	erval.	
on	4 wooks ago	fixed	tool/rsb	Joel Sherrill	Chris Johns	4 wooks ago
#3927 Summar	4 weeks ago tclsh required to bu		kes all BSP bsets fail	Joer Shernin		4 weeks ago
У				equirement, then host setup ins should not be in the BSP bsets.	tructions, sb-check, etc are impacted. But di	tc is a required component for a few things an
Descripti	This is IMO a blocke	er at some level	for the 5.1 release be	cause it impacts building all bse		
on	3080801/tool/mksq mkkeywordhash -D 1/build-xc//sqlite-	lite3h.tcl /home SQLITE_OMIT_\ src-3080801/to	e/joel/rtems-cron-5/rte WAL=1 -DSQLITE_ENA	ems-source-builder/rtems/build/ \BLE_COLUMN_METADATA=1 /I Jcc -g -o lemon /home/joel/rtem	ems/build/sqlite-3.8.8.1-powerpc-rtems5-1/ sqlite-3.8.8.1-powerpc-rtems5-1/build-xc// nome/joel/rtems-cron-5/rtems-source-builde ns-cron-5/rtems-source-builder/rtems/build/	/sqlite-src-3080801 >sqlite3.h gcc  -g -o r/rtems/build/sqlite-3.8.8.1-powerpc-rtems5-
<del>#3936</del>	3	fixed	arch/sparc64	Sebastian Huber	Gedare Bloom	3 weeks ago
Summar y	C++ thread-local st	torage broken o	n sparc64			
Descripti on	5/bsps/sparc6 sptls02/sptls /build/rtems/ /build/rtems/ /build/rtems/	4/shared/star 02-var.o ./ 5/lib/gcc/spa 5/lib/gcc/spa 5/lib/gcc/spa	<pre>t -Wl,wrap=printf //lib/libbsp/spar rc64-rtems5/7.5.0/. rc64-rtems5/7.5.0/.</pre>	-Wl,wrap=puts -Wl,wrap c64/niagara/librtemsbp.a //./sparc64-rtems5/t ///sparc64-rtems5/t ///sparc64-rtems5/t	<pre>specs -qrtems -L.//.cpukit -L/hom =putchar -Wl,gc-sections -o sptls02 /.//cpukit/librtemscpu.a.//.c in/ld: warning: dot moved backwards b in/ld: warning: dot moved backwards b in/ld: section .got LMA [00000000003</pre>	.exe sptls02/sptls02-init.o pukit/librtemstest.a efore `.got' efore `.got'
	One option is to dis				1.10	
Summar	3 weeks ago Many (~40) BSPs F	fixed ail to Link all Te	ests	Joel Sherrill	joel@	3 weeks ago
У						
	/rtems/conf	iguretarge deenable-t	t=arm-rtems5pref		·	enable-cxxdisable-rdbgenable- sable-adadisable-expadaenable-
Descripti on	arm-atsamv arm-In Ipc32xx_mzx_stage m68k-mcf52235 mi mpc5668g powerpc	n3s3749 arm-Im e_1 arm-rtl22xx 68k-mcf5225x r mpc5674f_ecu	arm-rtl22xx_t arm-str m68k-mrm332 powerp	m32f105rc arm-stm32f4 arm-tm c-gwlcfm powerpc-mpc5566evb c5674f_ecu508_boot powerpc-m	s570ls3137_hdk_intram arm-tms570ls3137 powerpc-mpc5566evb_spe powerpc-mpc556	
<del>#3944</del>	3 weeks ago	fixed	bsps	Joel Sherrill	Sebastian Huber	20 hours ago
Summar	qoriq_e500 BSP bse	et fails				
У	Looks like curl isn't	building for the	qoriq_e500 bset. This	seems like a weird outlier. Any	ideas?	
)escripti on	checking for checking for checking for checking for configure: er shell cmd fai	gethostbyname gethostbyname gethostbyname ror: couldn't led: /bin/sh	<pre>for Minix 3 no for eCos no for AmigaOS bsdsoc in -lnetwork no in -lnet no find libraries for -ex /home/joel/rte .l-powerpc-rtems5-1</pre>	· gethostbyname() ms-work/rtems-source-builde	r/rtems/build/curl-v7.65.1-powerpc-rt	ems5-1/do-build
<del>#3945</del>	3 weeks ago	fixed	doc	Cláudio Maia	Chris Johns	22 hours ago
Summar			s/user/rsb/configuration			
У	The DTC example o	n rtems-docs/u	ser/rsb/configuration r	st should be reviewed and unda	ted in order to be consistent with what is ava	ailable in the RSB tree
escripti on	For instance, the lin	nk (http://www.	jdl.com/software) prov	vided in the webpage is not avail	able anymore and some environment variab	les described in the text need also to be verifi
<del>#3949</del>	("DESTDIR" and a " 2 weeks ago	DISTDIR"). Thu	posix	w this part of the documentatio Sebastian Huber	n. Sebastian Huber	2 weeks ago
ummar	clock_settime() can		. ·			
y escripti	A time too far in the	e future can lea	d to a failed assertion i	in _Watchdog_Ticks_from_times	pec(). This should be an error status instead	1.
on	12 dave	fixed	rtono-	Cohootion Unit	Cohostian Uniter	44.1
<b>¥3953</b> Summar	13 days ago rtems_extensions_o	fixed create() accepts	rtems a NULL pointer table	Sebastian Huber	Sebastian Huber	11 days ago
y Descripti	It should return RTI	EMS_INVALID_A	ADDRESS instead.			
on #3956	12 days ago	fixed	tool/rsb	Chris Johns	Chris Johns	23 hours ago
Summar	RSB BSP build with		· ·	Chilo Johno	cinio solilio	25 Hours ago
y Descripti						
	The tests are not in	stalled by defau	It. Add support to the	RSB to copy the tests to the ins	talled BSP prefix.	
on	The tests are not in 11 days ago	stalled by defau	ult. Add support to the	RSB to copy the tests to the ins	talled BSP prefix. Chris Johns <chrisj@></chrisj@>	25 hours ago

## https://devel.rtems.org/wiki/Release//5/5.1

Summar y	Add to FreeBSD host setup information Installing GCC on FreeBSD 12 leads to build issues. This along with the issue that the GCC build should be done with the GNU sed in the \$PATH during the RSB process needs to be added to the documentation here: https://docs.rtems.org/branches/master/user/hosts/posix.html#freebsd						
Descripti on							
<del>#3961</del>	9 days ago	fixed	arch/arm	Christian Mauderer	Christian Mauderer	9 days ago	
Summar	bsps/arm: CPU counter based on arm generic timer doesn't work correctly						
У		On at least the imx BSP the CPU counter based on the arm generic timer isn't initialized correctly. The frequency is set to 0.					

t a g s 5 5.1 release